

W541C250 Data Sheet



4-BIT MICROCONTROLLER

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1. GENERAL DESCRIPTION

The W541C250 is fully compatible with W741C250 in the terms of pin assignment and IC function. It is a high-performance 4-bit microcontroller (μ C) that provides an LCD driver. The device contains a 4-bit ALU, two 8-bit timers, a divider, a 24×4 LCD driver, and five 4-bit I/O ports (including 1 output port for LED driving). There are also five interrupt sources and 8-level subroutine nesting for interrupt applications. The W541C250 operates on low voltage and very low current and has two power reduction modes, hold mode and stop mode, which help to minimize power dissipation.

The W541C250 is suitable for remote controllers, watches and clocks, multiple I/O products, keyboard controllers, speech synthesis LSI controllers, and other products.

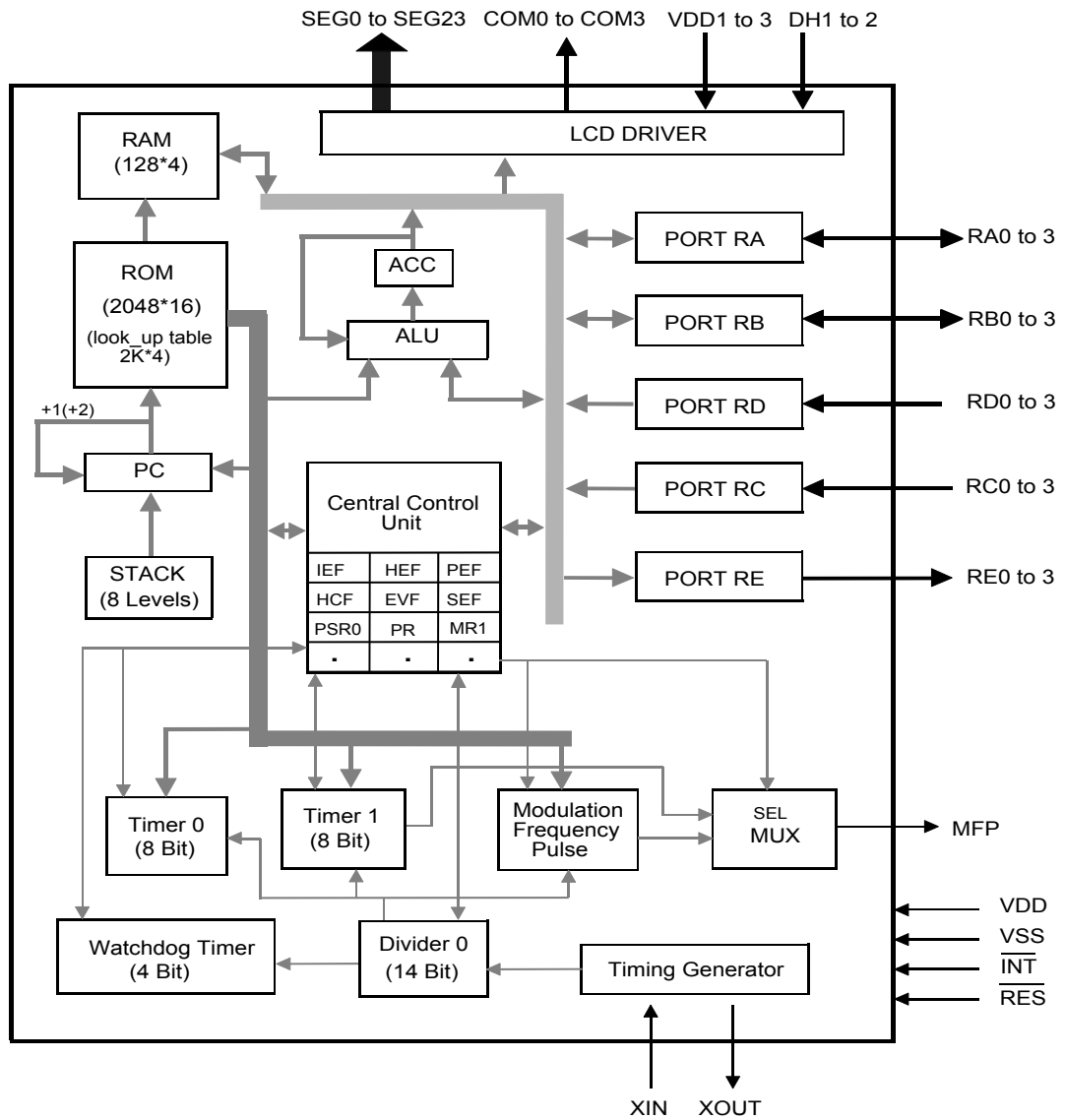
2. FEATURES

- Operating voltage: 2.4 ~ 5.5V (LCD drive voltage: 3.0V, or 4.5V)
- Single system clock
 - Oscillation frequency up to 4 MHz, Crystal or RC oscillator is selected by mask code option,
 - High-frequency (400 KHz to 4 MHz) or low-frequency (32.768 KHz) oscillation is selected by mask code option
- Memory
 - 2048×16 bit program ROM (including $2K \times 4$ bit look-up table)
 - 128×4 bit data RAM (including 16 working registers)
 - 24×4 LCD data RAM
- 21 input/output pins
 - Ports for input only: 2 ports/8 pins
 - Input/output ports: 2 ports/8 pins
 - Port for output only: 1 port /4 pins (high sink current to drive LEDs)
 - MFP output pin: 1 pin (MFP)
 - Do not be floating when it is as input or output open-drain (NMOS type).
- Power-down mode
 - Hold function: no operation (except for oscillator)
 - Stop function: no operation (including main clock)
- Five types of interrupts
 - Three internal interrupts (Divider 0, Timer 0, Timer 1)
 - Two external interrupt (Port RC and $\overline{\text{INT}}$ pin)

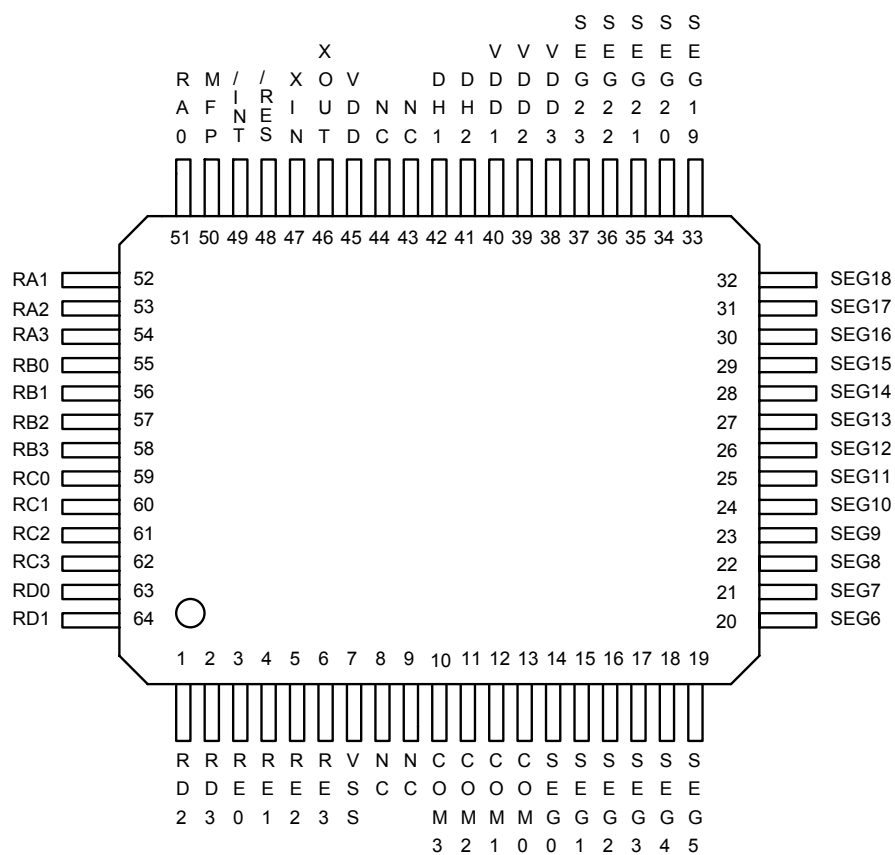


- LCD driver output
 - 24 segment $\times$ 4 common
 - Static, 1/2 duty (1/2 bias), 1/3 duty (1/2 or 1/3 bias), 1/4 duty (1/3 bias) driving mode can be selected
 - LCD driver output pins can be used as DC output ports; selectable by code option
- MFP output pin
 - Output is software selectable as modulating or nonmodulating frequency
 - Works as frequency output specified by Timer 1
- Built-in 14-bit clock frequency divider circuit
- Two built-in 8-bit programmable countdown timers
 - Timer 0: one of two internal clock frequencies ($F_{osc}/4$ or $F_{osc}/1024$) can be selected
 - Timer 1: includes an auto-reload function; and one of two internal clock frequencies (F_{osc} or $F_{osc}/64$) can be selected or falling edge of pin RC.0 can be selected (output through MFP pin)
- Built-in 18/14-bit watchdog timer selectable for system reset
- Powerful instruction set: 119 instructions
- 8-level subroutine (include interrupt) nesting
- Up to 1 μ S instruction cycle (with 4 MHz operating frequency)
- Packaged in 64-pin QFP

3. BLOCK DIAGRAM



4. PIN CONFIGURATION



5. PIN DESCRIPTION

SYMBOL	I/O	FUNCTION																									
XIN	I	Input pin for oscillator. Connected to crystal or resistor to generate system clock by code option. External 10~20pF capacitor uses to get accurate freq in crystal mode.																									
XOUT	O	Output pin for oscillator. Connected to crystal or resistor to generate system clock by code option. 20PF capacitor is built in internal for crystal mode.																									
RA0–RA3	I/O	Input/Output port. Input/output mode specified by port mode 1 register (PM1).																									
RB0–RB3	I/O	Input/Output port. Input/output mode specified by port mode 2 register (PM2).																									
RC0–RC3	I	4-bit port for input only. Each pin has an independent interrupt capability. And build-in schmitt trigger																									
RD0–RD3	I	4-bit port for input only.																									
RE0–RE3	O	Output port only. This port provides high sink current to drive LEDs.																									
MFP	O	Output pin only. This pin can output modulating or nonmodulating frequency, or Timer 1 clock output specified by mode register 1 (MR1).																									
$\overline{\text{INT}}$	I	External interrupt pin with pull-high resistor.																									
$\overline{\text{RES}}$	I	System reset pin with pull-high resistor.																									
SEG0–SEG23	O	LCD segment output pins. Can also be used as DC output ports specified by code option.																									
COM0–COM3	O	LCD common signal output pins. <table border="1"><thead><tr><th></th><th>Static</th><th>1/2 Duty</th><th>1/3 Duty</th><th>1/4 Duty</th></tr></thead><tbody><tr><td>COM0</td><td>Used</td><td>Used</td><td>Used</td><td>Used</td></tr><tr><td>COM1</td><td>Not Used</td><td>Used</td><td>Used</td><td>Used</td></tr><tr><td>COM2</td><td>Not Used</td><td>Not Used</td><td>Used</td><td>Used</td></tr><tr><td>COM3</td><td>Not Used</td><td>Not Used</td><td>Not Used</td><td>Used</td></tr></tbody></table> The LCD alternating frequency can be selected by code option.		Static	1/2 Duty	1/3 Duty	1/4 Duty	COM0	Used	Used	Used	Used	COM1	Not Used	Used	Used	Used	COM2	Not Used	Not Used	Used	Used	COM3	Not Used	Not Used	Not Used	Used
	Static	1/2 Duty	1/3 Duty	1/4 Duty																							
COM0	Used	Used	Used	Used																							
COM1	Not Used	Used	Used	Used																							
COM2	Not Used	Not Used	Used	Used																							
COM3	Not Used	Not Used	Not Used	Used																							
DH1, DH2	I	Connection terminals for voltage doubler (halver) capacitor.																									
$V_{\text{DD1}}, V_{\text{DD2}}$ V_{DD3}	I	Positive (+) supply voltage terminal. Refer to Functional Description.																									
V_{DD}	I	Positive power supply (+).																									
V_{SS}	I	Negative power supply (-).																									



6. ELECTRICAL CHARACTERISTICS

6.1 Absolute Maximum Ratings

PARAMETER	RATING	UNIT
Supply Voltage to Ground Potential	-0.3 to +7.0	V
Applied Input/Output Voltage	-0.3 to +7.0	V
Power Dissipation	120	mW
Ambient Operating Temperature	0 to +70	°C
Storage Temperature	-55 to +150	°C

Note: Exposure to conditions beyond those listed under Absolute Maximum Ratings may adversely affect the life and reliability of the device.

6.2 DC Characteristics

(V_{DD}-V_{SS} = 3.0V, F_{osc} = 32.768 KHz, T_A = 25° C; unless otherwise specified)

PARAMETER	SYM.	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Op. Voltage	V _{DD}	-	2.4	-	5.5	V
Op. Current (Crystal type)	I _{OP1}	No load (Ext-V)	-	8	20	μA
Op. Current (RC type)	I _{OP2}	No load (Ext-V)	-	35	65	μA
Hold Current (Crystal type)	I _{HM1}	Hold mode No load (Ext-V)	-	4	6	μA
Hold Current (RC type)	I _{HM2}	Hold mode No load (Ext-V)	-	16	40	μA
Stop Current (Crystal type)	I _{SM1}	Stop mode No load (Ext-V)	-	0.1	2	μA
Stop Current (RC type)	I _{SM2}	Stop mode No load (Ext-V)	-	0.1	2	μA
Input Low Voltage	V _{IL}	-	V _{SS}	-	0.3 V _{DD}	V
Input High Voltage	V _{IH}	-	0.7 V _{DD}	-	V _{DD}	V
MFP Output Low Voltage	V _{ML}	I _{OL} = 2.7 mA	-	-	0.4	V
MFP Output High Voltage	V _{MH}	I _{OH} = -3.5 mA	2.4	-	-	V
Port RA, RB Output Low Voltage	V _{ABL}	I _{OL} = 2.0 mA	-	-	0.4	V
Port RA, RB Output high Voltage	V _{ABH}	I _{OH} = -2.0 mA	2.4	-	-	V
LCD Supply Current	I _{LCD}	All Seg. On	-	-	10	μA
SEG0-SEG23 Sink Current (work as LCD output pins)	I _{OL}	V _{OL} = 0.4V V _{LCD} = 0.0V	0.4	-	-	μA
SEG0-SEG23 Drive Current (work as LCD output pins)	I _{OH}	V _{OH} = 2.4V V _{LCD} = 3.0V	-0.3	-	-	μA



DC Characteristics, continued

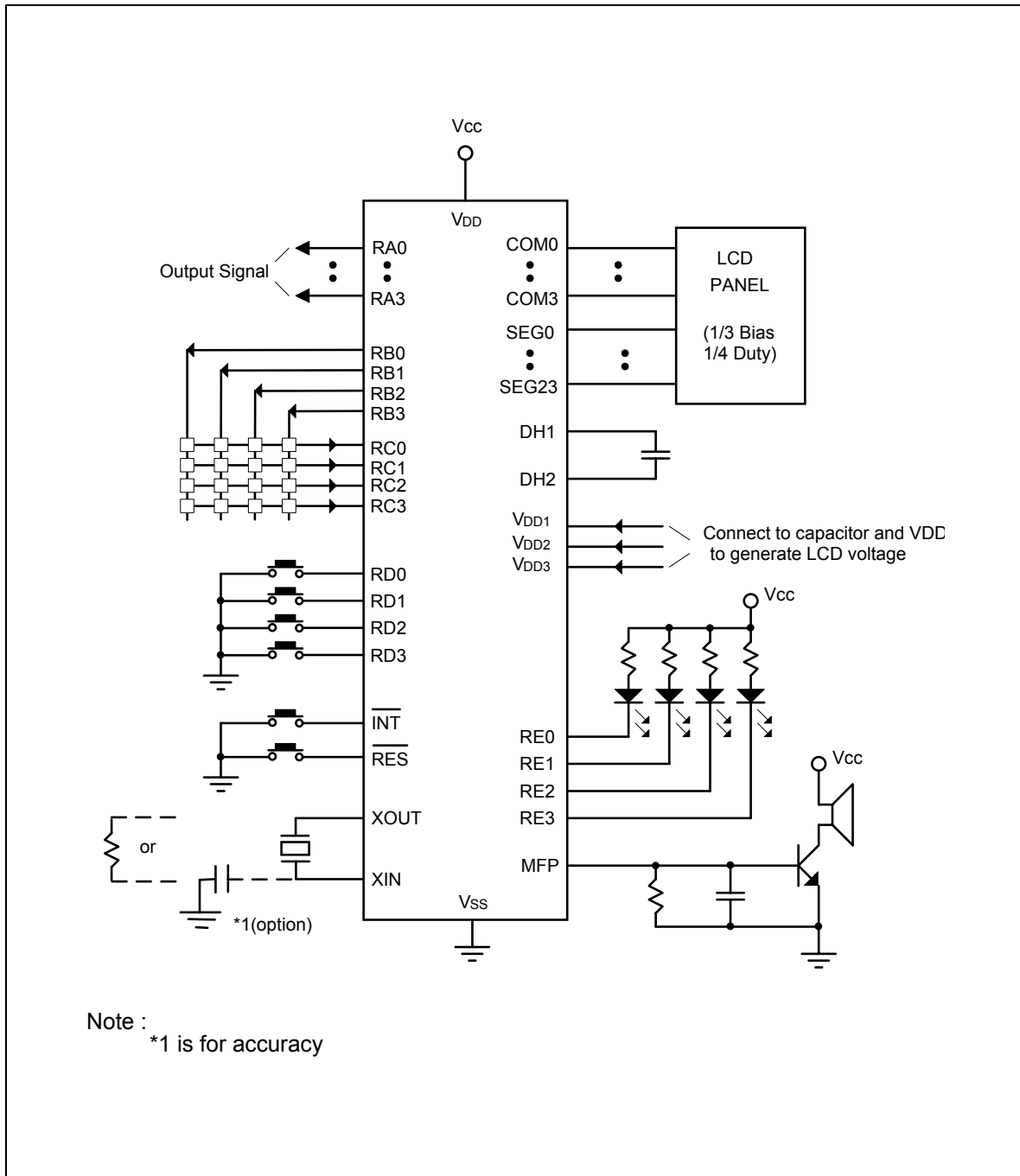
PARAMETER	SYM.	CONDITIONS	MIN.	TYP.	MAX.	UNIT
SEG0–SEG23 Output Low Voltage (work as DC output pins)	VSL	IOL = 0.6 mA	-	-	0.4	V
SEG0–SEG23 Output High Voltage (work as DC output pins)	VSH	IOH = -0.3 μ A	2.4	-	-	V
Port RE Sink Current	IEL	VOL = 0.9V	7.5	-	-	mA
Port RE Source Current	IEH	VOH = 2.4V	-0.4	-1.2	-	mA
Pull-up Resistor	RCD	Port RC, RD	100	350	1000	K Ω
$\overline{\text{INT}}$ Pull-up Resistor	RINT	-	50	250	1000	K Ω
$\overline{\text{RES}}$ Pull-up Resistor	RRES	-	20	100	500	K Ω

6.3 AC Characteristics

(V_{DD}–V_{SS} = 3V, T_A = 25° C; unless otherwise specified)

PARAMETER	SYM.	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Op. Frequency	FOSC	RC type	-	-	4000	KHz
		Crystal type 1 (Option low-speed type)	-	32.768	-	
		Crystal type 2 (Option high-speed type)	400	-	3580	
Instruction Cycle Time	T _I	One machine cycle	-	4/FOSC	-	mS
Reset Active Width	T _{RAW}	FOSC = 32.768 KHz	1	-	-	μ S
Interrupt Active Width	T _{IAW}	FOSC = 32.768 KHz	1	-	-	μ S

7. APPLICATION CIRCUIT



**8. REVISION HISTORY**

VERSION	DATE	DESCRIPTION
A2	—	Note: Xin/Xout had been built-in 20 pF capacitor. Delete XOUT capacitor from application circuit
A3	—	Note: Update Hi freq range from 2MHz to 3.58MHz (C250)
A4	—	Note: RC is built-in schmitt trigger
A5	May 29, 2003	Note: Do not be floating when it is as input or output open-drain (NMOS type)
A6	April 14, 2005	Add Important Notice

Important Notice

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