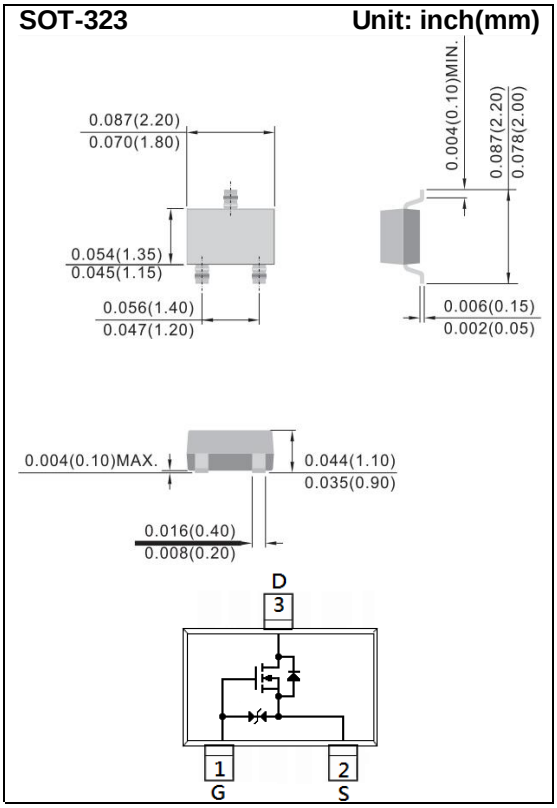
PJC7410

20V N-Channel Enhancement Mode MOSFET

Voltage	20 V	Current	500mA



Features

- Low Voltage Drive (1.2V).
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-323 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00018 ounces, 0.005 grams
- Marking: C10

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±10	V
Continuous Drain Current		I_D	500	mA
Pulsed Drain Current (Note 4)		I_{DM}	1000	mA
Power Dissipation	T_a=25°C	P_D	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)		$R_{\theta JA}$	357	°C/W

PJC7410

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	0.3	0.65	0.9	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=4.5V, I_D=500mA	-	280	400	mΩ
		V_{GS}=2.5V, I_D=200mA	-	350	650	
		V_{GS}=1.8V, I_D=100mA	-	400	800	
		V_{GS}=1.5V, I_D=50mA	-	500	1200	
		V_{GS}=1.2V, I_D=20mA	-	700	3000	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=16V, V_{GS}=0V	-	-	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V	-	±0.5	±10	uA
Dynamic (Note 5)						
Total Gate Charge	Q_g	V_{DS}=10V, I_D=500mA, V_{GS}=4.5V (Note 1,2)	-	1.4	-	nC
Gate-Source Charge	Q_{gs}		-	0.22	-	
Gate-Drain Charge	Q_{gd}		-	0.21	-	
Input Capacitance	Ciss	V_{DS}=10V, V_{GS}=0V, f=1.0MHZ	-	67	-	pF
Output Capacitance	Coss		-	19	-	
Reverse Transfer Capacitance	Crss		-	6	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=10V, I_D=150mA, V_{GS}=4.0V, R_G=10Ω (Note 1,2)	-	2.8	-	ns
Turn-On Rise Time	tr		-	20	-	
Turn-Off Delay Time	$td_{(off)}$		-	23	-	
Turn-Off Fall Time	tf		-	23	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	500	mA
Diode Forward Voltage	V_{SD}	I_S=500mA, V_{GS}=0V	-	0.87	1.3	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.

PJC7410

TYPICAL CHARACTERISTIC CURVES

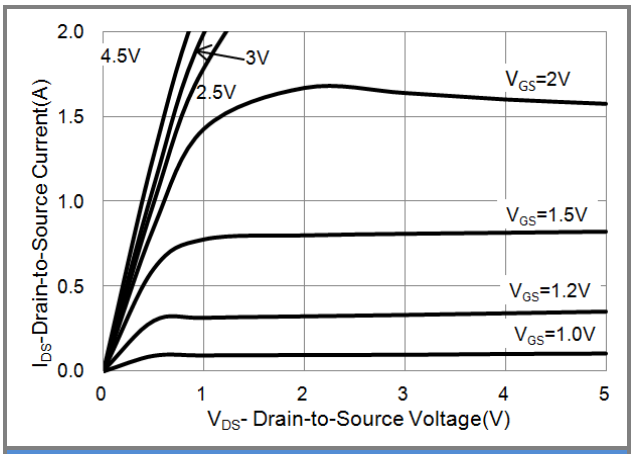


Fig.1 On-Region Characteristics

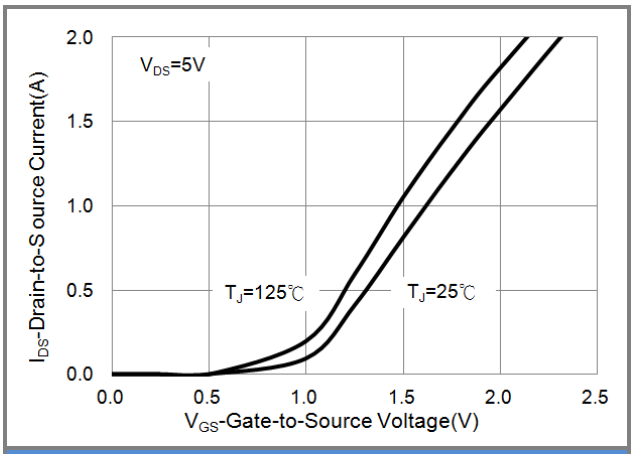


Fig.2 Transfer Characteristics

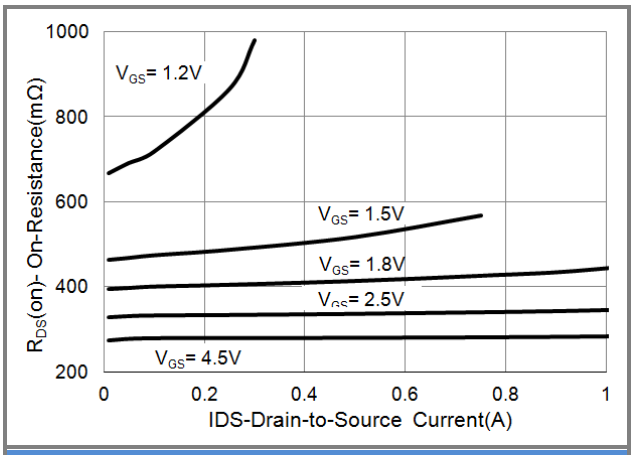


Fig.3 On-Resistance vs. Drain Current

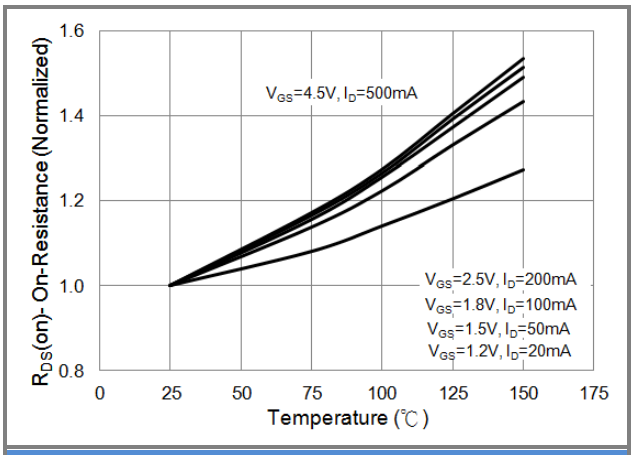


Fig.4 On-Resistance vs. Junction temperature

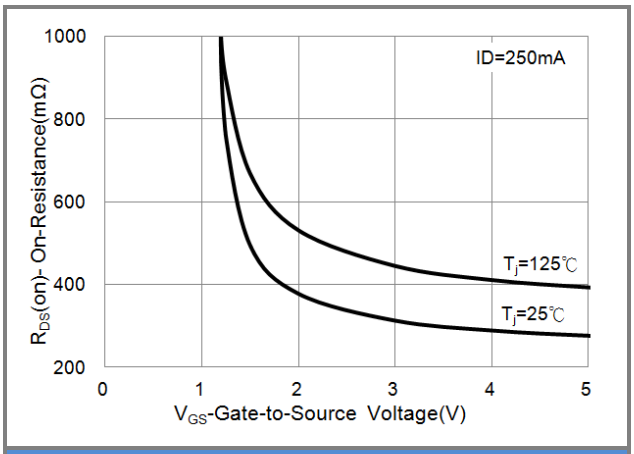


Fig.5 On-Resistance Variation with VGS.

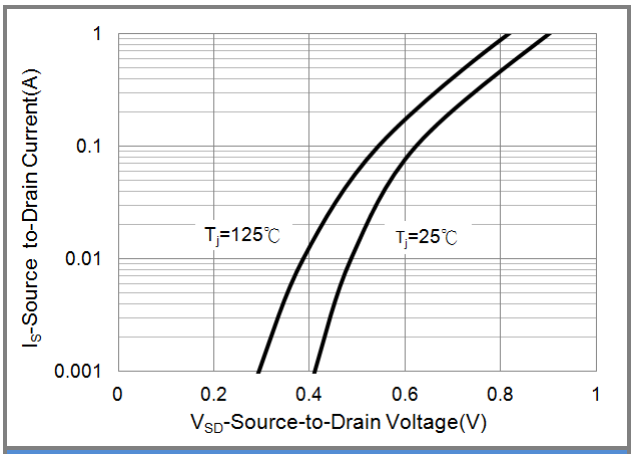


Fig.6 Body Diode Characteristics

TYPICAL CHARACTERISTIC CURVES

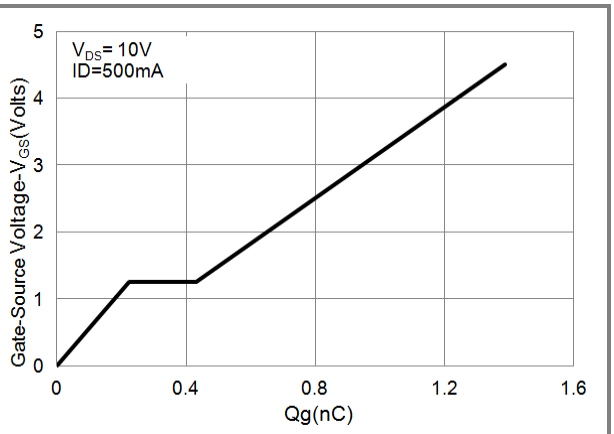


Fig.7 Gate-Charge Characteristics

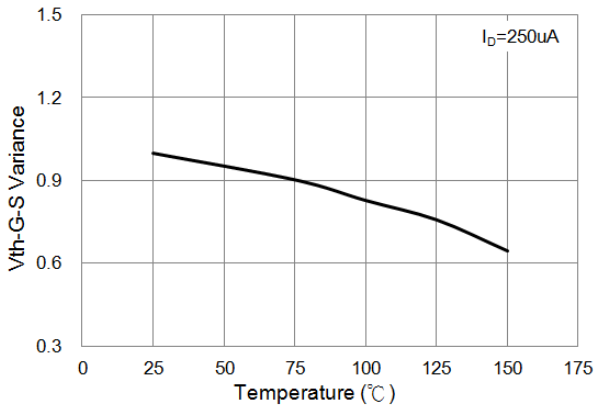


Fig.8 Threshold Voltage Variation with Temperature.

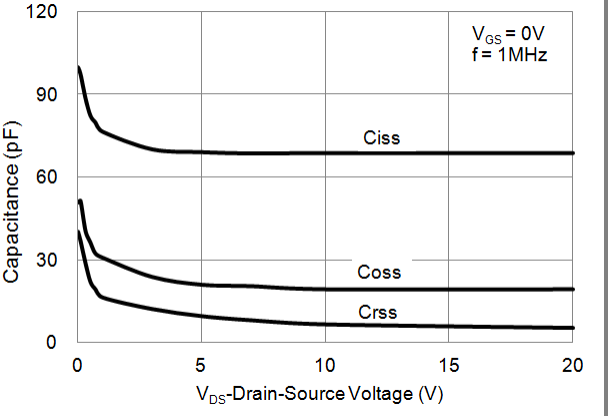


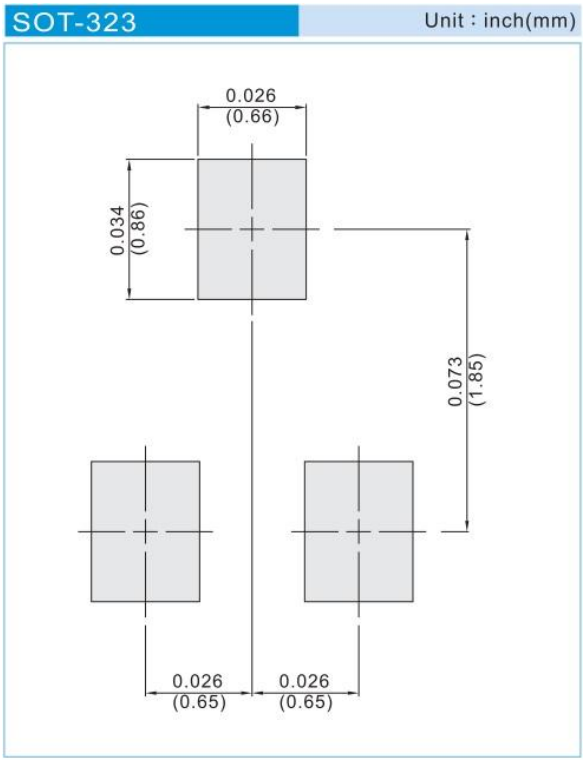
Fig.9 Capacitance vs. Drain-Source Voltage.

PJC7410

PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJC7410_R1_00001	SOT-323	3K pcs / 7” reel	C10	Halogen free
PJC7410_R2_00001	SOT-323	12K pcs / 7” reel	C10	Halogen free

MOUNTING PAD LAYOUT



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