

# FML-05D

## Ultra fast Plastic Power Rectifiers

VOLTAGE: 200V

CURRENT: 5.0A

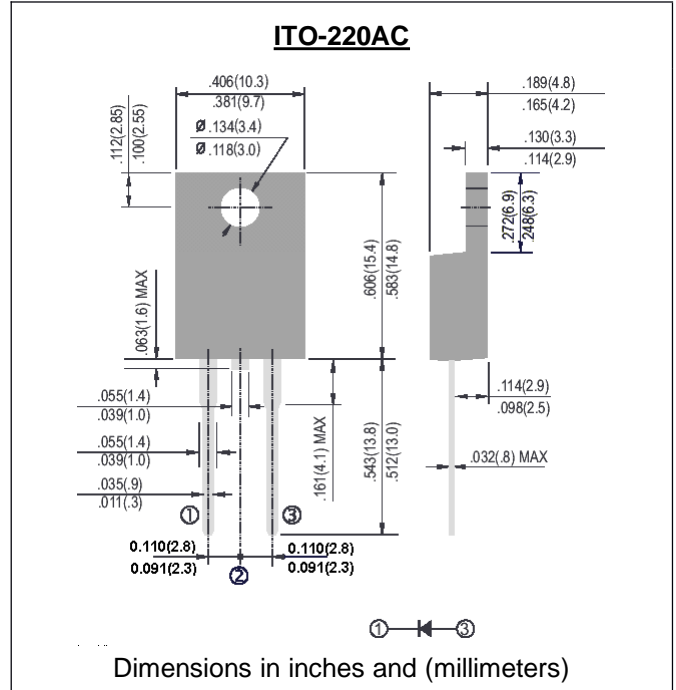


### FEATURE

- Plastic package has Underwriters Laboratories Flammability Classification 94V-0
- Ideally suited for use in very high frequency switching power supplies, inverters and as free wheeling diodes
- Ultra fast recovery time for high efficiency
- Excellent high temperature switching
- Glass passivated junction
- High voltage and high reliability
- High speed switching
- Low forward voltage

### MECHANICAL DATA

Case: JEDEC TO-220 molded plastic body over passivated chip  
 Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026  
 Polarity: Color band denotes cathode end  
 Mounting Position: Any



### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(single-phase, half-wave, 60HZ, resistive or inductive load rating at 25°C, unless otherwise stated)

	SYMBOL	FML-05D	units
Maximum Recurrent Peak Reverse Voltage	V <sub>rrm</sub>	200	V
Maximum RMS Voltage	V <sub>rms</sub>	140	V
Maximum DC blocking Voltage	V <sub>dc</sub>	200	V
Maximum Average Forward Rectified at T <sub>c</sub> =120°C	I <sub>f(av)</sub>	5.0	A
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I <sub>fsm</sub>	65	A
Maximum Forward Voltage at rated Forward Current and 25°C at 5A	V <sub>f</sub>	0.98	V
Maximum Reverse Recovery Time (Note 1)	T <sub>rr</sub>	30	nS
Typical thermal resistance junction to case	R <sub>θ Jc</sub>	5.0	°C/W
Maximum DC Reverse Current Ta =25°C	I <sub>r</sub>	10	μA
at rated DC blocking voltage Ta =125°C		100	μA
Storage and Operating Temperature Range	T <sub>stg</sub> , T <sub>j</sub>	-55 to +150	°C

**Note:**

1. Reverse Recovery Condition I<sub>f</sub> =0.5A, I<sub>r</sub> =1.0A, I<sub>rr</sub> =0.25A

RATINGS AND CHARACTERISTIC CURVES FML-5D

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FIG. 1- MAXIMUM FORWARD CURRENT DERATING CURVE

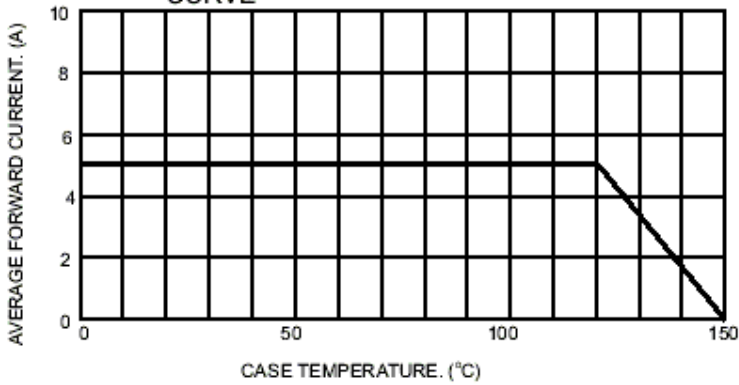


FIG. 2 - TYPICAL REVERSE CHARACTERISTICS

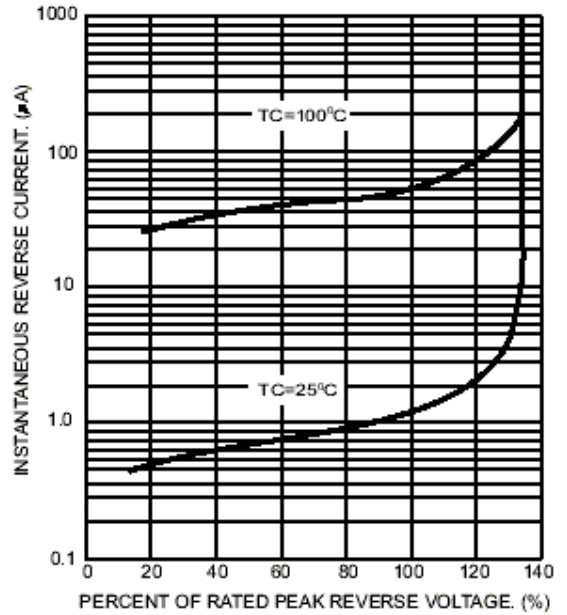


FIG. 3- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

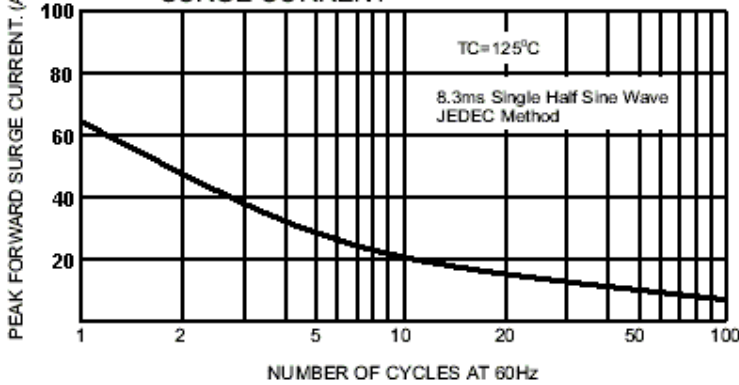


FIG. 5- TYPICAL FORWARD CHARACTERISTICS

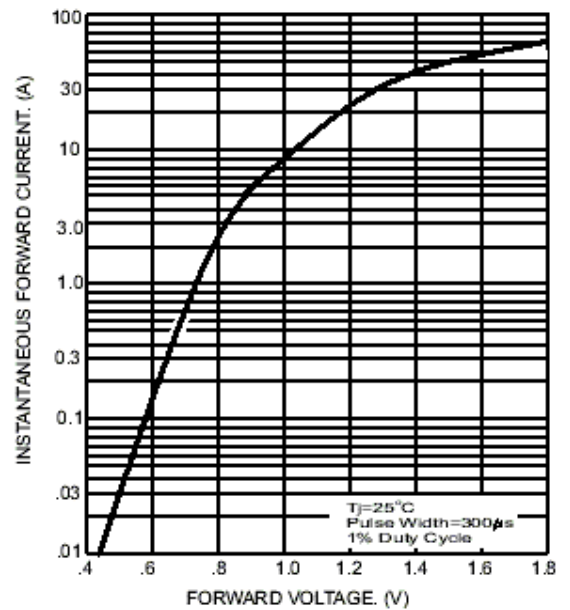


FIG. 4- TYPICAL JUNCTION CAPACITANCE

