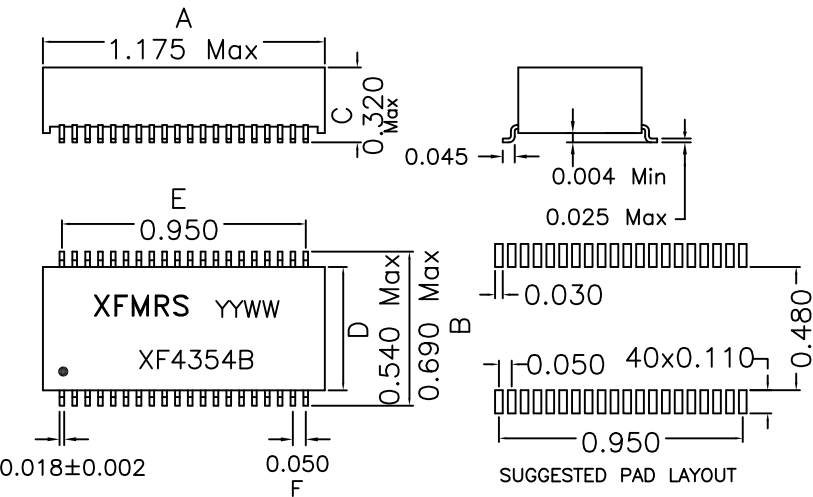


1. Mechanical Dimensions:



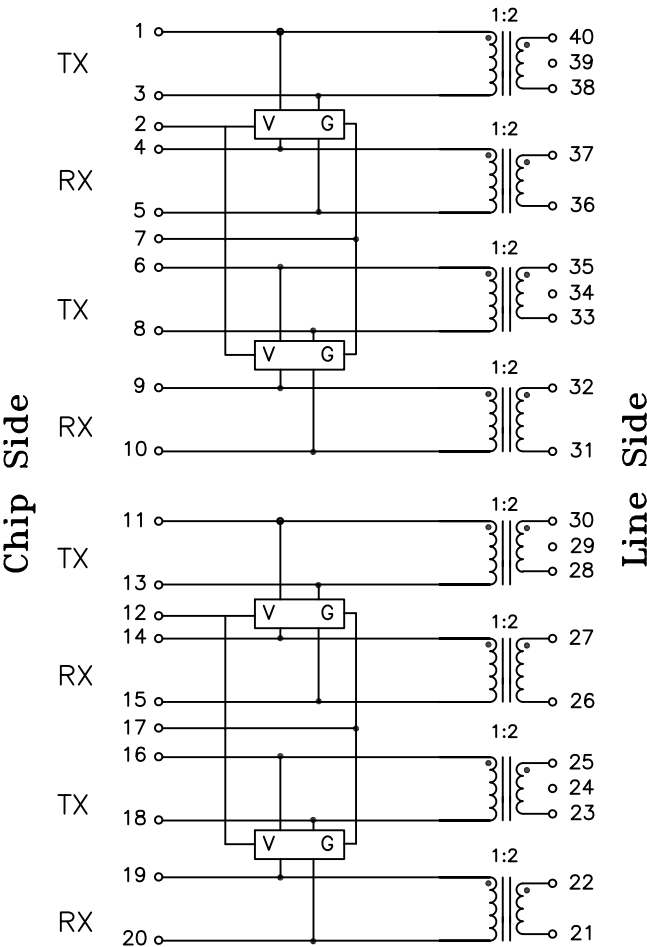
3. Electrical Specifications: @25°C

- Turns Ratio: CHIP/LINE TX 1:2, RX 1:2
Isolation Voltage: 1500 Vrms (Input to Output)
Line OCL: TX & RX 1.2mH Min @10KHz 0.1V
Line LL: TX & RX 0.65uH Max @1MHz 0.1V, Short Sec
Line/Chip Cw/w: 30pF Max @10KHz 0.1V
Crosstalk: 50dB Typ

Notes:

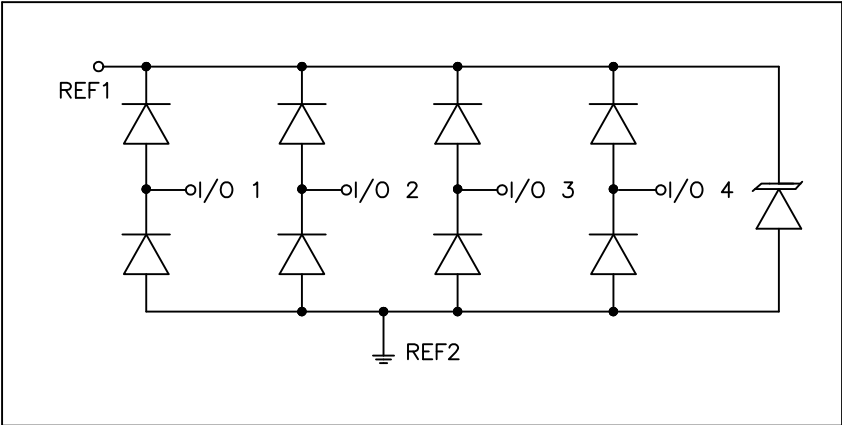
1. Solderability: Leads shall meet MIL-STD-202G, Method 208H for solderability.
2. Flammability: UL94V-0
3. ASTM oxygen index: > 28%
4. Insulation System: Class F 155°C. UL file E151556
5. Operating Temperature Range: All listed parameters are to be within tolerance from -40°C to +85°C
6. Storage Temperature Range: -55°C to +125°C
7. Aqueous wash compatible
8. SMD Lead Coplanarity: ±0.004"(0.102mm)
9. Electrical and mechanical specifications 100% tested
10. RoHS 5/6 Compliant, 63/37 leaded solder dipping on FG pins
11. Recommended IR Reflow temp 225C Max.

2. Schematic:

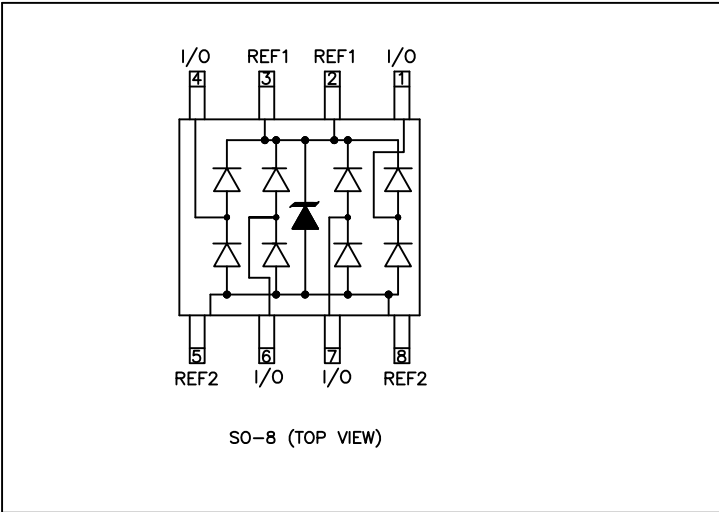


XFMRS Inc www.XFMRS.com	Title: QUAD PORT T1/CEPT/ISDN-PRI TRANSFORMER		
	UNLESS OTHERWISE SPECIFIED	P/N: XF4354B	REV. A
	TOLERANCES: .xxx ±0.010	DWN.	Xian Yi
	Dimensions in INCH	CHK.	YK Liao
SCALE 2:1 SHT 1 OF 2		APP.	DDC
			May-04-09
			May-04-09
			Sep-11-13

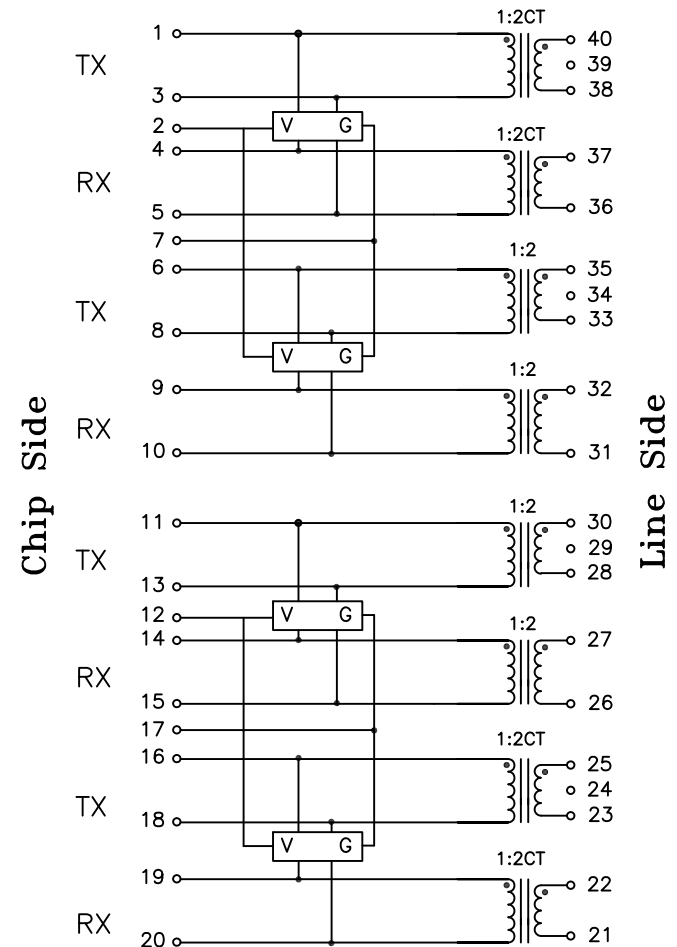
CIRCUIT DIAGRAM:



SCHEMATIC & PIN CONFIGURATION:



2. Schematic:



DETAILS FOR P/N: XF4354B "V G" PORTION OF THE CIRCUIT

XFMRS Inc www.XFMRS.com	Title: QUAD PORT T1/CEPT/ISDN-PRI TRANSFORMER		
	P/N: XF4354B		REV. A
	UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010	DWN.	Xian Yi
	Dimensions in INCH	CHK.	YK Liao
	SCALE 2:1 SHT 2 OF 2	APP.	DDC
			May-04-09
			May-04-09
			Sep-11-13