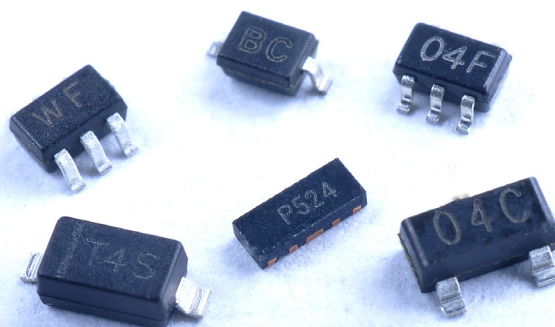




TVS/ESD Arrays

RLST23A032~362V Series

TVS/ESD Arrays - RLST23A032-362V Series

Features

- 400 Watts peak pulse power ($t_p = 8/20\mu s$)
- Transient protection for high speed data lines to IEC 61000-4-2 (ESD) $\pm 15kV$ (air), $\pm 8kV$ (contact)
IEC 61000-4-4 (EFT) 40A (5/50ns)
IEC 61000-4-5 (Lightning) 24A (8/20 μs)
- One device protects one unidirectional line
- Two devices protect two high-speed line pairs
- Low capacitance
- Low leakage current
- Low operating and clamping voltages
- Solid-state EPD TVS process technology



Mechanical Characteristics

- SOT-23 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel per EIA 481
- Lead Finish: Matte tin
- RoHS Compliant

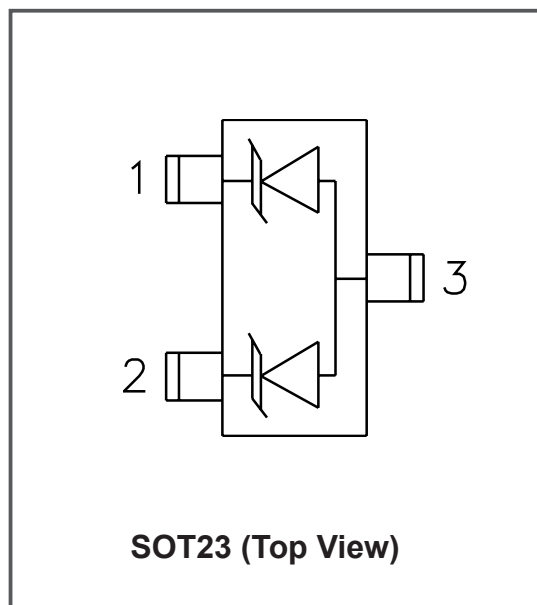
Applications

- Cellular Handsets and Accessories
- Portable Electronics
- Industrial Controls
- Set-Top Box
- Servers, Notebook, and Desktop PC

Life Support Note

- Not Intended for Use in Life Support or Life Saving Applications
- The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated

Pinout and Functional Block Diagram



TVS/ESD Arrays - RLST23A032-362V Series

Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp =8/20μs)	P _{pk}	400	Watts
Lead Soldering Temperature	T _L	260 (10 sec.)	°C
Operating Temperature	T _J	-55 to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

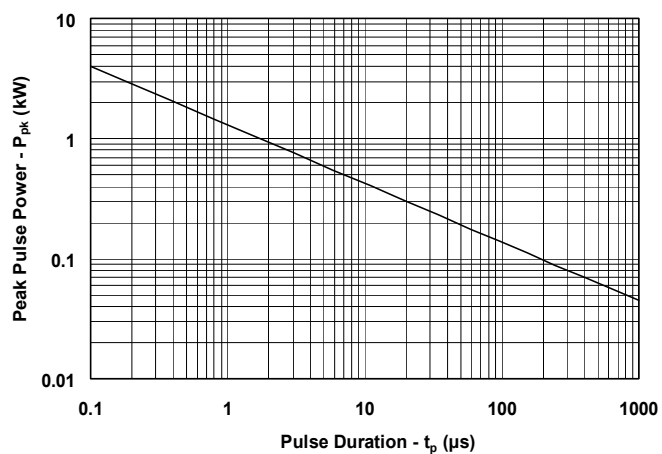
Electrical Characteristics Per Lin (@ 25°C Unless Otherwise Specified)

Type Number	Peak Power Dissipation @8/20μS P _{ppm}	Reverse Stand-Off Voltage	Peak Pulse Voltage @8/20μS P _{ppm}	Peak Pulse Current @8/20μS P _{ppm}	Reverse Leakage @V _{rw}	Maximum Capacitance @1MHZ
		V _{rw}	V _{c@IPP}	I _{PP}	I _{R@V_{rw}}	C _o
		Max			Max	Max
	W	V	V	A	μA	BI(pF)
RLST23A032V	400	3.3	6.5	43	125	300
RLST23A052V	400	5	10	20	10	220
RLST23A122V	400	12	25	20	1	100
RLST23A152V	400	15	30	20	1	90
RLST23A242V	400	24	45	15	1	80
RLST23A362V	400	36	75	7	1	70

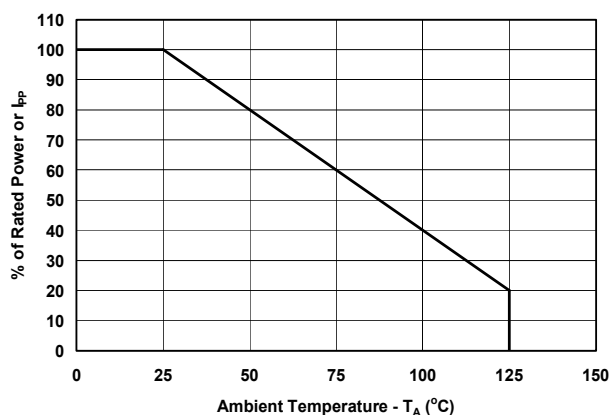
TVS/ESD Arrays - RLST23A032-362V Series

Typical Characteristics

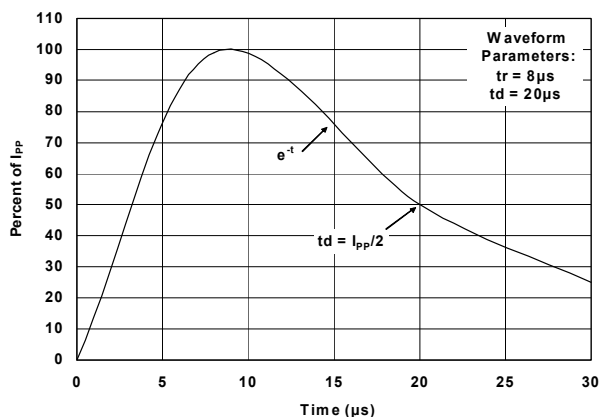
Non-Repetitive Peak Pulse Power vs. Pulse Time



Power Derating Curve



Pulse Waveform



TVS/ESD Arrays - RLST23A032V Series

Applications Information

Device Connection Options

The RLST23A032V and RLST23A362V series is designed to protect one bidirectional or two unidirectional data or I/O lines operating at 5 to 36 volts. Connection options are as follows:

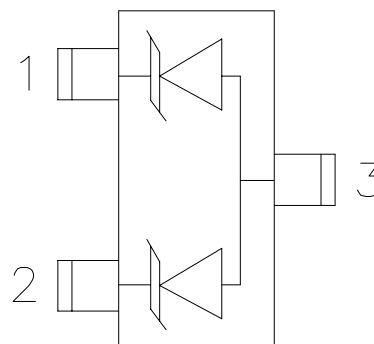
- **Bidirectional:** Pin 1 is connected to the data line and pin 2 is connected to ground (Since the device is symmetrical, these connections may be reversed). The ground connection should be made directly to a ground plane. The path length should be kept as short as possible to minimize parasitic inductance. Pin 3 is not connected.
- **Unidirectional:** Data lines are connected to pin 1 and pin 2. Pin 3 is connected to ground. For best results, this pin should be connected directly to a ground plane on the board. The path length should be kept as short as possible to minimize parasitic inductance. Circuit Board Layout Recommendations for Suppression of ESD. Good circuit board layout is critical for the suppression of fast rise-time transients such as ESD. The following guidelines are recommended (Refer to application note SI99-01 for more detailed information):

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

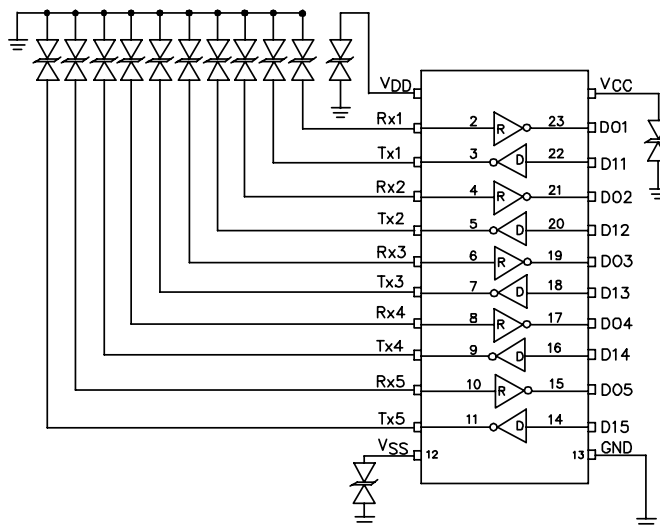
Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the and pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

Device Schematic & Pin Configuration

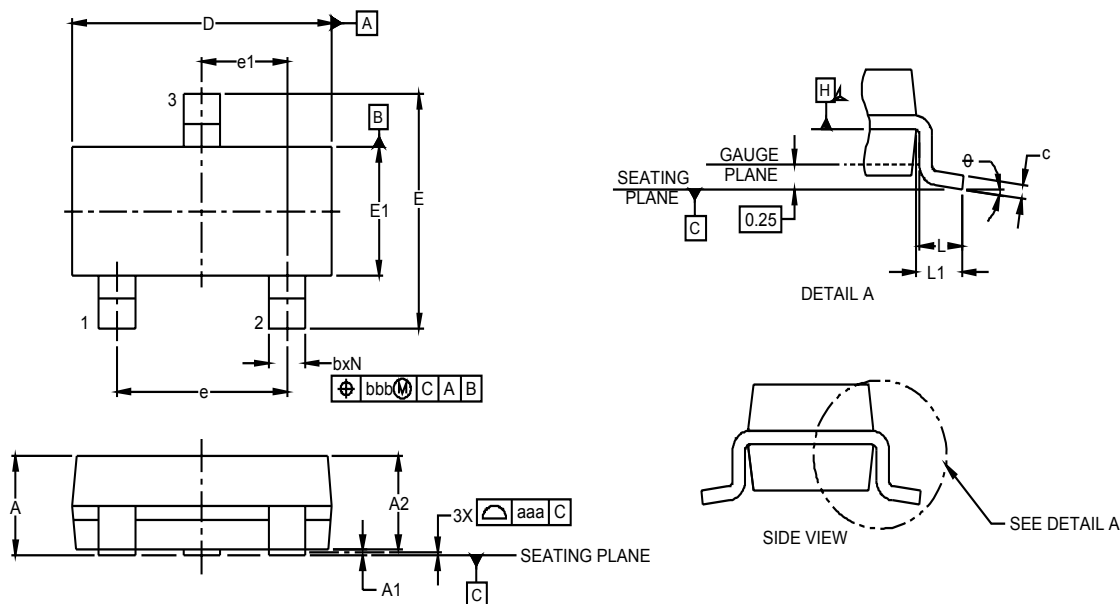


RS-232 Transceiver Protection Example



TVS/ESD Arrays - RLST23A032-362V Series

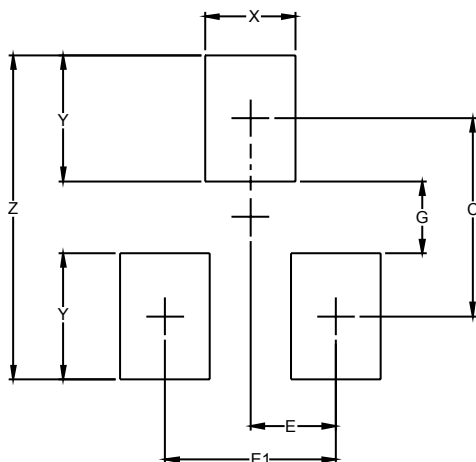
Package dimension SOT-23



Dimensions						
DIM	Inches			Millimeters		
	Min	Nom	Max	Min	Nom	Max
A	.035	-	.044	0.89	-	1.12
A1	-	-	.004	0.01	-	0.10
A2	.035	.037	.040	0.01	-	0.10
b	.012	-	.020	0.30	-	0.51
c	.003	-	.007	0.08	-	0.18
D	.110	.114	.120	2.80	2.90	3.04
E	.082	.093	.104	2.10	2.37	2.64
E1	0.47	.051	.055	1.20	1.30	1.40
e		.075			1.90 BCS	
e1		.037			0.95 BCS	
L	.015	.020	.024	0.40	0.50	0.60
L1		.022			(0.55)	
N		3			3	

TVS/ESD Arrays - RLST23A032-362V Series

Land Pattern SOT-23

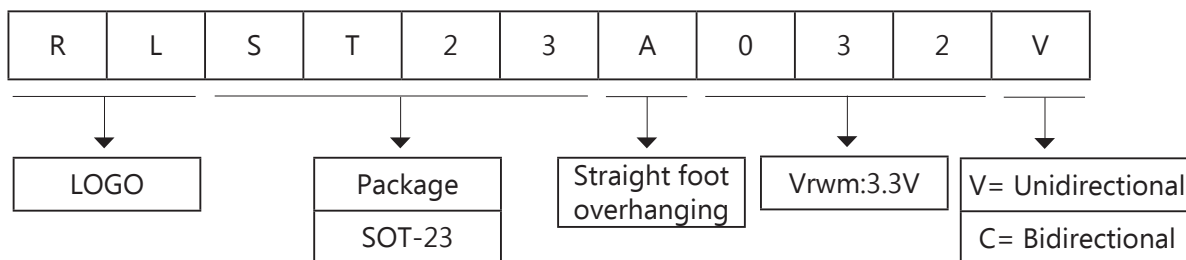


NOTES:

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY
CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR
COMPANY'S MANUFACTURING GUIDELINES ARE MET.
2. REFERENCE IPC-SM-782A.

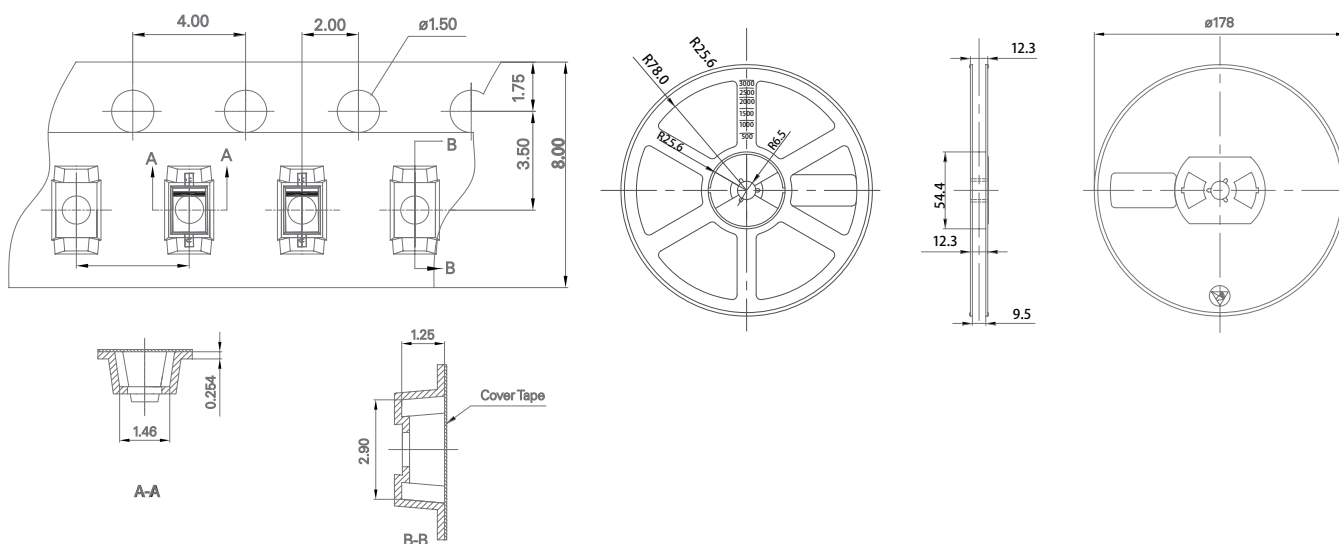
DIMENSIONS		
DIM	INCHES	MILLIMETERS
C	(.087)	(2.20)
E	.037	0.95
E1	.075	1.90
G	.031	0.80
X	.039	1.00
Y	.055	1.40
Z	.141	3.60

Part Number Code



TVS/ESD Arrays - RLST23A032-362V Series

Ordering Information



Ordering Information

Part Number	Package	Min. Order Qty.
RLST23A032-036V	SOT-23	3000pcs

Warehouse Storage Conditions of Products

- Storage Conditions:
 - Storage Temperature: $-10^{\circ}\text{C} \sim +40^{\circ}\text{C}$
 - Relative Humidity: $\leq 75\% \text{RH}$
 - Keep away from corrosive atmosphere and sunlight.
 - Period of Storage: 1 year

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