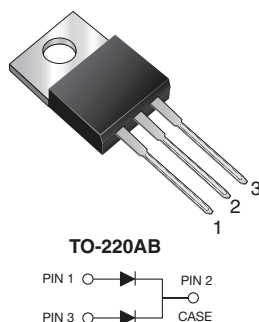


Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.37 \text{ V}$ at $I_F = 5 \text{ A}$



RoHS
COMPLIANT
HALOGEN
FREE

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating
Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 x 30 A
V_{RRM}	100 V
I_{FSM}	320 A
V_F at $I_F = 30 \text{ A}$ (125 °C)	0.63 V
T_J max.	175 °C
Package	TO-220AB
Circuit configuration	Common cathode

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)			
PARAMETER	SYMBOL	V61M103C	UNIT
Maximum repetitive peak reverse voltage	V_{RRM}	100	V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$	60	A
		30	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode	I_{FSM}	320	A
Operating junction temperature range	$T_J^{(1)}$	-40 to +175	°C
Storage temperature range	T_{STG}	-55 to +175	

Note

(1) The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{\theta JA}$



ELECTRICAL CHARACTERISTICS (T _J = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _J = 25 °C	V _F ⁽¹⁾	0.47	-	V
	I _F = 15 A			0.58	-	
	I _F = 30 A			0.71	0.77	
	I _F = 5 A	T _J = 125 °C		0.37	-	
	I _F = 15 A			0.51	-	
	I _F = 30 A			0.63	0.68	
Reverse current at rated V _R per diode	V _R = 70 V	T _J = 25 °C	I _R ⁽²⁾	0.008	-	mA
		T _J = 125 °C		5.2	-	
	V _R = 100 V	T _J = 25 °C		-	0.8	
		T _J = 125 °C		12	30	
Typical junction capacitance	4 V, 1MHz		C _J	3250	-	pF

Notes

⁽²⁾ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽³⁾ Pulse test: Pulse width $\leq 5\text{ ms}$

THERMAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	V61M103C	UNIT
Typical thermal resistance per device	$R_{\theta JC}^{(1)}$	1.0	$^{\circ}\text{C/W}$

Note

⁽⁴⁾ Thermal resistance junction-to-case to follow JEDEC[®] 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
V61M103C-M3/P	1.88	P	50/tube	Tube

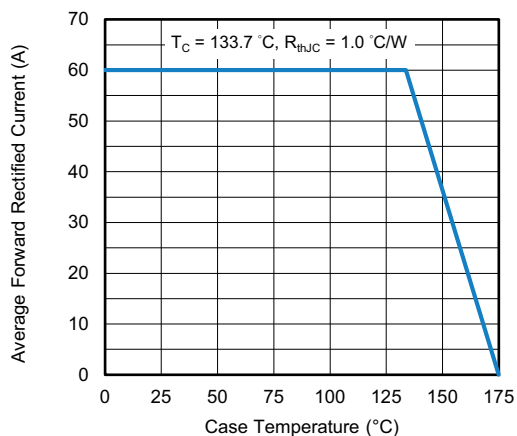
RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)


Fig. 1 - Forward Current Derating Curve

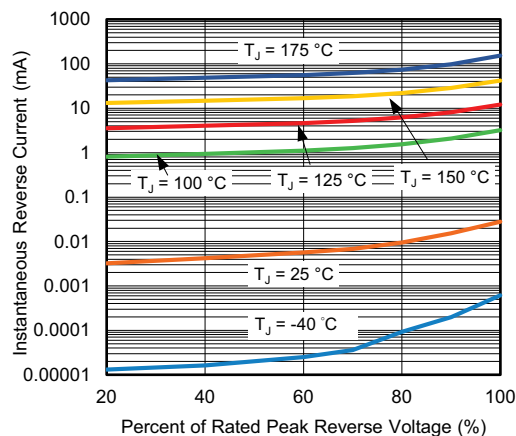


Fig. 4 - Typical Reverse Characteristics Per Diode

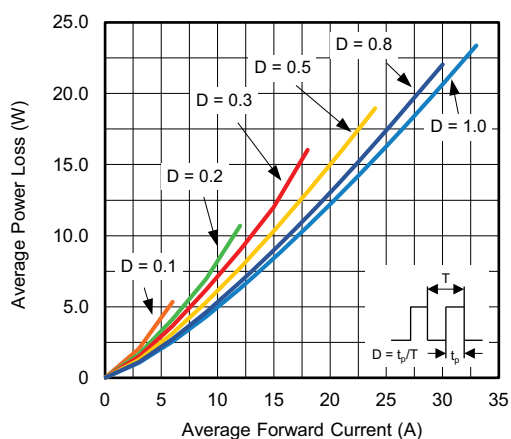


Fig. 2 - Forward Power Loss Characteristics Per Diode

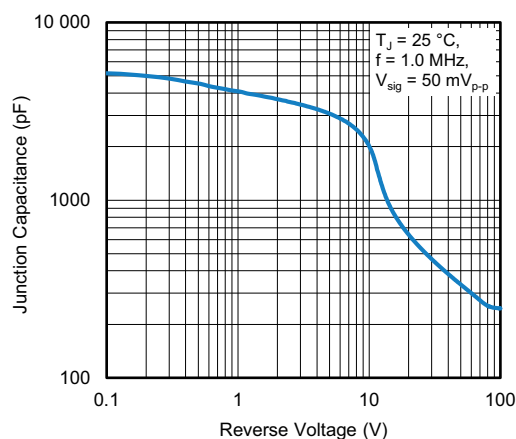


Fig. 5 - Typical Junction Capacitance

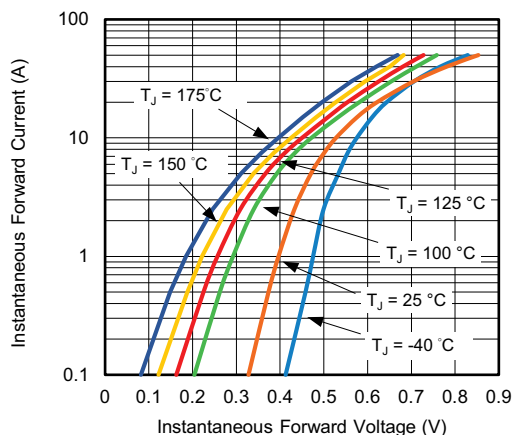


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

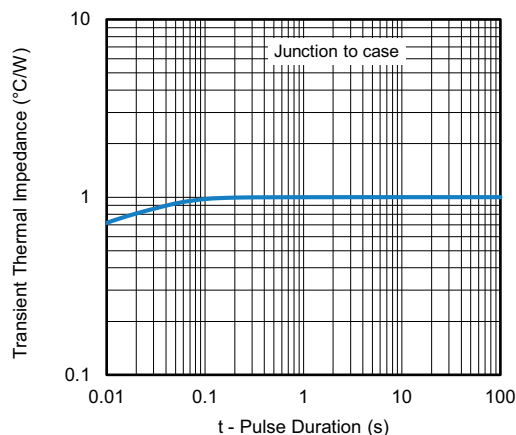
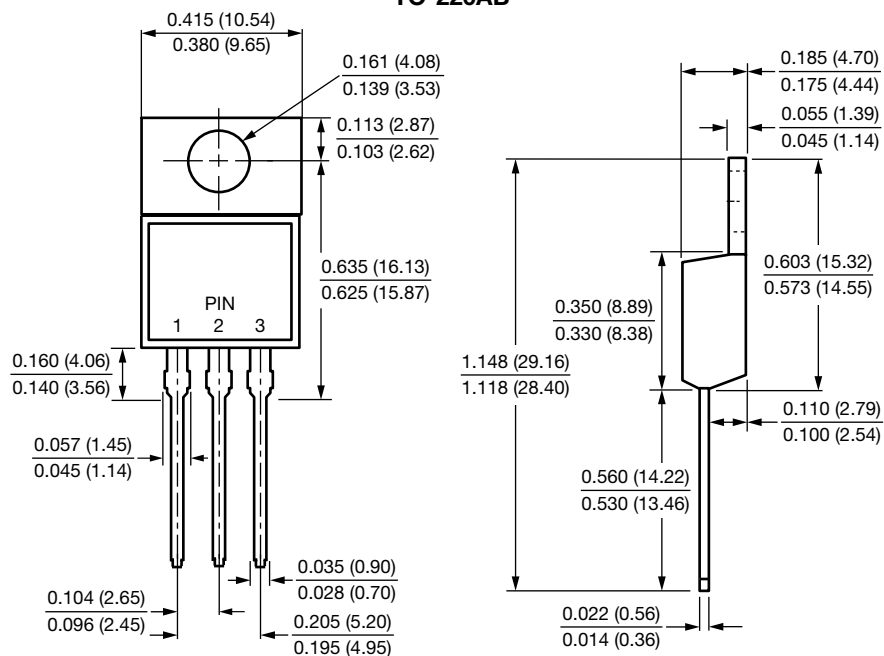


Fig. 6 - Typical Transient Thermal Impedance Per Device



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-220AB





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