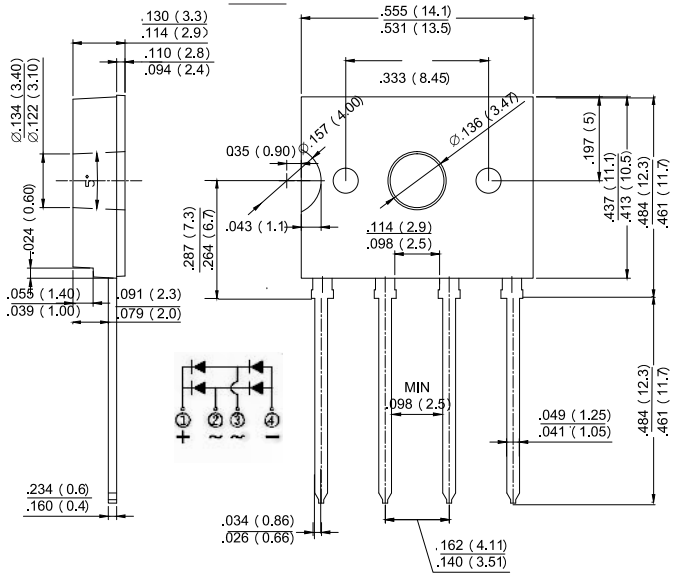
UG4KB05 THRU UG4KB100

SILICON BRIDGE RECTIFIERS

Reverse Voltage - 50 to 1000 Volts Forward Current - 4.0 Amperes

D3K



Dimensions in inches and (millimeters)

FEATURES

- Glass passivated die construction
- Low forward voltage drop
- High current capability
- High surge current capability
- Designed for surface mount application
- Plastic material-UL flammability 94V-O

MECHANICAL DATA

Case: D3K molded plastic body
Terminals: Plated leads solderable per MIL-STD-202, Method 208
Polarity: As marked on case
Mounting Position: Any
Marking : Type number

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25℃ ambient temperature unless otherwise specified.
Single phase half-wave 60Hz,resistive or inductive load,for capacitive load current derate by 20%.

MDD Catalog Number	*SYMBOLS*	UG4K B05	UG4K B10	UG4K B20	UG4K B40	UG4K B60	UG4K B80	UG4K B100	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	VOLTS
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	VOLTS
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	VOLTS
Maximum average forward output rectified current at T_A=40℃(Note 1)	$I_{(AV)}$	4.0							Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	80.0							Amps
Forward voltage per element @IF=4.0A	V_F	1.1							Volts
Maximum DC reverse current T_A=25℃	I_R	5.0							μA
at rated DC blocking voltage T_A=125℃		0.5							mA
Typical juntion capacitance per leg	C_J	21							pF
Typical Thermal Resistance per leg(Note 2)	$R_{\theta JA}$	55							°C/W
	$R_{\theta JL}$	15							
Operating junction temperature range	T_J, T_{STG}	-55 to +150							°C

Note:1. Mounted on glass epoxy PC board with 1.3mm^2 solder pad.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C..

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RATINGS AND CHARACTERISTIC CURVES UG4KB05 THRU UG4KB100

Fig. 1 Output Current Derating Curve

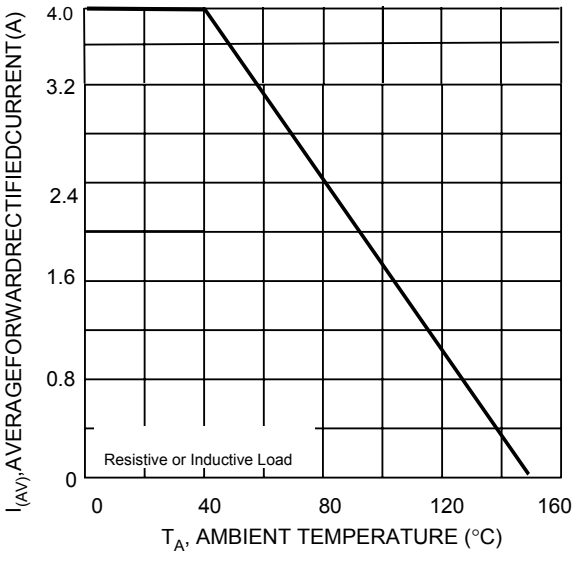


Fig. 2 Typical I Forward Characteristics (per leg)

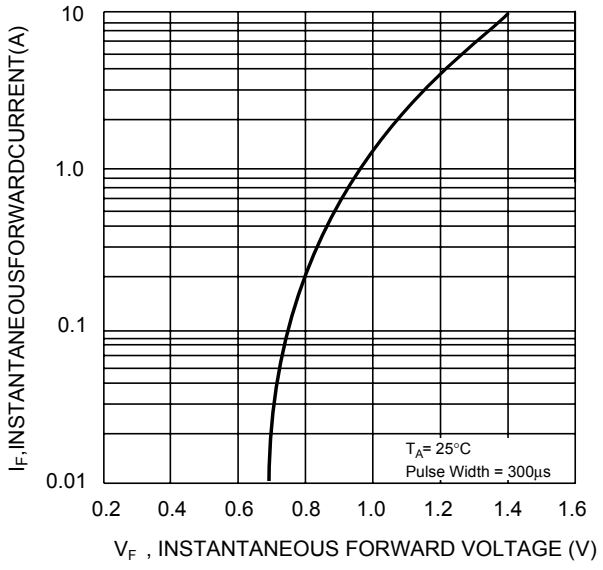


Fig. 3 Maximum Peak Forward Surge Current (per leg)

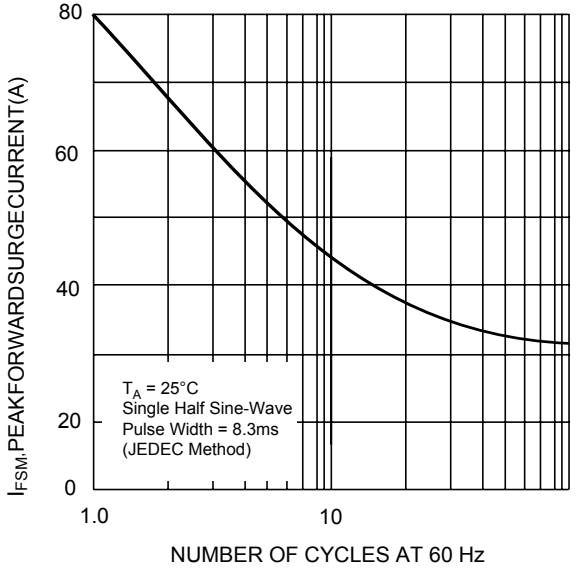
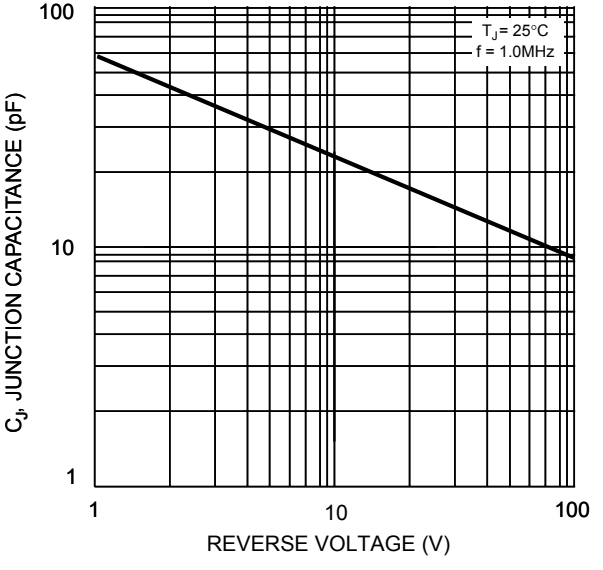


Fig.4 Typical Junction Capacitance Per Diode



The cruve graph is for reference only, can't be the basis for judgment( )!

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