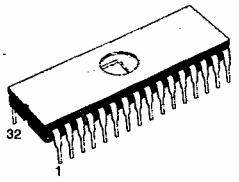
1024K (128K x 8) CMOS UV ERASABLE PROM

ADVANCED DATA

- 8 BITS OUTPUTS
- FAST ACCESS TIME 120ns.
- LOW "CMOS" CONSUMPTION 50mA (MAX.)
- PROGRAMMING VOLTAGE 12.5V
- ELECTRONIC SIGNATURE FOR AUTOMATED PROGRAMMING
- PROGRAMMING TIMES IN THE 20 SECONDS RANGE.



F
DIP-32
(Ceramic Bull's eye)

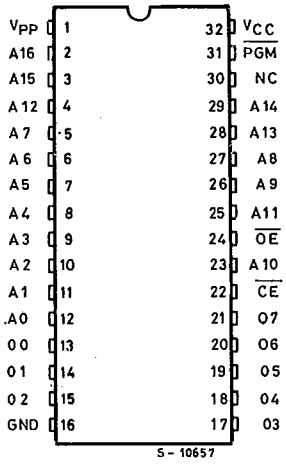
(Ordering Information at the end of the datasheet)

DESCRIPTION

The ST27C1001 is a high speed 1 Mbit UV erasable and electrically programmable EPROM ideally suited for 8-bit microprocessors systems requiring large programs.
It is organized as 131072 words by 8 bits, and packaged in a 32 pins Ceramic DIP Bull's eye package.
ST will also introduce the following versions based on the same architecture but with different configurations. They are:

- ST27C1011 is a page addressed 1024K (8 x 16K x 8) device, packaged in a 28 pin DIP for easy replacement of 64K and 128K standard EPROM versions.
- ST87C1011 is the same device as the ST27C1011 with latched addresses for design optimization in multiplexed bus environment.
- ST27C1000 is organized as 128K x 8 bits with a ROM compatible pinout.
- ST87C1000 is the same device as the ST27C1000 with latched addresses for design optimization in multiplexed bus environment.

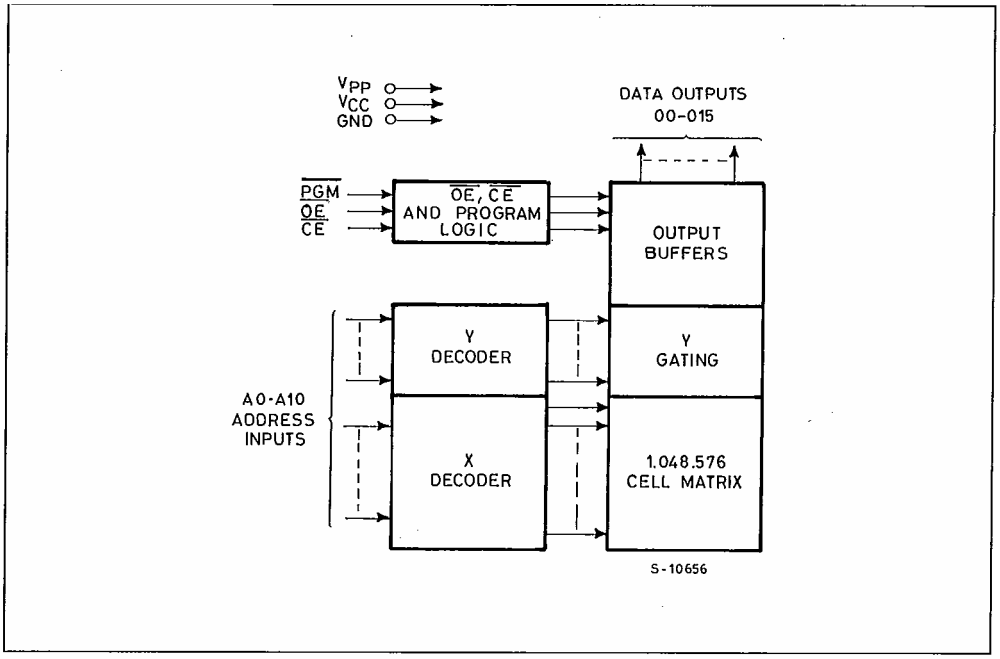
PIN CONNECTIONS



PIN NAMES

A0—A16	ADDRESS INPUT
$\overline{CE}$	CHIP ENABLE INPUT
$\overline{OE}$	OUTPUT ENABLE
$\overline{PGM}$	PROGRAM
O_0-O_7	DATA INPUT/OUTPUT
NC	NON CONNECTED

This is advance information on a new product now in development or undergoing evaluation. Details are subject to change without notice.


VPP
VCC
GND
DATA OUTPUTS
O0-O15
PGM
OE
CE
OE, CE
AND PROGRAM
LOGIC
OUTPUT
BUFFERS
Y
DECODER
Y
GATING
A0-A10
ADDRESS
INPUTS
X
DECODER
1.048.576
CELL MATRIX
S-10656