

# 12N70K-MT

**Power MOSFET**

## 12A, 700V N-CHANNEL POWER MOSFET

### ■ DESCRIPTION

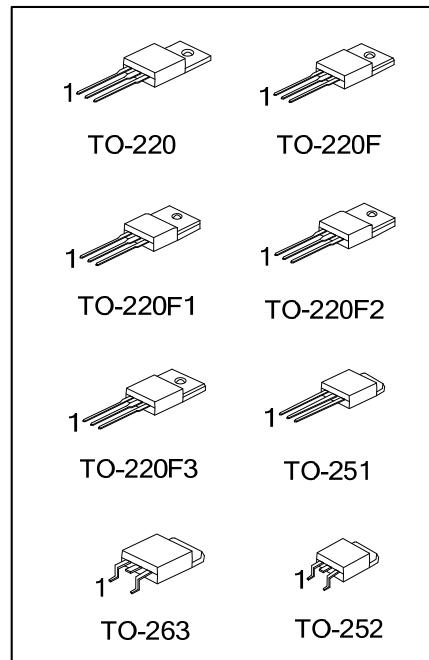
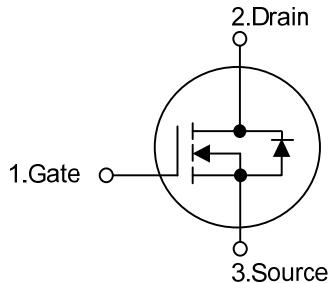
The UTC **12N70K-MT** are N-Channel enhancement mode power MOSFET which are produced using UTC's proprietary, planar stripe, DMOS technology.

These devices are suited for high efficiency switch mode power supply. To minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode the advanced technology has been especially tailored.

### ■ FEATURES

- \*  $R_{DS(ON)} \leq 0.83\Omega$  @  $V_{GS}=10V$ ,  $I_D=6.0A$
- \* Fast switching capability
- \* Avalanche energy specified
- \* Improved dv/dt capability, high ruggedness

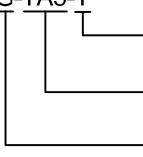
### ■ SYMBOL



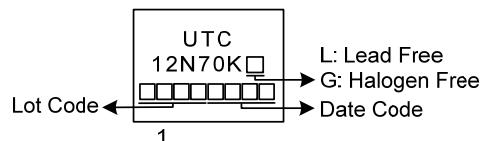
### ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12N70KL-TA3-T	12N70KG-TA3-T	TO-220	G	D	S	Tube
12N70KL-TF1-T	12N70KG-TF1-T	TO-220F1	G	D	S	Tube
12N70KL-TF2-T	12N70KG-TF2-T	TO-220F2	G	D	S	Tube
12N70KL-TF3T-T	12N70KG-TF3T-T	TO-220F3	G	D	S	Tube
12N70KL-TF3-T	12N70KG-TF3-T	TO-220F	G	D	S	Tube
12N70KL-TM3-T	12N70KG-TM3-T	TO-251	G	D	S	Tube
12N70KL-TN3-R	12N70KG-TN3-R	TO-252	G	D	S	Tape Reel
12N70KL-TQ2-T	12N70KG-TQ2-T	TO-263	G	D	S	Tube
12N70KL-TQ2-R	12N70KG-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, TF3T: TO-220F3TM3: TO-251, TN3: TO-252, TQ2: TO-263 (3) G: Halogen Free and Lead Free, L: Lead Free
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### ■ MARKING



L: Lead Free  
G: Halogen Free

■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	700	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	12	A
	Pulsed (Note 2)	$I_{DM}$	24	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	170	mJ
Peak Diode Recovery $dv/dt$ (Note 4)		$dv/dt$	2.9	V/ns
Power Dissipation	TO-220/TO-263	$P_D$	140	W
	TO-220F/ TO-220F1		34	W
	TO-220F3		36	W
	TO-220F2		50	W
	TO-251/TO-252			
Junction Temperature	$T_J$		+150	$^\circ\text{C}$
Operating Temperature	$T_{OPR}$		-55 ~ +150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$		-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=10\text{mH}$ ,  $I_{AS}=5.8\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$

4.  $I_{SD} \leq 12\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J=25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220F3/TO-263	$\theta_{JA}$	62.5	$^\circ\text{C/W}$
	TO-251/TO-252		110	$^\circ\text{C/W}$
Junction to Case	TO-220/TO-263	$\theta_{JC}$	0.89	$^\circ\text{C/W}$
	TO-220F/ TO-220F1 TO-220F3		3.68	$^\circ\text{C/W}$
	TO-220F2		3.47	$^\circ\text{C/W}$
	TO-251/TO-252		2.5	$^\circ\text{C/W}$

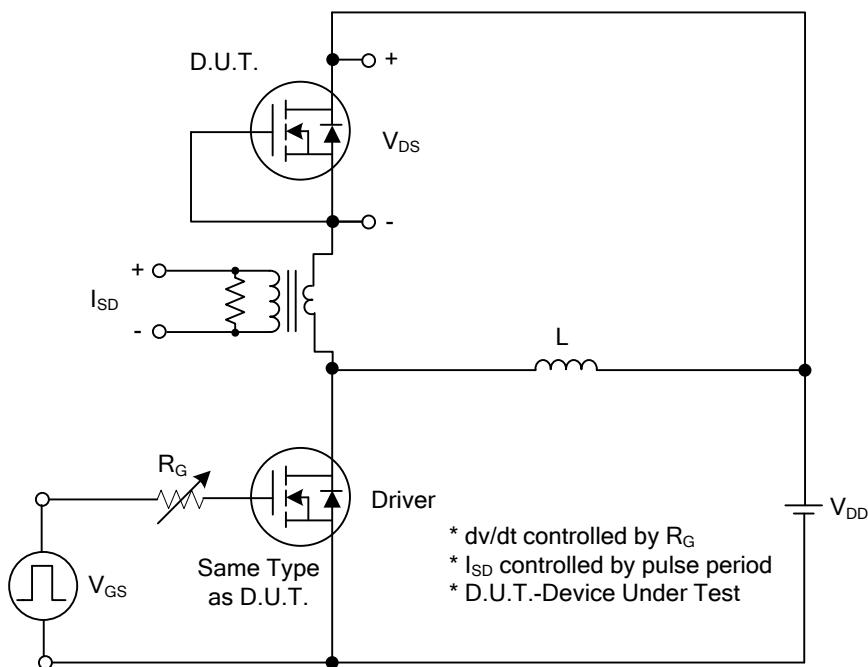
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	700			V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=700\text{V}, V_{\text{GS}}=0\text{V}$		10		$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 30\text{V}, V_{\text{DS}}=0\text{V}$			$\pm 100$	nA
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}}=250\mu\text{A}$ , Referenced to $25^\circ\text{C}$	0.7			$\text{V}/^\circ\text{C}$
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=6.0\text{A}$		0.70	0.83	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		2000		pF
Output Capacitance	$C_{\text{OSS}}$			160		pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			7		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge	$Q_G$	$V_{\text{DS}}=560\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=12\text{A}$ (Note 1, 2)		40		nC
Gate-Source Charge	$Q_{\text{GS}}$			10		nC
Gate-Drain Charge	$Q_{\text{GD}}$			7		nC
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}}=100\text{V}, I_{\text{D}}=12\text{A}, R_{\text{G}}=25\Omega$ (Note 1, 2)		28		ns
Turn-On Rise Time	$t_R$			21		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			122		ns
Turn-Off Fall Time	$t_F$			37		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$				12	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{\text{SM}}$				24	A
Drain-Source Diode Forward Voltage	$V_{\text{SD}}$	$I_S=12\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_S=12\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=200\text{A}/\mu\text{s}$ (Note 1)		534		ns
Reverse Recovery Charge	$Q_{\text{rr}}$			5.7		$\mu\text{C}$

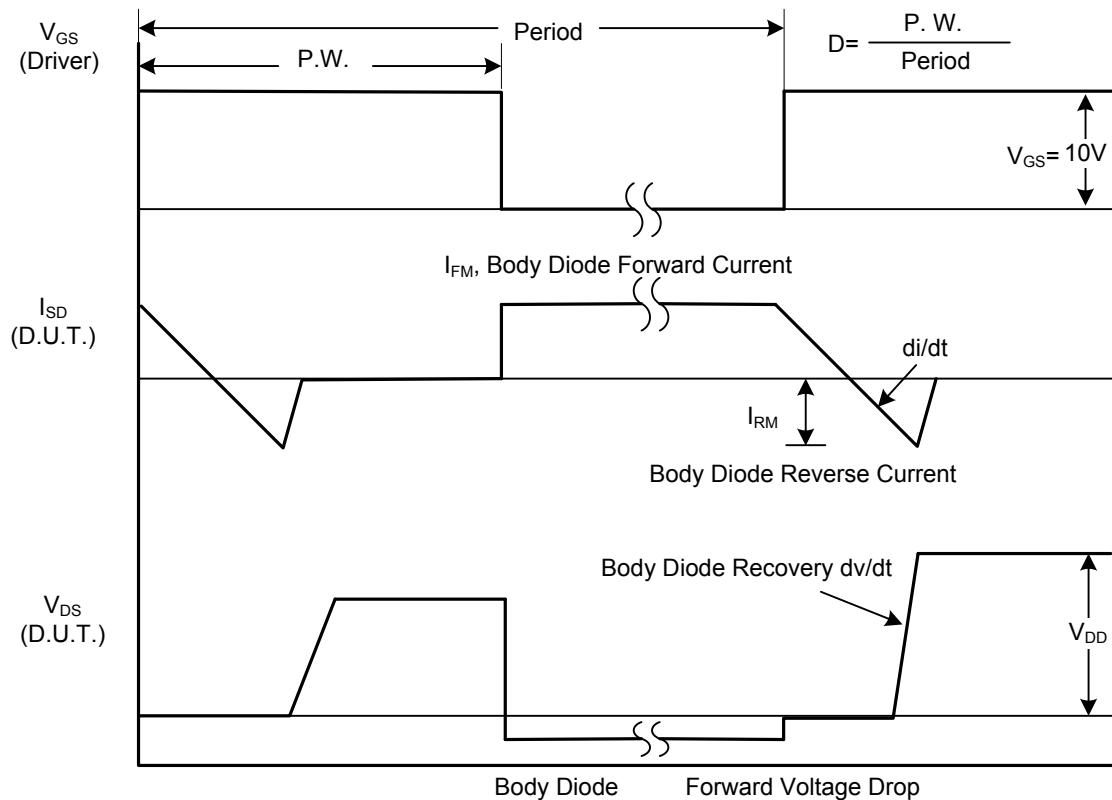
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

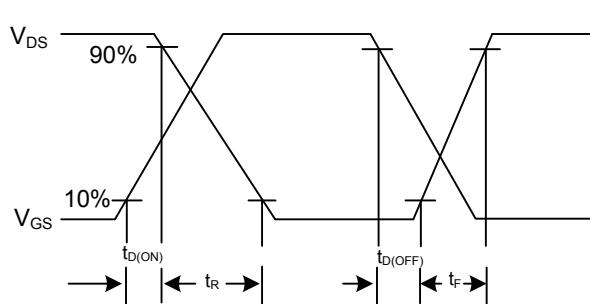
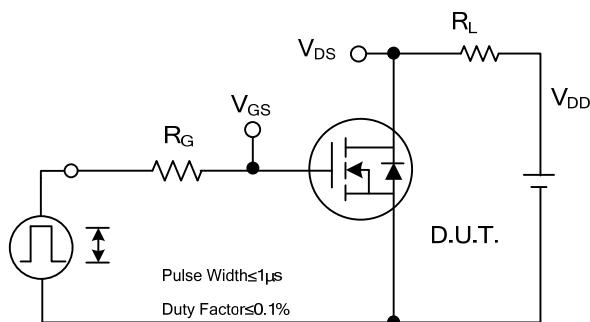


Peak Diode Recovery  $dv/dt$  Test Circuit



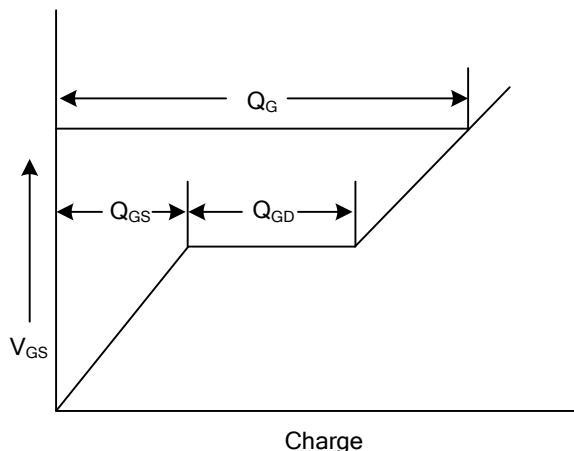
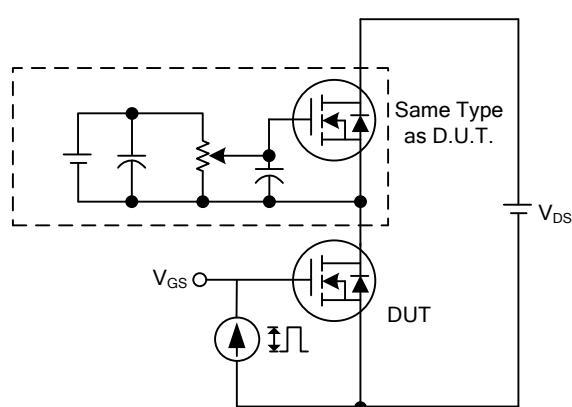
Peak Diode Recovery  $dv/dt$  Waveforms

### ■ TEST CIRCUITS AND WAVEFORMS



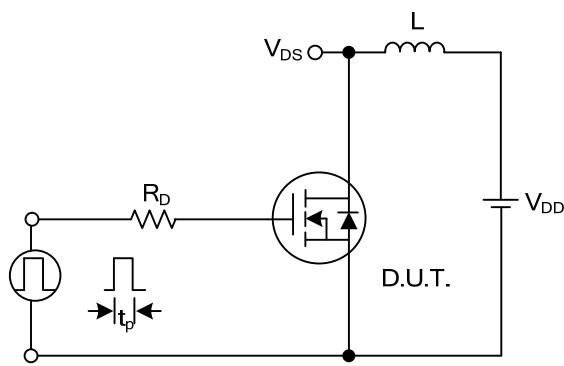
**Switching Test Circuit**

**Switching Waveforms**

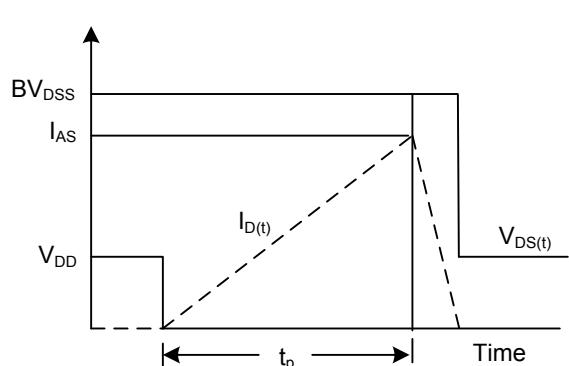


**Gate Charge Test Circuit**

**Gate Charge Waveform**

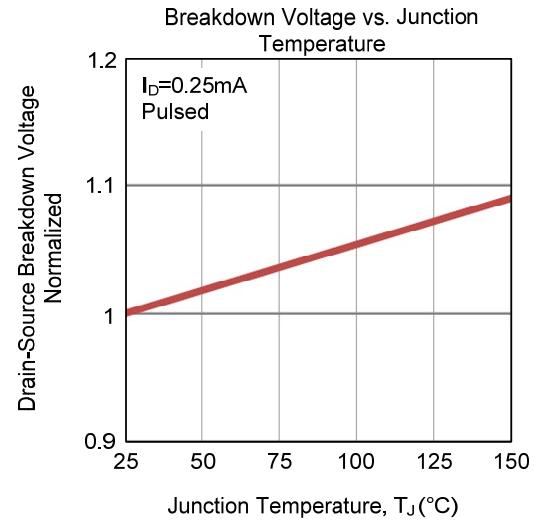
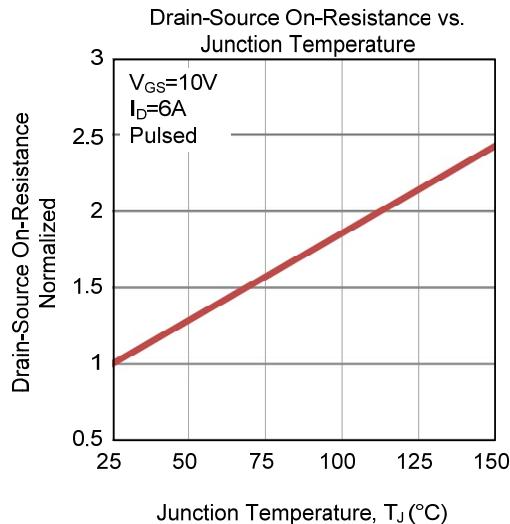
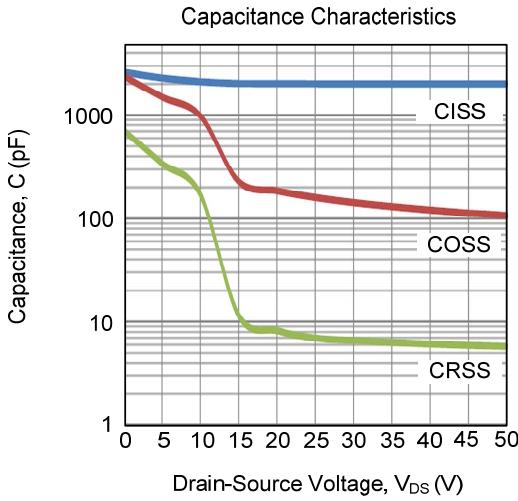
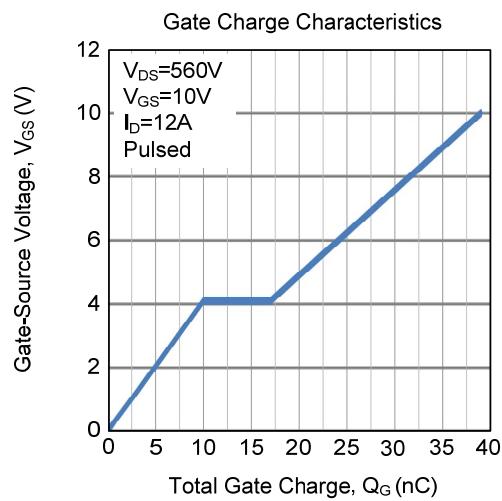
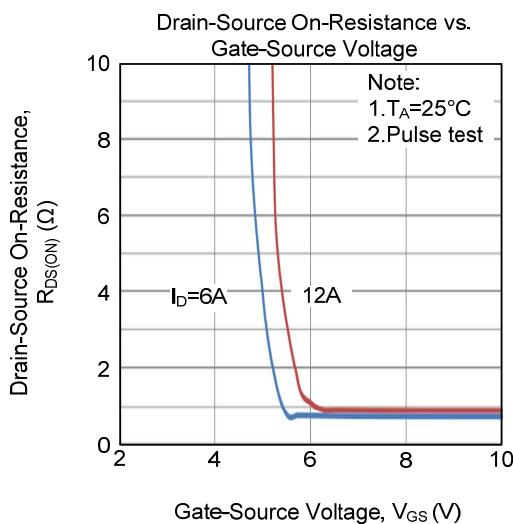
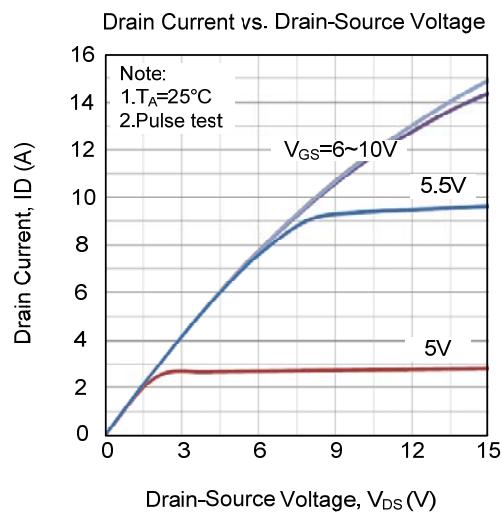


**Unclamped Inductive Switching Test Circuit**

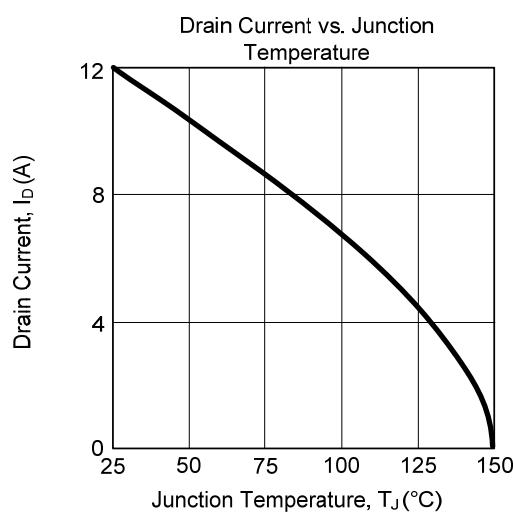
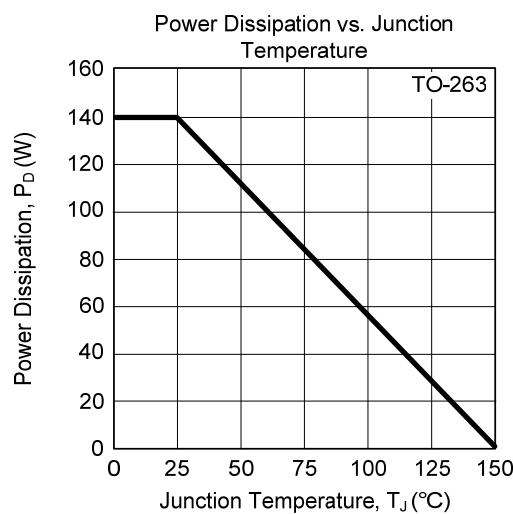
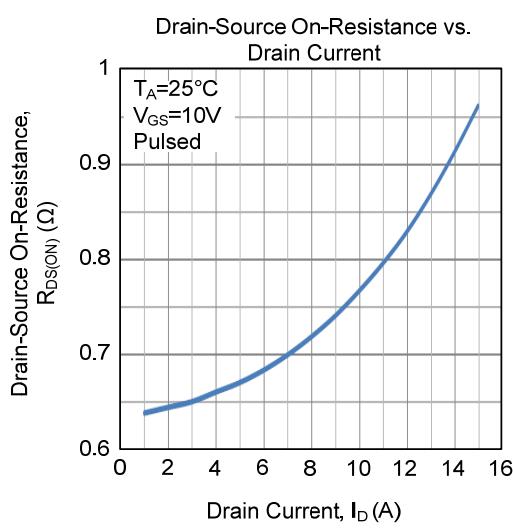
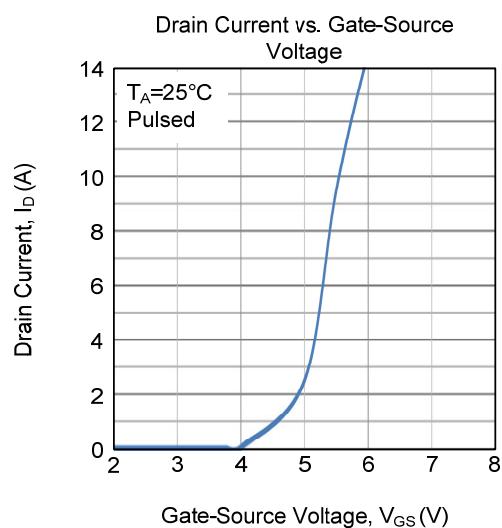
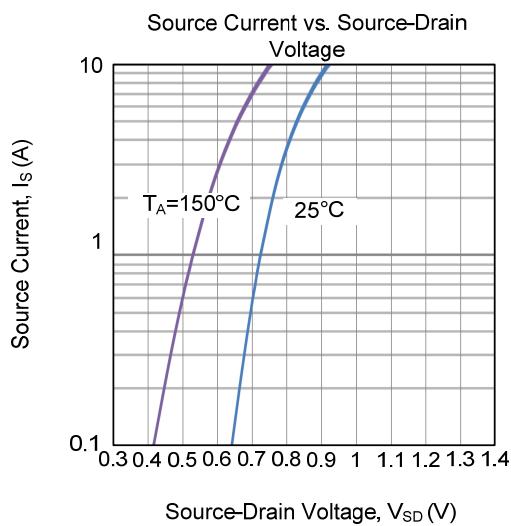
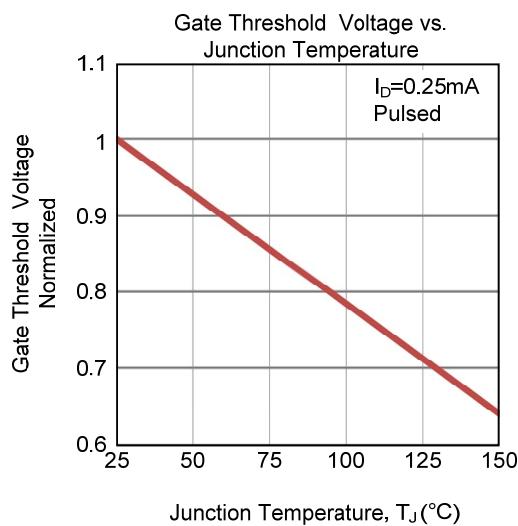


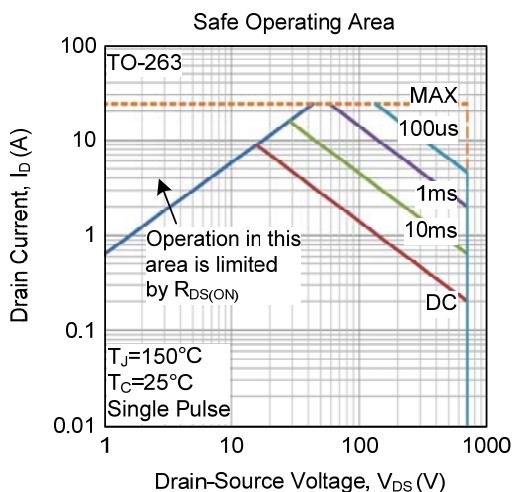
**Unclamped Inductive Switching Waveforms**

■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



**■ TYPICAL CHARACTERISTICS (Cont.)**

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