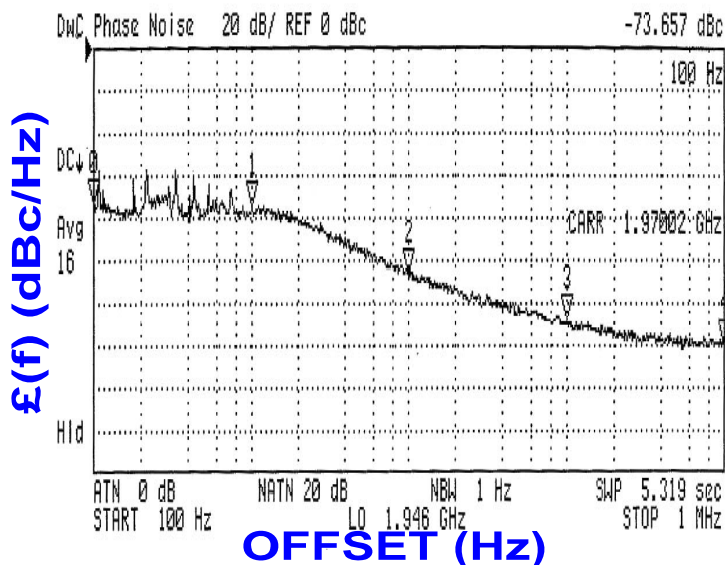




PHASE NOISE (1 Hz BW, typical)



FEATURES

- Frequency Range: 1970 - 2030 MHz
- Step Size: 100 KHz
- PLL-24H - Style Package

APPLICATIONS

- Telecommunications
- Satellite
- Telemetry

PERFORMANCE SPECIFICATIONS

	VALUE	UNITS
Frequency Range	1970 - 2030	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-107	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	2±2.5	dBm
Load Impedance	50	Ω
Step Size	100	KHz
Charge Pump Output Current	5000	μ A
Switching Speed (typ., adjacent channel)	2.5	mSec
Startup Lock Time (typ.)	2.5	mSec
Operating Temperature Range	-40 to 85	$^{\circ}$ C
Package Style	PLL-24H	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	25	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

NOTES:

Reference Oscillator Signal: 5 MHz < f_{osc} < 100 MHz
 Frequency Synthesizer: Analog Devices - ADF4106

Prescaler: 32

