



300mA CMOS LDO Regulator

FEATURES

- Guaranteed 300mA output current
- Low dropout voltage of 180mV typical at 300mA
- Stable with 1 μ F ceramic output capacitor
- External 10nF bypass capacitor for low noise
- Quick-start feature
- No-load ground current of 55 μ A typical
- Full-load ground current of 80 μ A typical
- $\pm 1.0\%$ output voltage initial accuracy
- $\pm 2.0\%$ accuracy over temperature
- “Zero” current shutdown mode
- Fold-back current limit and under-voltage lockout
- Thermal protection
- Thin SOT23-5 package

APPLICATIONS

- Cellular phones
- Battery-powered devices
- Consumer Electronics

For Ordering Information details, see page 9.

DESCRIPTION

The CAT6218 is a 300mA CMOS low dropout regulator that provides fast response time during load current and line voltage changes.

The quick-start feature allows the use of an external bypass capacitor to reduce the overall output noise without affecting the turn-on time of just 150 μ s.

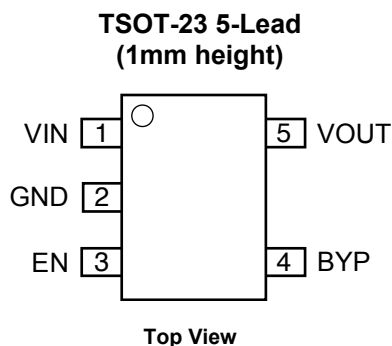
With zero shutdown current and low ground current of 55 μ A typical, the CAT6218 is ideal for battery-operated devices with supply voltages from 2.3V to 5.5V. An internal under voltage lockout circuit disables the output at supply voltages under 2.1V typical.

The CAT6218 offers 1% initial accuracy and low dropout voltage, 180mV typical at 300mA. Stable operation is provided with a small value ceramic capacitor, reducing required board space and component cost.

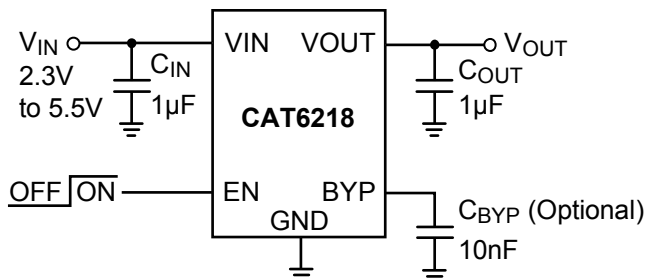
Other features include fold-back current limit and thermal protection.

The device is available in the low profile (1mm max height) 5-lead thin SOT23 package.

PIN CONFIGURATION



TYPICAL APPLICATION CIRCUIT



PIN DESCRIPTIONS

Pin #	Name	Function
1	V _{IN}	Supply voltage input.
2	GND	Ground reference.
3	EN	Enable input (active high); a 2.5MΩ pull-down resistor is provided.
4	BYP	Optional bypass capacitor connection for noise reduction and PSRR enhancing.
5	V _{OUT}	LDO Output Voltage.

BLOCK DIAGRAM

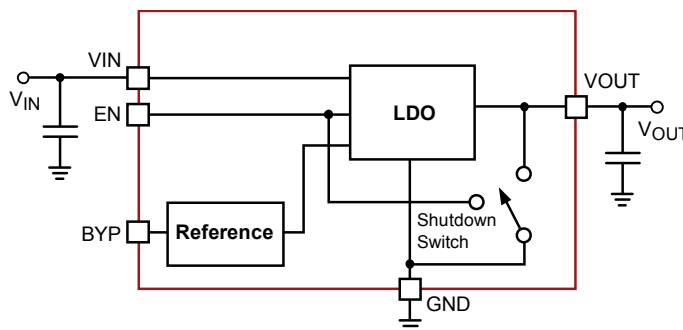


Figure 2. CAT6218 Functional Block Diagram

PIN FUNCTION

V_{IN} is the supply pin for the LDO. A small 1μF ceramic bypass capacitor is required between the V_{IN} pin and ground near the device. When using longer connections to the power supply, C_{IN} value can be increased without limit. The operating input voltage range is from 2.3V to 5.5V.

EN is the enable control logic (active high) for the regulator output. It has a 2.5MΩ pull-down resistor, which assures that if EN pin is left open, the circuit is disabled.

V_{OUT} is the LDO regulator output. A small 1μF ceramic bypass capacitor is required between the V_{OUT} pin and ground for stability. For better transient response, its value can be increased to 4.7μF.

The capacitor should be located near the device. ESR domain is 5mΩ to 500mΩ. V_{OUT} can deliver a maximum guaranteed current of 300mA. For input-to-output voltages higher than 1V, a continuous 300mA output current might turn-on the thermal protection. A 250Ω internal shutdown switch discharges the output capacitor in the no-load condition.

GND is the ground reference for the LDO. The pin must be connected to the ground plane on the PCB.

BYP is the reference bypass pin. An optional 0.01μF capacitor can be connected between BYP pin and GND to reduce the output noise and enhance the PSRR at high frequency.

ABSOLUTE MAXIMUM RATINGS ⁽¹⁾

Parameter	Rating	Unit
V _{IN}	0 to 6.5	V
V _{EN} , V _{OUT}	-0.3 to V _{IN} + 0.3	V
Junction Temperature, T _J	+150	°C
Power Dissipation, P _D	Internally Limited ⁽²⁾	mW
Storage Temperature Range, T _S	-65 to +150	°C
Lead Temperature (soldering, 5 sec.)	260	°C
ESD Rating (Human Body Model)	3	kV

RECOMMENDED OPERATING CONDITIONS ⁽³⁾

Parameter	Range	Unit
V _{IN}	2.3 to 5.5	V
V _{EN}	0 to V _{IN}	V
Junction Temperature Range, T _J	-40 to +125	°C
Package Thermal Resistance (SOT23-5), θ _{JA}	235	°C/W

Typical application circuit with external components is shown on page 1.

Notes:

- (1) Exceeding maximum rating may damage the device
- (2) The maximum allowable power dissipation at any T_A (ambient temperature) is $P_{Dmax} = (T_{Jmax} - T_A)/\theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
- (3) The device is not guaranteed to work outside its operating rating.

ELECTRICAL OPERATING CHARACTERISTICS ⁽¹⁾

$V_{IN} = V_{OUT} + 1.0V$, $V_{EN} = \text{High}$, $I_{OUT} = 100\mu A$, $C_{IN} = C_{OUT} = 1\mu F$, ambient temperature of 25°C (over recommended operating conditions unless specified otherwise). **Bold numbers** apply for the entire junction temperature range.

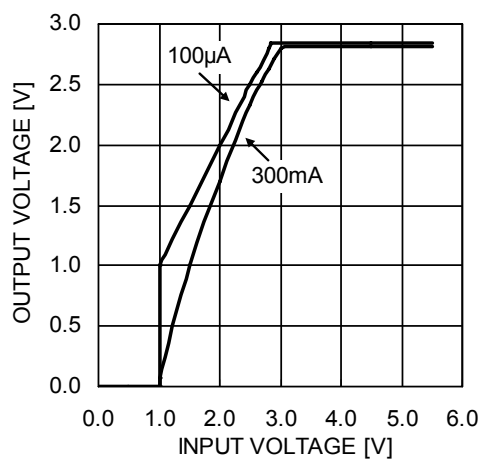
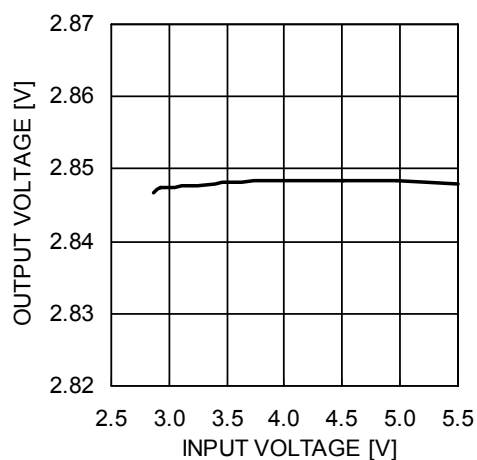
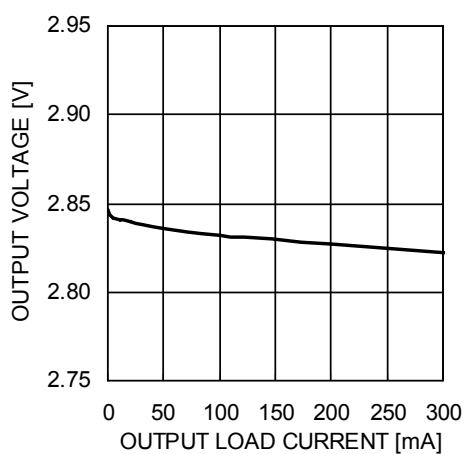
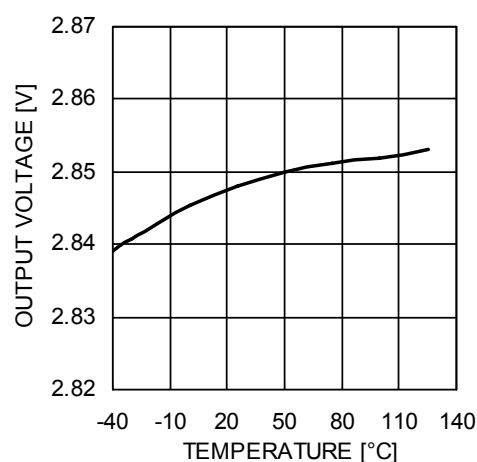
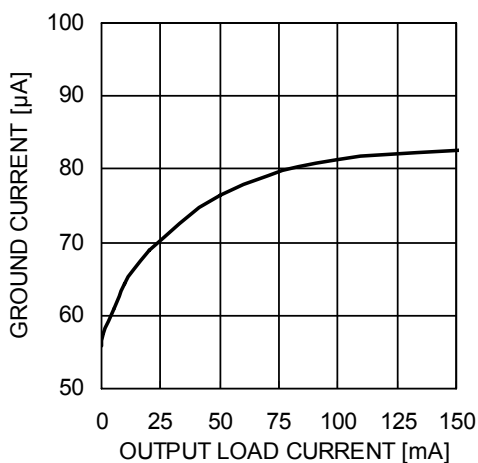
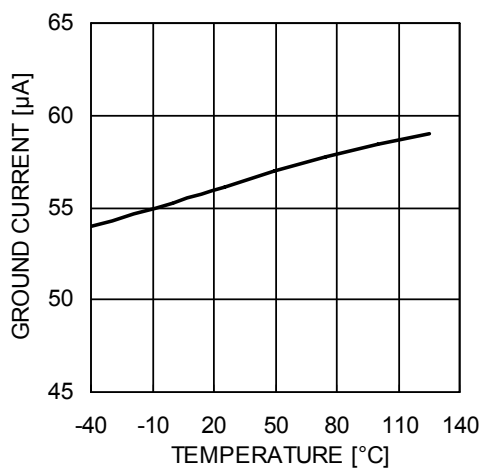
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{OUT-ACC}	Output Voltage Accuracy	Initial accuracy	-1.0		+1.0	%
			-2.0		+2.0	
TC _{OUT}	Output Voltage Temp. Coefficient			40		ppm/°C
V _{R-LINE}	Line Regulation	V _{IN} = V _{OUT} + 1.0V to 5.5V	-0.2	±0.1	+0.2	%/V
			-0.4		+0.4	
V _{R-LOAD}	Load Regulation	I _{OUT} = 100μA to 300 mA		0.7	1.2	%
					1.5	
V _{DROP}	Dropout Voltage ⁽²⁾	I _{OUT} = 300mA		180	250	mV
					300	
I _{GND}	Ground Current	I _{OUT} = 0μA		55	75	μA
					90	
		I _{OUT} = 300mA		80		
I _{GND-SD}	Shutdown Ground Current	V _{EN} < 0.4V			1	μA
					2	
PSRR	Power Supply Rejection Ratio	f = 1kHz, C _{BYP} = 10nF		64		dB
		f = 20kHz, C _{BYP} = 10nF		54		
I _{SC}	Output short circuit current limit	V _{OUT} = 0V		180		mA
T _{ON}	Turn-On Time	C _{BYP} = 10nF		150		μs
e _N	Output Noise Voltage ⁽³⁾	BW = 10Hz to 100kHz		45		μVrms
R _{OUT-SH}	Shutdown Switch Resistance			250		Ω
R _{EN}	Enable pull-down resistor			2.5		MΩ
V _{UVLO}	Under-voltage lock out (UVLO) threshold			2.1		V
ESR	C _{OUT} equivalent series resistance		5		500	mΩ
Enable Input						
V _{HI}	Logic High Level	V _{IN} = 2.3 to 5.5V	1.8			V
V _{LO}	Logic Low Level	V _{IN} = 2.3 to 5.5V			0.4	V
I _{EN}	Enable Input Current	V _{EN} = 0.4V		0.15	1	μA
		V _{EN} = V _{IN}		1.5	4	
Thermal Protection						
T _{SD}	Thermal Shutdown			160		°C
T _{HYS}	Thermal Hysteresis			10		°C

Notes:

- (1) Specification for 2.85V output version unless specified otherwise.
- (2) Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. During test, the input voltage stays always above the minimum 2.3V.
- (3) Specification for 1.8V output version.

TYPICAL CHARACTERISTICS (shown for 2.85V output version)

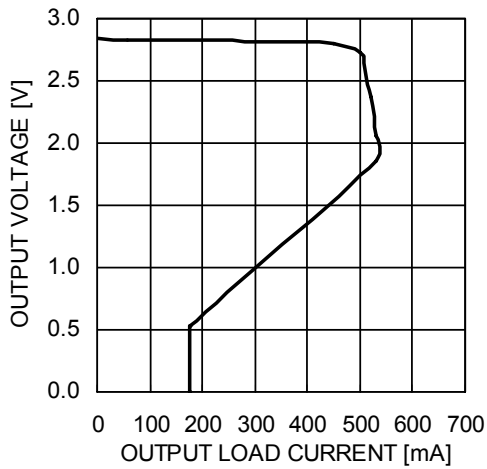
 $V_{IN} = 3.85V$, $I_{OUT} = 100\mu A$, $C_{IN} = C_{OUT} = 1\mu F$, $C_{BYP} = 10nF$, $T_A = 25^\circ C$ unless otherwise specified.

Dropout Characteristics

Line Regulation

Load Regulation

Output Voltage vs. Temperature

Ground Current vs. Load Current

Ground Current vs. Temperature


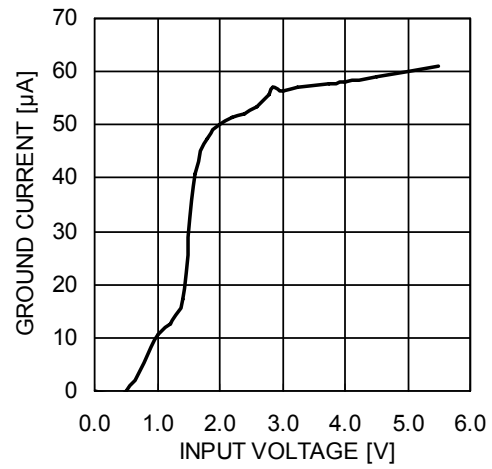
TYPICAL CHARACTERISTICS (shown for 2.85V output option)

$V_{IN} = 3.85V$, $I_{OUT} = 100\mu A$, $C_{IN} = C_{OUT} = 1\mu F$, $C_{BYP} = 10nF$, $T_A = 25^\circ C$ unless otherwise specified.

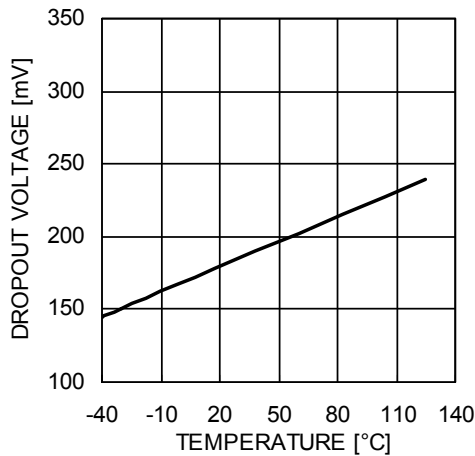
Output Voltage vs. Load Current



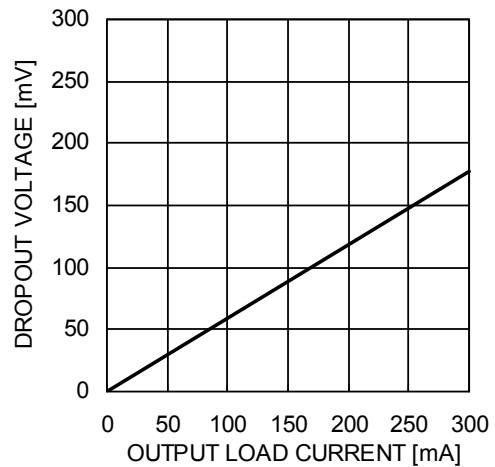
Ground Current vs. Input Voltage



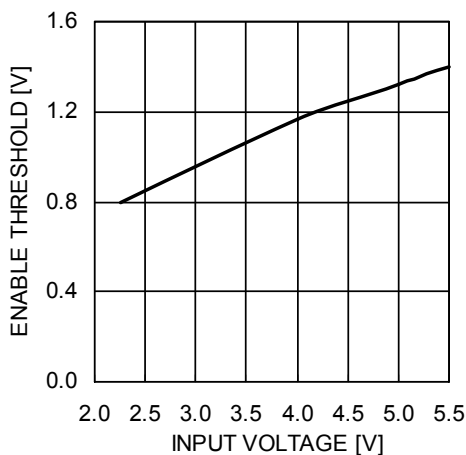
Dropout vs. Temperature (300mA Load)



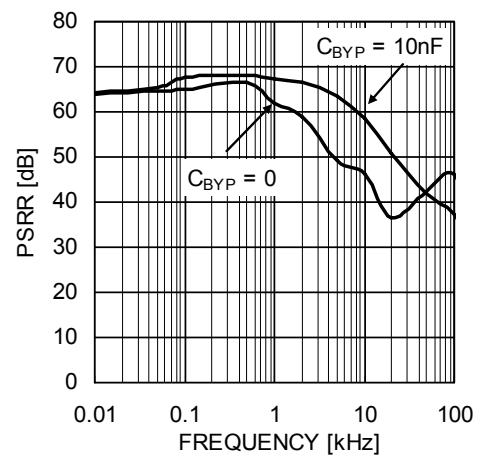
Dropout vs. Load Current



Enable Threshold vs. Input Voltage

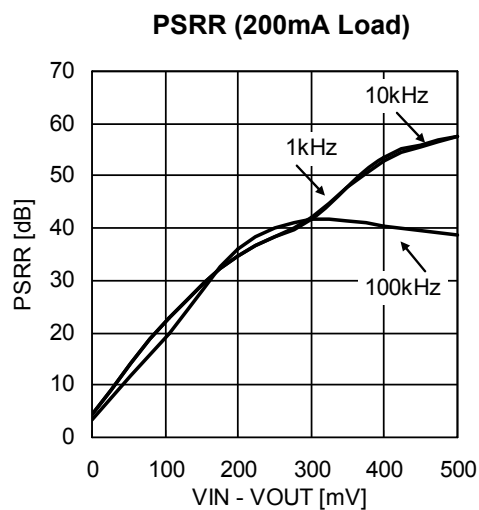
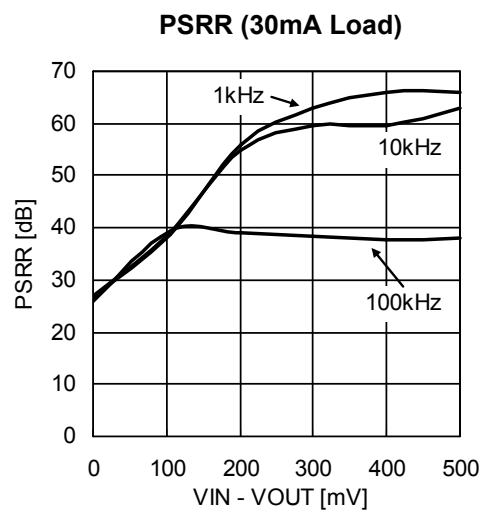


PSRR vs. Frequency (10mA Load)



TYPICAL CHARACTERISTICS (shown for 2.85V output option)

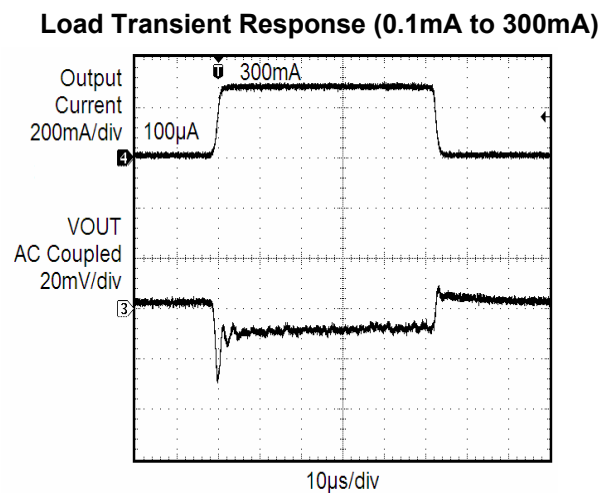
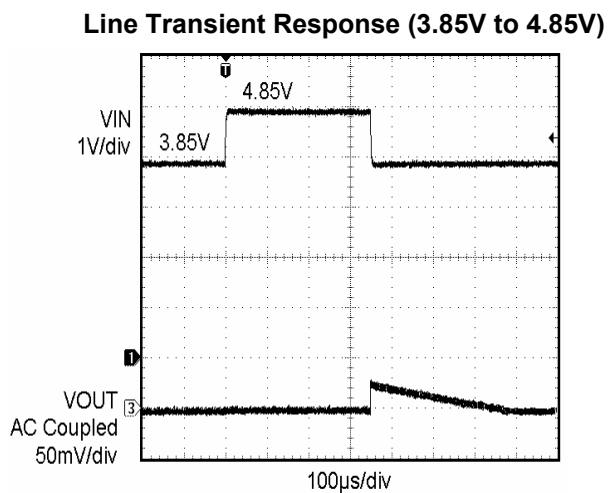
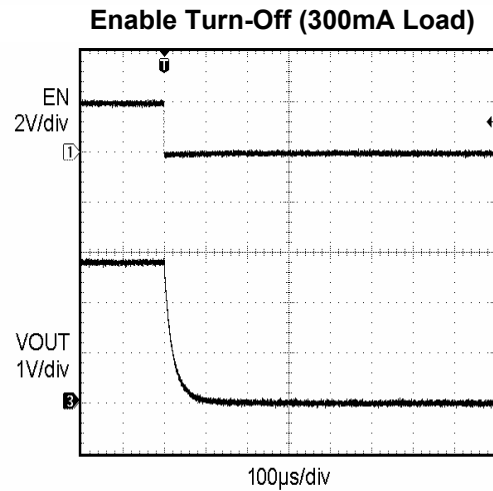
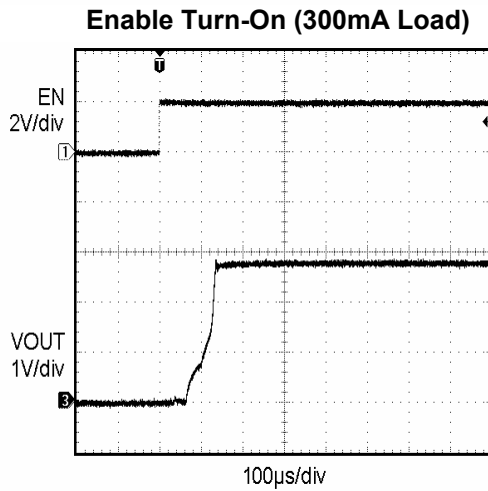
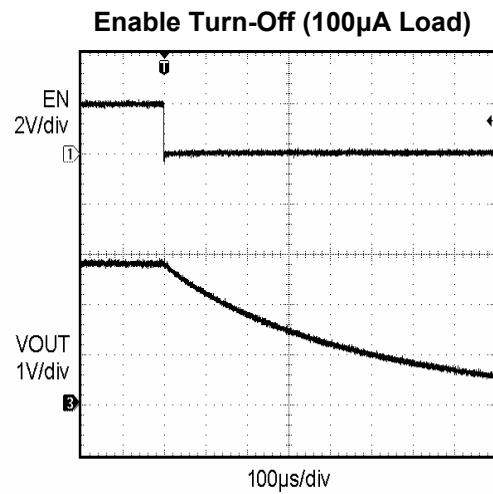
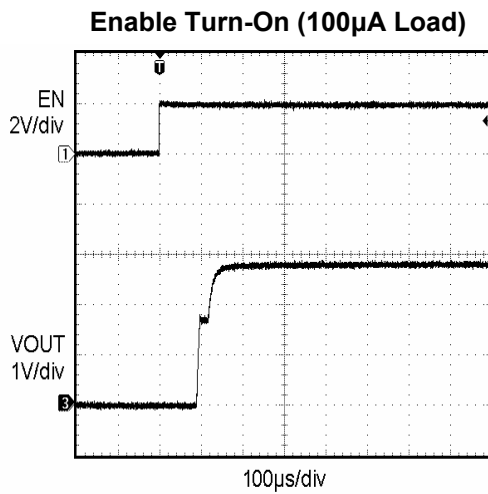
$V_{IN} = 3.85V$, $I_{OUT} = 100\mu A$, $C_{IN} = C_{OUT} = 1\mu F$, $C_{BYP} = 10nF$, $T_A = 25^\circ C$ unless otherwise specified.



TRANSIENT CHARACTERISTICS (shown for 2.85V output option)

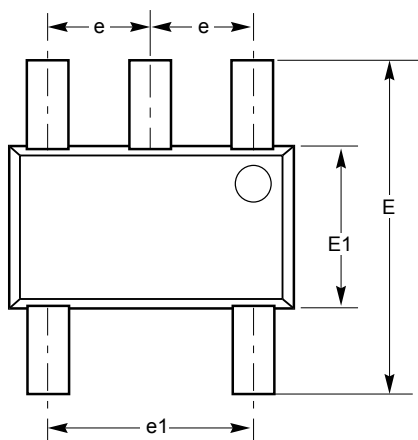
$V_{IN} = 3.85V$, $I_{OUT} = 100\mu A$, $C_{IN} = C_{OUT} = 1\mu F$, $C_{BYP} = 10nF$, $T_A = 25^\circ C$ unless otherwise specified.

Note: All transient characteristics are generated using the evaluation board CAT621XEVAL1.

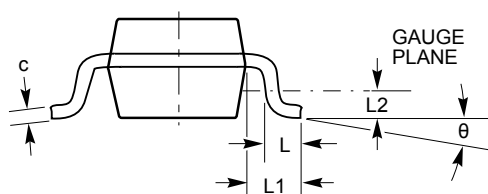
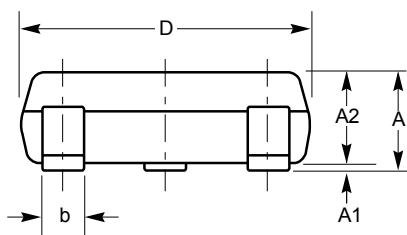


PACKAGE DIMENSIONS

5-LEAD TSOT-23 ⁽¹⁾⁽²⁾



SYMBOL	MIN	NOM	MAX
A	—	—	1.00
A1	0.01	0.05	0.10
A2	0.80	0.87	0.90
b	0.30	—	0.45
c	0.12	0.15	0.20
D	2.90 BSC		
E	2.80 BSC		
E1	1.60 BSC		
e	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.40	0.50
L1	0.60 REF		
L2	0.25 BSC		
θ	0°		8°

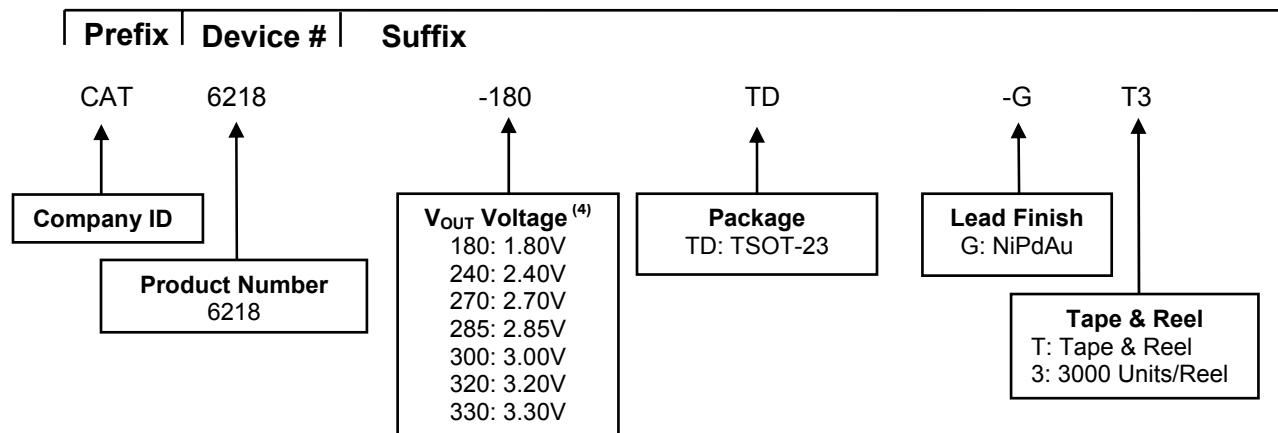


For current Tape and Reel information, download the PDF file from:
<http://www.catsemi.com/documents/tapeandreel.pdf>.

Notes:

- (1) All dimensions are in millimeters, angles in degrees.
- (2) Refer JEDEC MO-193.

EXAMPLE OF ORDERING INFORMATION



Part Number	V _{OUT} Voltage	Package	Quantity per Reel
CAT6218-180TD-GT3 ⁽⁴⁾	1.80V	TSOT-23	3000
CAT6218-240TD-GT3	2.40V	TSOT-23	3000
CAT6218-270TD-GT3	2.70V	TSOT-23	3000
CAT6218-285TD-GT3 ⁽⁴⁾	2.85V	TSOT-23	3000
CAT6218-300TD-GT3	3.00V	TSOT-23	3000
CAT6218-320TD-GT3 ⁽⁴⁾	3.20V	TSOT-23	3000
CAT6218-330TD-GT3	3.30V	TSOT-23	3000

Notes:

- (1) All packages are RoHS-compliant (Lead-free, Halogen-free).
- (2) The standard finish is NiPdAu.
- (3) The device used in the above example is a CAT6218-180 TD-GT3 (V_{OUT} = 1.8V, in an TSOT-23 package, NiPdAu, Tape and Reel, 3000 units).
- (4) Standard voltages are 2.4V, 2.7V, 3.0V, and 3.3V. For other voltage options, please contact your nearest Catalyst Semiconductor Sales office.
- (5) Top marking for CAT6218 is RU.

REVISION HISTORY

Date	Rev.	Reason
06/19/2007	A	Preliminary Revision

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