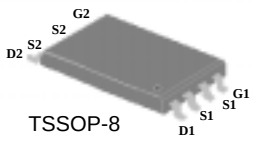
N-CHANNEL ENHANCEMENT-MODE POWER MOSFETS

- Low on-resistance
- Capable of 2.5V gate drive
- Low drive current
- Surface-mount package

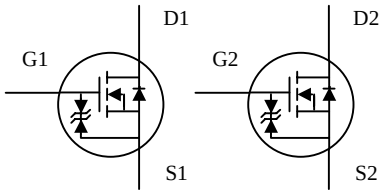


TSSOP-8

BV_{DSS}	20V
$R_{DS(ON)}$	28mΩ
I_D	4.6A

Description

Power MOSFETs from Silicon Standard provide the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost-effectiveness.



RoHS compliant.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D @ T_A=25°C	Continuous Drain Current[3]	4.6	A
I_D @ T_A=70°C	Continuous Drain Current[3]	3.7	A
I_{DM}	Pulsed Drain Current[1,2]	20	A
P_D @ T_A=25°C	Total Power Dissipation	1	W
	Linear Derating Factor	0.008	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter		Value	Unit
Rthj-amb	Thermal Resistance Junction-ambient	Max.	125	°C/W

Electrical Characteristics @ T_j=25°C (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	20	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I_D=1mA	-	0.1	-	V/°C
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=4.5V, I_D=4A	-	-	28	mΩ
		V_{GS}=2.5V, I_D=2A	-	-	40	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	0.5	-	-	V
g_{fs}	Forward Transconductance	V_{DS}=10V, I_D=4.6A	-	9.7	-	S
I_{DSS}	Drain-Source Leakage Current (T_j=25°C)	V_{DS}=20V, V_{GS}=0V	-	-	1	uA
	Drain-Source Leakage Current (T_j=70°C)	V_{DS}=20V ,V_{GS}=0V	-	-	25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}= ± 10 V	-	-	± 10	uA
Q_g	Total Gate Charge[2]	I_D=4.6A	-	12.5	-	nC
Q_{gs}	Gate-Source Charge	V_{DS}=20V	-	1	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=5V	-	6.5	-	nC
$t_{d(on)}$	Turn-on Delay Time[2]	V_{DS}=10V	-	5	-	ns
t_r	Rise Time	I_D=1A	-	9	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω,V_{GS}=5V	-	26.2	-	ns
t_f	Fall Time	R_D=10Ω	-	6.8	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	355	-	pF
C_{oss}	Output Capacitance	V_{DS}=20V	-	190	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	85	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I_S	Continuous Source Current (Body Diode)	V_D=V_G=0V,V_S=1.2V	-	-	1.25	A
I_{SM}	Pulsed Source Current (Body Diode)[1]		-	-	20	A
V_{SD}	Forward On Voltage[2]	T_j=25°C,I_S=1.25A,V_{GS}=0V	-	-	1.2	V

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse width ≤300us , duty cycle ≤2%.

3.Surface mounted on FR4 board, t≤10 sec.

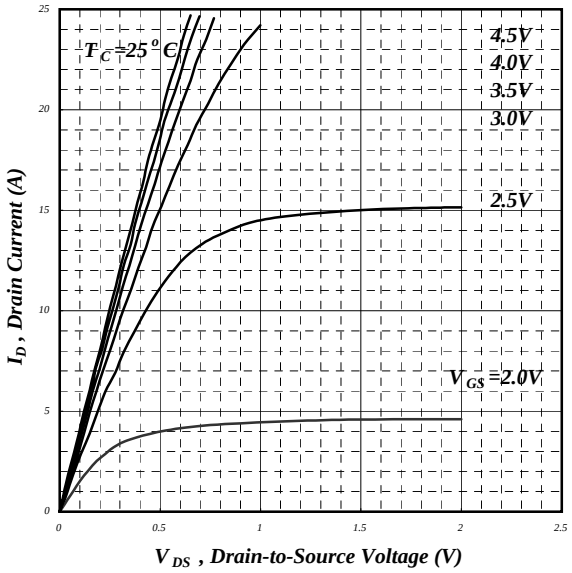


Fig 1. Typical Output Characteristics

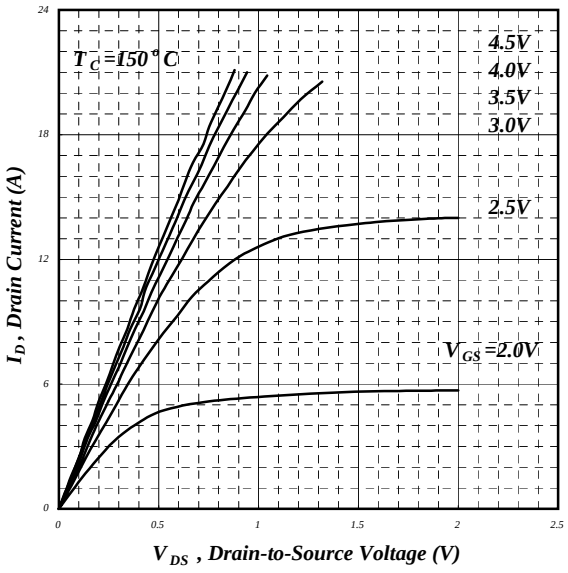


Fig 2. Typical Output Characteristics

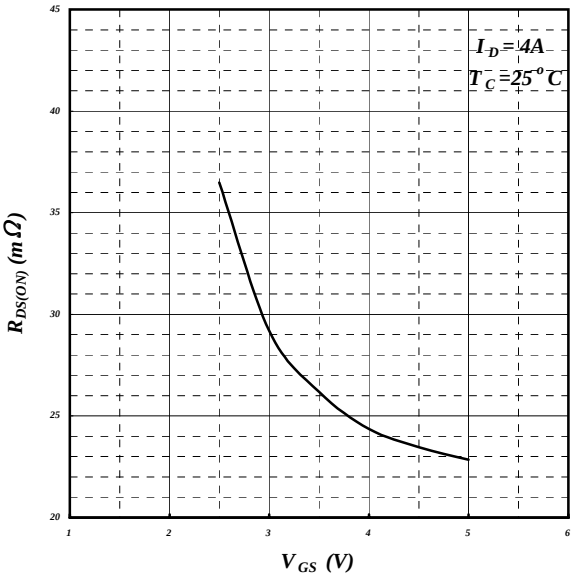


Fig 3. On-Resistance v.s. Gate Voltage

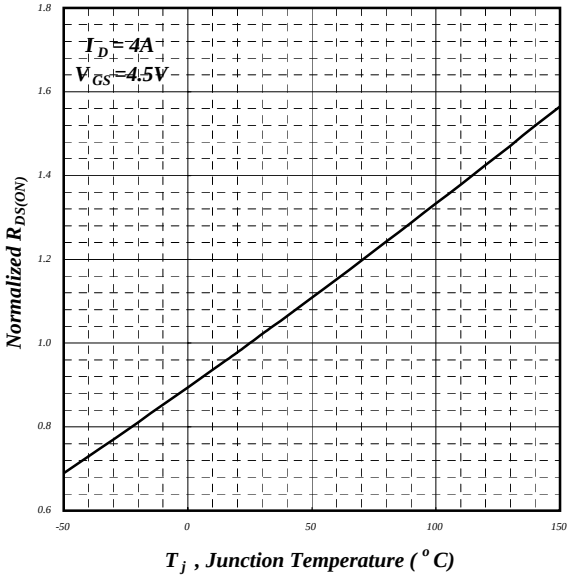


Fig 4. Normalized On-Resistance vs. Junction Temperature

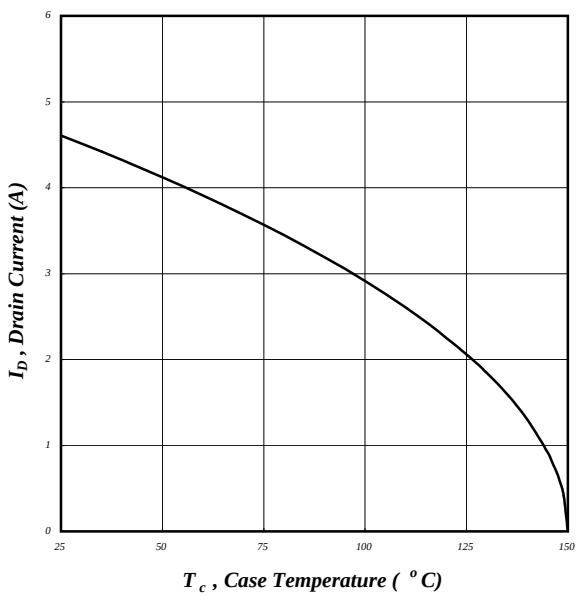


Fig 5. Maximum Drain Current v.s. Case Temperature

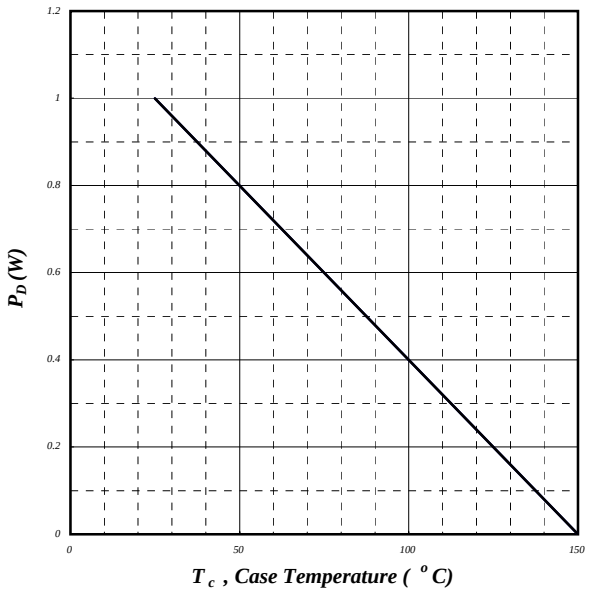


Fig 6. Typical Power Dissipation

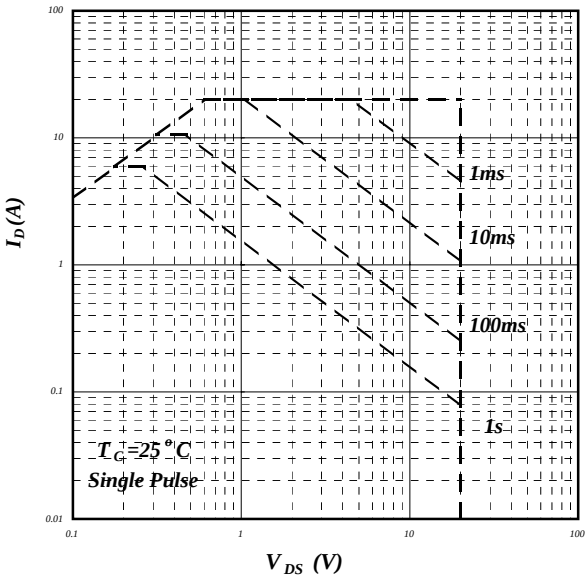


Fig 7. Maximum Safe Operating Area

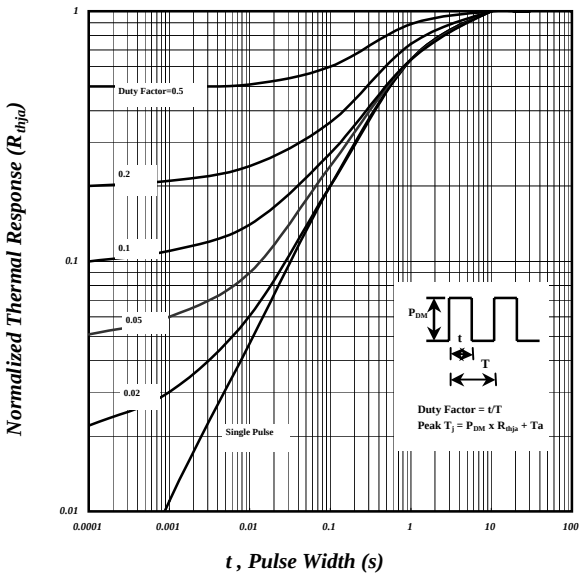


Fig 8. Effective Transient Thermal Impedance

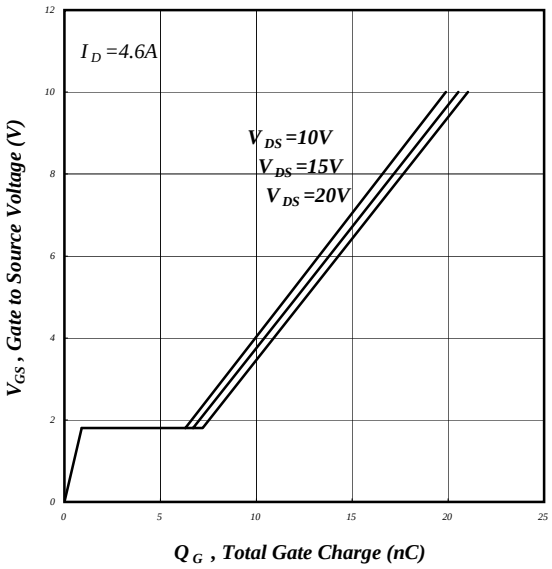


Fig 9. Gate Charge Characteristics

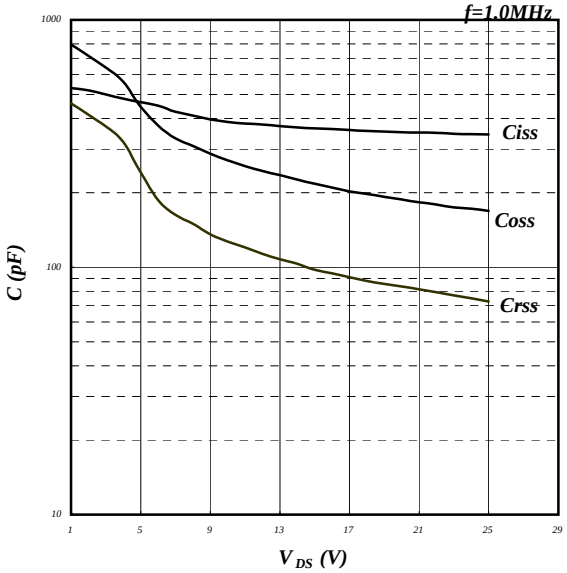


Fig 10. Typical Capacitance Characteristics

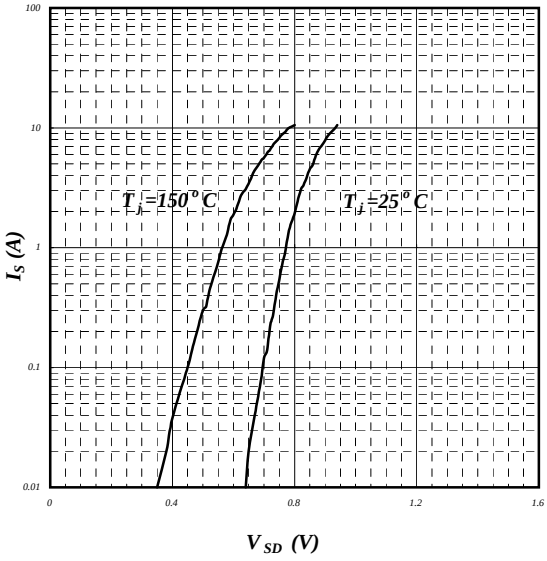


Fig 11. Forward Characteristic of Reverse Diode

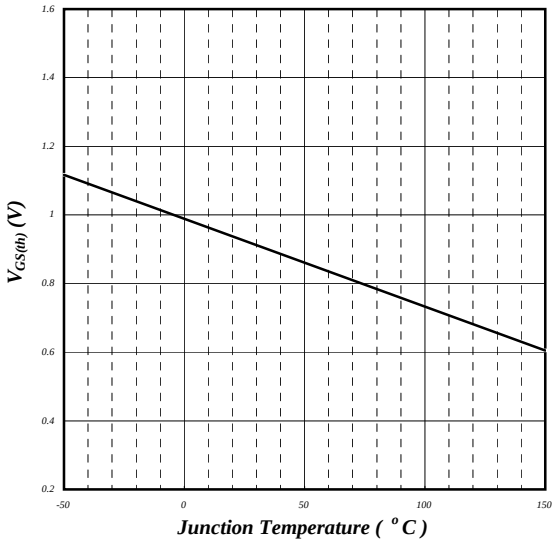


Fig 12. Gate Threshold Voltage vs. Junction Temperature

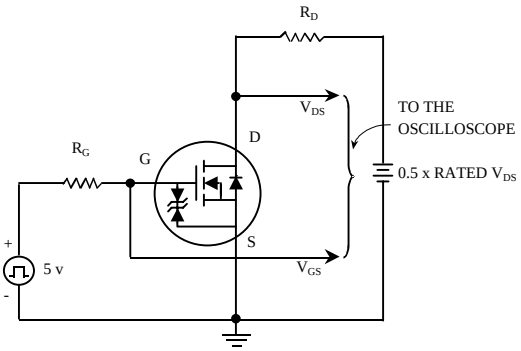


Fig 13. Switching Time Circuit

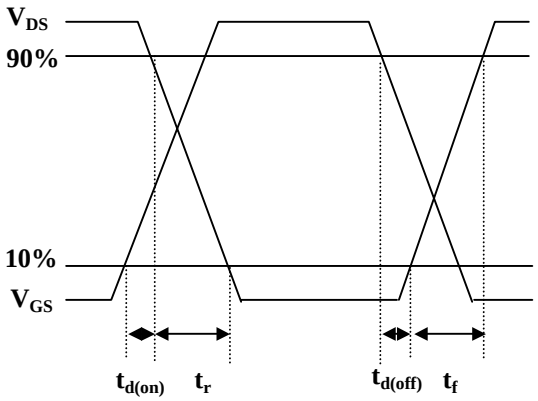


Fig 14. Switching Time Waveform

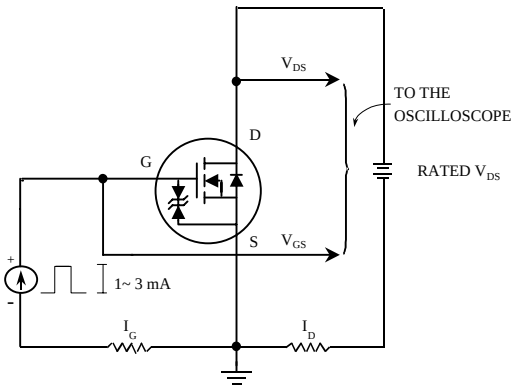


Fig 15. Gate Charge Circuit

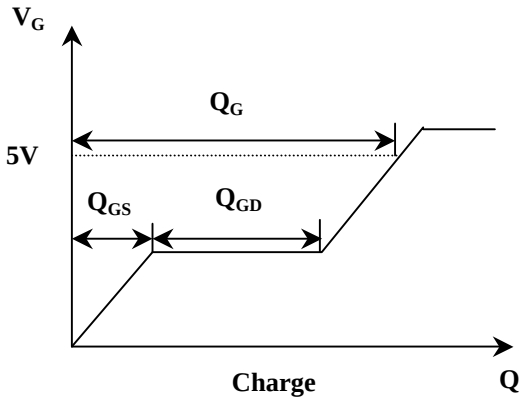


Fig 16. Gate Charge Waveform

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