- Operating Current Range ... $20 \mu \mathrm{~A}$ to 20 mA
- $1.5 \%$ and $3 \%$ Initial Voltage Tolerance
- Reference Impedance
- LM385 . . $1 \Omega$ Max at $25^{\circ} \mathrm{C}$
- All Devices . . . $1.5 \Omega$ Max Over Full Temperature Range
- Very Low Power Consumption
- Applications
- Portable Meter References
- Portable Test Instruments
- Battery-Operated Systems
- Current-Loop Instrumentation
- Panel Meters
- Interchangeable With Industry-Standard LM285-2.5 and LM385-2.5


## description/ordering information

These micropower two-terminal band-gap voltage references operate over a $20-\mu \mathrm{A}$ to $20-\mathrm{mA}$ current range and feature exceptionally low dynamic impedance and good temperature stability. On-chip trimming provides tight voltage tolerance. The band-gap reference for these devices has low noise and long-term stability.

ORDERING INFORMATION

| $\mathrm{T}_{\mathbf{A}}$ | $\mathrm{V}_{\mathrm{Z}}$ <br> TOLERANCE | PACKAGE $\dagger$ |  | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $0^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ | 3\% | SOIC (D) | Tube of 75 | LM385D-2-5 | 385-25 |
|  |  |  | Reel of 2000 | LM385DR-2-5 |  |
|  |  | TO226/TO-92 (LP) | Tube of 1000 | LM385LP-2-5 | 385-25 |
|  |  |  | Reel of 2000 | LM385LPR-2-5 |  |
|  |  | TSSOP (PW) | Tube of 150 | LM385PW-2-5 | 385-25 |
|  |  |  | Reel of 2000 | LM385PWR-2-5 |  |
|  | 1.5\% | SOIC (D) | Tube of 75 | LM385BD-2-5 | 385B25 |
|  |  |  | Reel of 2000 | LM385BDR-2-5 |  |
|  |  | TO226/TO-92 (LP) | Tube of 1000 | LM385BLP-2-5 | 385-25 |
|  |  |  | Reel of 2000 | LM385BLPR-2-5 |  |
|  |  | TSSOP (PW) | Tube of 150 | LM385BPW-2-5 | 385B25 |
|  |  |  | Reel of 2000 | LM385BPWR-2-5 |  |
| $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ | 1.5\% | SOIC (D) | Tube of 75 | LM285D-2-5 | 285-25 |
|  |  |  | Reel of 2000 | LM285DR-2-5 |  |
|  |  | TO226/TO-92 (LP) | Tube of 1000 | LM285LP-2-5 | 285-25 |
|  |  |  | Reel of 2000 | LM285LPR-2-5 | 285-25 |

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

## LM285-2.5, LM385-2.5, LM385B-2.5

## MICROPOWER VOLTAGE REFERENCES

SLVS023J - JANUARY 1989 - REVISED MARCH 2005

## description/ordering information (continued)

The design makes these devices exceptionally tolerant of capacitive loading and, thus, easier to use in most reference applications. The wide dynamic operating temperature range accommodates varying current supplies, with excellent regulation.

The extremely low power drain of this series makes them useful for micropower circuitry. These voltage references can be used to make portable meters, regulators, or general-purpose analog circuitry, with battery life approaching shelf life. The wide operating current range allows them to replace older references with tighter-tolerance parts.

## symbol


schematic


NOTE A: All component values shown are nominal.

# LM285-2.5, LM385-2.5, LM385B-2.5 MICROPOWER VOLTAGE REFERENCES 

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted) $\dagger$




LP package ............................... 140… $14{ }^{\circ}$ W
PW package .............................. 149 ${ }^{\circ} \mathrm{C} / \mathrm{W}$
Lead temperature $1,6 \mathrm{~mm}(1 / 16 \mathrm{inch})$ from case for 10 seconds .................................... $260^{\circ} \mathrm{C}$

$\dagger$ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTES: 1. Maximum power dissipation is a function of $\mathrm{T}_{\mathrm{J}_{(\max )},} \theta_{\mathrm{JA}}$, and $\mathrm{T}_{\mathrm{A}}$. The maximum allowable power dissipation at any allowable ambient temperature is $P_{D}=\left(T_{J(\max )}-T_{A}\right) / \theta_{J A}$. Operation at the absolute maximum $T_{J}$ of $150^{\circ} \mathrm{C}$ can affect reliability.
2. The package thermal impedance is calculated in accordance with JESD 51-7.
recommended operating conditions


## electrical characteristics at specified free-air temperature

| PARAMETER |  | TEST CONDITIONS | $\mathrm{T}_{\mathrm{A}}{ }^{\ddagger}$ | LM285-2.5 |  |  | LM385-2.5 |  |  | LM385B-2.5 |  |  | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | MIN |  | TYP | MAX | MIN | TYP | MAX | MIN | TYP | MAX |  |
| $V_{Z}$ | Reference voltage |  | $\begin{aligned} & \mathrm{I} \mathrm{Z}=20 \mu \mathrm{~A} \\ & \text { to } 20 \mathrm{~mA} \end{aligned}$ | $25^{\circ} \mathrm{C}$ | 2.462 | 2.5 | 2.538 | 2.425 | 2.5 | 2.575 | 2.462 | 2.5 | 2.538 | V |
| $\alpha$ VZ | Average temperature coefficient of reference voltage§ | $\begin{aligned} & \mathrm{IZ}=20 \mu \mathrm{~A} \\ & \text { to } 20 \mathrm{~mA} \end{aligned}$ | Full range |  | $\pm 20$ |  |  | $\pm 20$ |  |  | $\pm 20$ |  | ppm $/{ }^{\circ} \mathrm{C}$ |
| $\Delta \mathrm{V}_{\mathrm{Z}}$ | Change in reference voltage with current | $\begin{aligned} & \mathrm{IZ}=20 \mu \mathrm{~A} \\ & \text { to } 1 \mathrm{~mA} \end{aligned}$ | $25^{\circ} \mathrm{C}$ |  |  | 1 |  |  | 2 |  |  | 2 | mV |
|  |  |  | Full range |  |  | 1.5 |  |  | 2 |  |  | 2 |  |
|  |  | $\begin{aligned} & I Z=1 \mathrm{~mA} \\ & \text { to } 20 \mathrm{~mA} \end{aligned}$ | $25^{\circ} \mathrm{C}$ |  |  | 10 |  |  | 20 |  |  | 20 |  |
|  |  |  | Full range |  |  | 30 |  |  | 30 |  |  | 30 |  |
| $\Delta \mathrm{V}_{\mathrm{Z}} / \Delta \mathrm{t}$ | Long-term change in reference voltage | $\mathrm{I} \mathrm{Z}=100 \mu \mathrm{~A}$ | $25^{\circ} \mathrm{C}$ |  | $\pm 20$ |  |  | $\pm 20$ |  |  | $\pm 20$ |  | ppm/khr |
| I (min) | Minimum reference current |  | Full range |  | 8 | 20 |  | 8 | 20 |  | 8 | 20 | $\mu \mathrm{A}$ |
| $\mathrm{z}_{\mathrm{z}}$ | Reference impedance | $\mathrm{I}=100 \mu \mathrm{~A}$ | $25^{\circ} \mathrm{C}$ |  | 0.2 | 0.6 |  | 0.4 | 1 |  | 0.4 | 1 | $\Omega$ |
|  |  |  | Full range |  |  | 1.5 |  |  | 1.5 |  |  | 1.5 |  |
| $\mathrm{V}_{\mathrm{n}}$ | Broadband noise voltage | $\begin{aligned} & \mathrm{IZ}=100 \mu \mathrm{~A}, \\ & \mathrm{f}=10 \mathrm{~Hz} \\ & \text { to } 10 \mathrm{kHz} \end{aligned}$ | $25^{\circ} \mathrm{C}$ |  | 120 |  |  | 120 |  |  | 120 |  | $\mu \mathrm{V}$ |

[^0]
## TYPICAL CHARACTERISTICS $\dagger$



Figure 1


Figure 3

Figure 2


Figure 4
† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

## TYPICAL CHARACTERISTICS $\dagger$



Figure 5


Figure 7

REFERENCE IMPEDANCE
vs
FREQUENCY


Figure 6

FILTERED RMS OUTPUT NOISE VOLTAGE
vs
FREQUENCY


Figure 8

[^1]
## TYPICAL CHARACTERISTICS $\dagger$



Figure 9

APPLICATION INFORMATION

† Adjust for 12.17 mV at $25^{\circ} \mathrm{C}$ across $412 \Omega$
Figure 10. Thermocouple Cold-Junction Compensator


Figure 11. Operation Over a Wide Supply Range


Figure 12. Reference From a 9-V Battery

## PACKAGE OPTION ADDENDUM

www.ti.com

## PACKAGING INFORMATION

| Orderable Device | Status <br> (1) | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <br> (2) | Lead/Ball Finish <br> (6) | MSL Peak Temp <br> (3) | Op Temp ( ${ }^{\circ} \mathrm{C}$ ) | Device Marking (4/5) | Samples |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| LM285D-2-5 | ACTIVE | SOIC | D | 8 | 75 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | -40 to 85 | 285-25 | Samples |
| LM285DG4-2-5 | ACTIVE | SOIC | D | 8 | 75 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | -40 to 85 | 285-25 | Samples |
| LM285DR-2-5 | ACTIVE | SOIC | D | 8 | 2500 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | -40 to 85 | 285-25 | Samples |
| LM285DRG4-2-5 | ACTIVE | SOIC | D | 8 | 2500 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | -40 to 85 | 285-25 | Samples |
| LM285LP-2-5 | ACTIVE | TO-92 | LP | 3 | 1000 | Pb-Free (RoHS) | CU SN | N / A for Pkg Type | -40 to 85 | 285-25 | Samples |
| LM285LPE3-2-5 | ACTIVE | TO-92 | LP | 3 | 1000 | Pb-Free (RoHS) | CU SN | N/ A for Pkg Type | -40 to 85 | 285-25 | Samples |
| LM285LPR-2-5 | ACTIVE | TO-92 | LP | 3 | 2000 | Pb-Free <br> (RoHS) | CU SN | N / A for Pkg Type | -40 to 85 | 285-25 | Samples |
| LM285LPRE3-2-5 | ACTIVE | TO-92 | LP | 3 | 2000 | Pb-Free <br> (RoHS) | CU SN | N/ A for Pkg Type | -40 to 85 | 285-25 | Samples |
| LM385BD-2-5 | ACTIVE | SOIC | D | 8 | 75 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385B25 | Samples |
| LM385BDE4-2-5 | ACTIVE | SOIC | D | 8 | 75 | Green (RoHS \& no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385B25 | Samples |
| LM385BDR-2-5 | ACTIVE | SOIC | D | 8 | 2500 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385B25 | Samples |
| LM385BDRE4-2-5 | ACTIVE | SOIC | D | 8 | 2500 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385B25 | Samples |
| LM385BLP-2-5 | ACTIVE | TO-92 | LP | 3 | 1000 | Pb-Free (RoHS) | CU SN | N/ A for Pkg Type | 0 to 70 | 385B25 | Samples |
| LM385BLPE3-2-5 | ACTIVE | TO-92 | LP | 3 | 1000 | Pb-Free (RoHS) | CU SN | N / A for Pkg Type | 0 to 70 | 385B25 | Samples |
| LM385BLPR-2-5 | ACTIVE | TO-92 | LP | 3 | 2000 | Pb-Free (RoHS) | CU SN | N / A for Pkg Type | 0 to 70 | 385B25 | Samples |
| LM385BPWR-2-5 | ACTIVE | TSSOP | PW | 8 | 2000 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385B25 | Samples |
| LM385D-2-5 | ACTIVE | SOIC | D | 8 | 75 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385-25 | Samples |


| Orderable Device | Status (1) | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <br> (2) | Lead/Ball Finish <br> (6) | MSL Peak Temp <br> (3) | Op Temp ( ${ }^{\circ} \mathrm{C}$ ) | Device Marking <br> (4/5) | Samples |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| LM385DR-2-5 | ACTIVE | SOIC | D | 8 | 2500 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385-25 | Samples |
| LM385DRG4-2-5 | ACTIVE | SOIC | D | 8 | 2500 | Green (RoHS \& no $\mathrm{Sb} / \mathrm{Br}$ ) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385-25 | Samples |
| LM385LP-2-5 | ACTIVE | TO-92 | LP | 3 | 1000 | Pb-Free (RoHS) | CU SN | N/ A for Pkg Type | 0 to 70 | 385-25 | Samples |
| LM385LPE3-2-5 | ACTIVE | TO-92 | LP | 3 | 1000 | Pb-Free (RoHS) | CU SN | N / A for Pkg Type | 0 to 70 | 385-25 | Samples |
| LM385LPR-2-5 | ACTIVE | TO-92 | LP | 3 | 2000 | Pb-Free (RoHS) | CU SN | N / A for Pkg Type | 0 to 70 | 385-25 | Samples |
| LM385PWR-2-5 | ACTIVE | TSSOP | PW | 8 | 2000 | Green (RoHS \& no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM | 0 to 70 | 385-25 | Samples |

${ }^{(1)}$ The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.
${ }^{(2)}$ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS \& no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed $0.1 \%$ by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
Green (RoHS \& no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed $0.1 \%$ by weight in homogeneous material)
${ }^{(3)}$ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
${ }^{(4)}$ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
${ }^{(5)}$ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
${ }^{(6)}$ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width

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## TAPE AND REEL INFORMATION

REEL DIMENSIONS


W1

TAPE AND REEL INFORMATION
*All dimensions are nominal

| Device | Package <br> Type | Package <br> Drawing | Pins | SPQ | Reel <br> Diameter <br> $(\mathbf{m m})$ | Reel <br> Width <br> $\mathbf{W 1}(\mathbf{m m})$ | A0 <br> $(\mathbf{m m})$ | B0 <br> $(\mathbf{m m})$ | K0 <br> $(\mathbf{m m})$ | P1 <br> $(\mathbf{m m})$ | W <br> $(\mathbf{m m})$ | Pin1 <br> Quadrant |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| LM285DR-2-5 | SOIC | D | 8 | 2500 | 330.0 | 12.4 | 6.4 | 5.2 | 2.1 | 8.0 | 12.0 | Q1 |
| LM385BDR-2-5 | SOIC | D | 8 | 2500 | 330.0 | 12.4 | 6.4 | 5.2 | 2.1 | 8.0 | 12.0 | Q1 |
| LM385BPWR-2-5 | TSSOP | PW | 8 | 2000 | 330.0 | 12.4 | 7.0 | 3.6 | 1.6 | 8.0 | 12.0 | Q1 |
| LM385DR-2-5 | SOIC | D | 8 | 2500 | 330.0 | 12.4 | 6.4 | 5.2 | 2.1 | 8.0 | 12.0 | Q1 |
| LM385PWR-2-5 | TSSOP | PW | 8 | 2000 | 330.0 | 12.4 | 7.0 | 3.6 | 1.6 | 8.0 | 12.0 | Q1 |


*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Length (mm) | Width (mm) | Height (mm) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| LM285DR-2-5 | SOIC | D | 8 | 2500 | 340.5 | 338.1 | 20.6 |
| LM385BDR-2-5 | SOIC | D | 8 | 2500 | 340.5 | 338.1 | 20.6 |
| LM385BPWR-2-5 | TSSOP | PW | 8 | 2000 | 367.0 | 367.0 | 35.0 |
| LM385DR-2-5 | SOIC | D | 8 | 2500 | 340.5 | 338.1 | 20.6 |
| LM385PWR-2-5 | TSSOP | PW | 8 | 2000 | 367.0 | 367.0 | 35.0 |

D (R-PDSO-G8)


NOTES: A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.

Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shal not exceed $0.006(0,15)$ each side.
D. Body width does not include interlead flash. Interlead flash shall not exceed $0.017(0,43)$ each side
E. Reference JEDEC MS-012 variation AA.

D (R-PDSO-G8)


NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Publication IPC-7351 is recommended for alternate designs.
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.


NOTES: A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
(c) Lead dimensions are not controlled within this area.
(D) Falls within JEDEC TO-226 Variation AA (TO-226 replaces TO-92).
E. Shipping Method:

Straight lead option available in bulk pack only.
Formed lead option available in tape \& reel or ammo pack.
Specific products can be offered in limited combinations of shipping mediums and lead options.
Consult product folder for more information on available options.


TAPE \& REEL

NOTES: A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. Tape and Reel information for the Formed Lead Option package.


NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
B. This drawing is subject to change without notice.

C Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
D Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
E. Falls within JEDEC MO-153

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TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

| Products |  | Applications |  |
| :---: | :---: | :---: | :---: |
| Audio | www.ti.com/audio | Automotive and Transportation | www.ti.com/automotive |
| Amplifiers | amplifier.ti.com | Communications and Telecom | www.ti.com/communications |
| Data Converters | dataconverter.ti.com | Computers and Peripherals | www.ti.com/computers |
| DLP® Products | www.dlp.com | Consumer Electronics | www.ti.com/consumer-apps |
| DSP | dsp.ti.com | Energy and Lighting | www.ti.com/energy |
| Clocks and Timers | www.ti.com/clocks | Industrial | www.ti.com/industrial |
| Interface | interface.ti.com | Medical | www.ti.com/medical |
| Logic | logic.ti.com | Security | www.ti.com/security |
| Power Mgmt | power.ti.com | Space, Avionics and Defense | www.ti.com/space-avionics-defense |
| Microcontrollers | $\underline{\text { microcontroller.ti.com }}$ | Video and Imaging | www.ti.com/video |
| RFID | www.ti-rfid.com |  |  |
| OMAP Applications Processors | www.ti.com/omap | TI E2E Community | e2e.ti.com |
| Wireless Connectivity | www.ti.com/wirelessco |  |  |


[^0]:    $\ddagger$ Full range is $0^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ for the LM385-2.5 and LM385B-2.5, and $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ for the LM285-2.5.
    $\S$ The average temperature coefficient of reference voltage is defined as the total change in reference voltage divided by the specified temperature range.

[^1]:    $\dagger$ Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.
    $\ddagger$ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

