

MSM9004-03/04

1/4 DUTY LCD DRIVER WITH 4-DOT COMMON DRIVER AND 50-DOT SEGMENT DRIVER

GENERAL DESCRIPTION

The MSM9004-03/04 is an LCD driver for dynamic display with a 1/4 duty and 50-dot segment driver. It can display up to 200 segments.

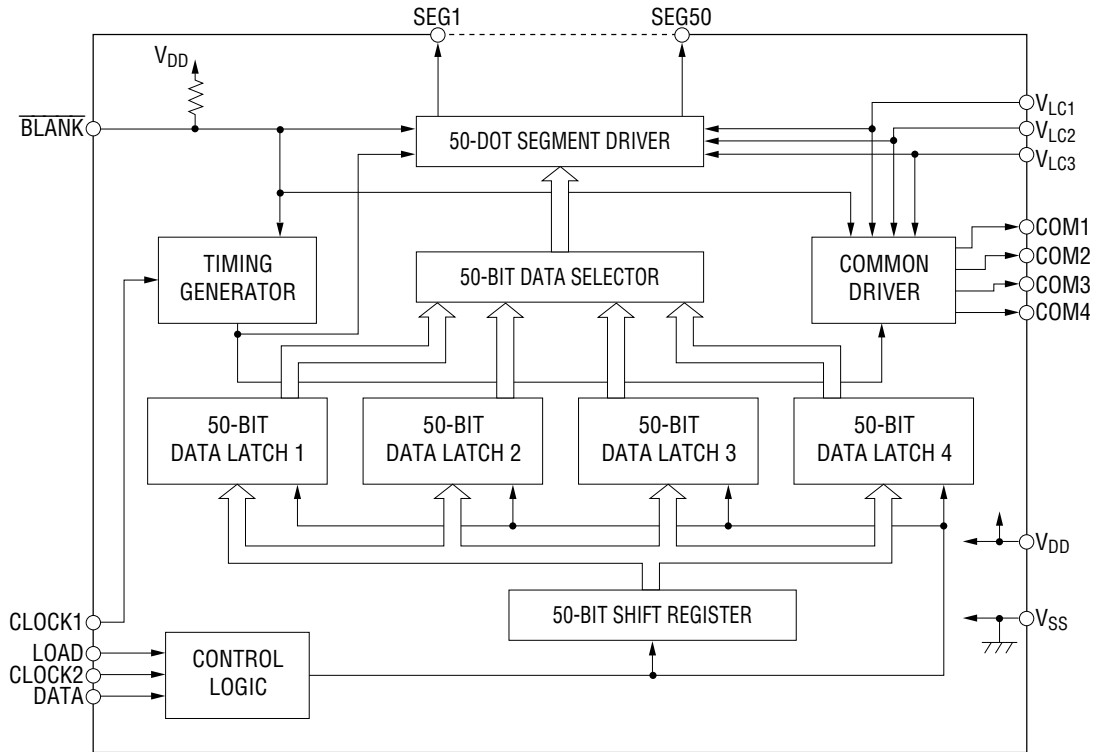
FEATURES

- Logic power supply voltage (V_{DD}) : 5.0 V \pm 10%
- LCD drive power supply voltage (V_{LCD}) : 3.7 V to 5.5 V
- Serial transfer clock frequency : 2 MHz max.
- LCD output resistance
 - Common driver : 20 k Ω
 - Segment driver : 60 k Ω
- Display duty : 1/4
- LCD segment output : 50
- Maximum number of display segments : 200 segments max.
- Display blanking terminal attached
- Operating temperature range : -40 to +85°C
- Interface with microcontroller
 - MSM9004-03 : LOAD, DATA, CLOCK1, CLOCK2
 - MSM9004-04 : LOAD, DATA, CLOCK2
- System clock
 - MSM9004-03 : external input
 - MSM9004-04 : internal oscillation circuit (external resistor and capacitor required)
- Package:
 - 64-pin plastic QFP (QFP 64-P-1414-0.80-BK) (Product name : MSM9004-03GS-BK)
 - (Product name : MSM9004-04GS-BK)
- Comparison of device codes with functions

Function Device code	LCD segment output		Duty		System clock	
	50	49	1/3	1/4	External input	Internal oscillation circuit
MSM9004-01	—	○	○	○	○	—
MSM9004-02	—	○	○	○	—	○
MSM9004-03	○	—	—	○	○	—
MSM9004-04	○	—	—	○	—	○

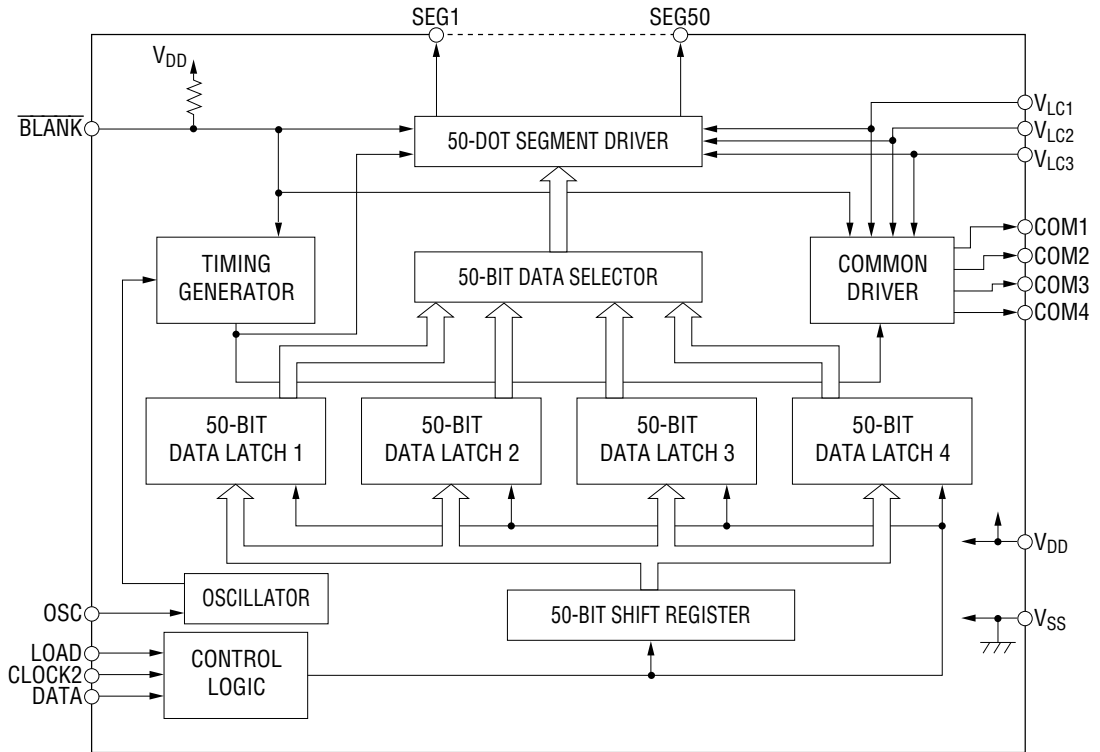
BLOCK DIAGRAM

MSM9004-03



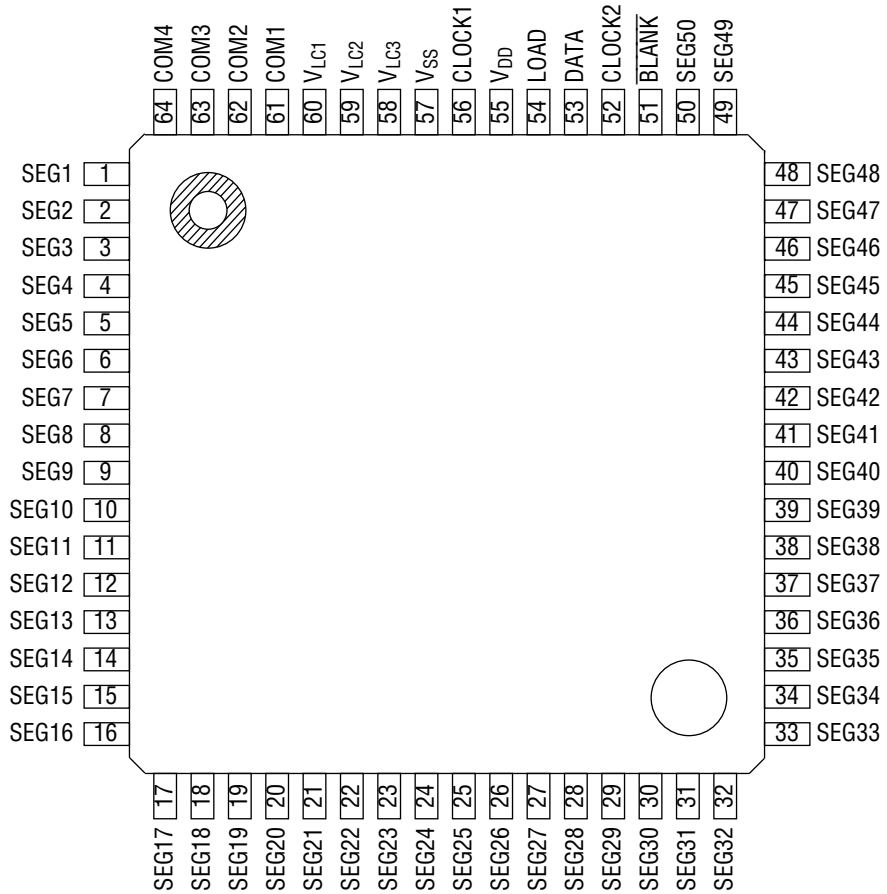
BLOCK DIAGRAM

MSM9004-04



PIN CONFIGURATION (TOP VIEW)

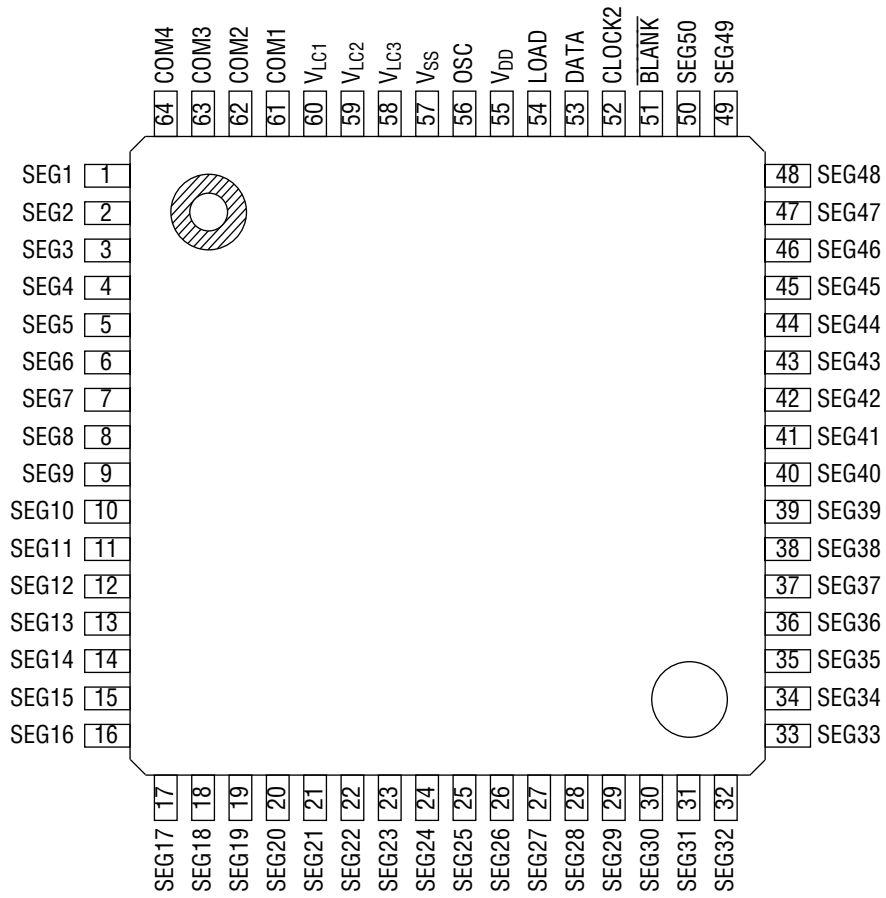
MSM9004-03



64-Pin Plastic QFP

PIN CONFIGURATION (TOP VIEW)

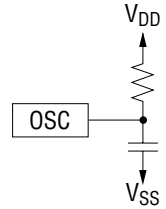
MSM9004-04



64-Pin Plastic QFP

PIN DESCRIPTIONS

Pin	Symbol	Type	Connected to	Description
56	CLOCK1 *1	I	Micro-controller	System clock input pin. For details see "MSM9004-04 Oscillation Circuit characteristics".
56	OSC *2	I	External resistor Capacitor	Pin for oscillation. Connecting the external resistor and capacitor to configure the oscillation circuit. Connect the resistor and capacitor as shown in the right figure. Make the length of wiring between this pin and the external resistor and capacitor as short as possible.
53	DATA	I	Micro-controller	Serial data input. For details on the configuration of input data, see "Data Configuration".
52	CLOCK2	I	Micro-controller	Shift clock input (Schmitt circuit included). Input data is read synchronizing with the rising edge of this clock.
54	LOAD	I	Micro-controller	Load signal input (Schmitt circuit included). Serial input data is transferred to the display latch when this LOAD signal is at a "H" level.
51	BLANK	I	—	Input pin for turning off all segments. All segments turn off, regardless of the display data, when this pin is at a "L" level, and all segments return to the status before turning off.
61 to 64	COM1-4	O	LCD	LCD common output pins.
1 to 50	SEG1-50	O	LCD	LCD segment output pins.
60 59 58	V _{LC1} V _{LC2} V _{LC3}	Power supply	—	Bias power supply pins for driving the LCD. Conditions for bias voltage are as follows : $3.7V \leq V_{DD} - V_{LC3} \leq 5.5V$ $V_{LC1} = V_{DD} - \frac{2}{3}(V_{DD} - V_{LC3})$ $V_{LC2} = V_{DD} - \frac{1}{3}(V_{DD} - V_{LC3})$ $V_{LC3} \geq V_{SS}$
55 57	V _{DD} V _{SS}	Power supply	—	Power supply pins. Normally used as V _{DD} = 4.5 to 5.5 V and V _{SS} = 0 V.



*1 Applied to MSM9004-03.

*2 Applied to MSM9004-04.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V_{DD}	—	-0.3 to +6.5	V
LCD Driving Voltage	V_{LCD}	—	-0.3 to $V_{DD} + 0.3$	V
Input Voltage	V_{IN}	—	-0.3 to $V_{DD} + 0.3$	V
Power Dissipation	P_D	$T_a = +85^{\circ}\text{C}$	450	mW
Storage Temperature	T_{STG}	—	-55 to +150	$^{\circ}\text{C}$

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Condition	Range	Unit
Power Supply Voltage	V_{DD}	—	4.5 to 5.5	V
LCD Driving Voltage	V_{LCD} * 1	* 2	3.7 to V_{DD} * 3	V
System Clock Frequency	f_{CP1}	Applied to MSM9004-01	1.5 to 4.3	kHz
Self Oscillation Frequency	f_{OSC}	Applied to MSM9004-02	1.5 to 4.3	kHz
Shift Frequency (Max)	f_{CP2}	—	2.0	MHz
Operating Temperature	T_{op}	—	-40 to +85	$^{\circ}\text{C}$

*1: $V_{DD} - V_{LC3}$

*2: The following relationship must be kept:

$$V_{DD} > V_{LC1} > V_{LC2} \geq V_{SS}$$

$$V_{LC1} = V_{DD} - \frac{1}{3} (V_{DD} - V_{LC3})$$

$$V_{LC2} = V_{DD} - \frac{2}{3} (V_{DD} - V_{LC3})$$

$$V_{LC3} = V_{DD} - \frac{3}{3} (V_{DD} - V_{LC3})$$

*3: V_{DD} is the reference potential for the LCD driving voltage. To decide the LCD driving voltage, change the voltage between V_{LC3} and V_{SS} ($V_{LC3} > 0$).

ELECTRICAL CHARACTERISTICS

DC Characteristics (MSM9004-03)

(Ta = -40 to +85°C, V_{DD} = 4.5 to 5.5 V, V_{DD} - V_{LC3} = 3.7 V to V_{DD})

Parameter	Symbol	Condition	Min.	Max.	Unit	Applicable pin	
"H" Input Voltage	V _{IH1}	—	0.8V _{DD}	V _{DD}	V	LOAD, CLOCK2 DATA, $\overline{\text{BLANK}}$	
	V _{IH2}	—	0.85V _{DD}	V _{DD}	V	CLOCK1	
"L" Input Voltage	V _{IL1}	—	0.0	0.2V _{DD}	V	LOAD, CLOCK2 DATA, $\overline{\text{BLANK}}$	
	V _{IL2}	—	0.0	0.15V _{DD}	V	CLOCK1	
"H" Input Current	I _{IH1}	V _{IN} =V _{DD}	-1.0	1.0	μA	LOAD, CLOCK1 CLOCK2, DATA	
	I _{IH2}	V _{IN} =V _{DD}	-3.0	3.0	μA	$\overline{\text{BLANK}}$	
"L" Input Current	I _{IL1}	V _{DD} =5.5 V, V _{IN} =0 V	-1.0	1.0	μA	LOAD, CLOCK1 CLOCK2, DATA	
	I _{IL2}	V _{DD} =5.5 V, V _{IN} =0 V	-160	-20	μA	$\overline{\text{BLANK}}$	
ON Resistance (SEG)	R _{ON V0}	I _O =-10 μA	V _{DD} =4.5 V V _{LC1} = $\frac{2}{3}$ V _{DD}	—	60	kΩ	SEG1-50
	R _{ON V1}	I _O =±10 μA					
	R _{ON V2}	I _O =±10 μA					
	R _{ON V3}	I _O =10 μA					
ON Resistance (COM)	R _{ON V0}	I _O =-30 μA	V _{LC2} = $\frac{1}{3}$ V _{DD} V _{LC3} =V _{SS}	—	20	kΩ	COM1-4
	R _{ON V1}	I _O =±30 μA					
	R _{ON V2}	I _O =±30 μA					
	R _{ON V3}	I _O =30 μA					
Dynamic Supply Current	I _{DD}	f _{CP1} =2.4 kHz f _{CP2} =1 MHz 1/4 duty COM1-4: No Load SEG1-50: No Load	* 1 —	0.65	mA	V _{DD}	

* 1: For input data, input a logic "0" and a logic "1" to LCD display bits alternately. The t_r and t_f time for the CLOCK1, CLOCK2, DATA, and LOAD pins must be less than 20ns.

DC Characteristics (MSM9004-04)

(Ta= - 40 to +85°C, V_{DD}=4.5 to 5.5 V, V_{DD}-V_{LC3}= 3.7 V to V_{DD})

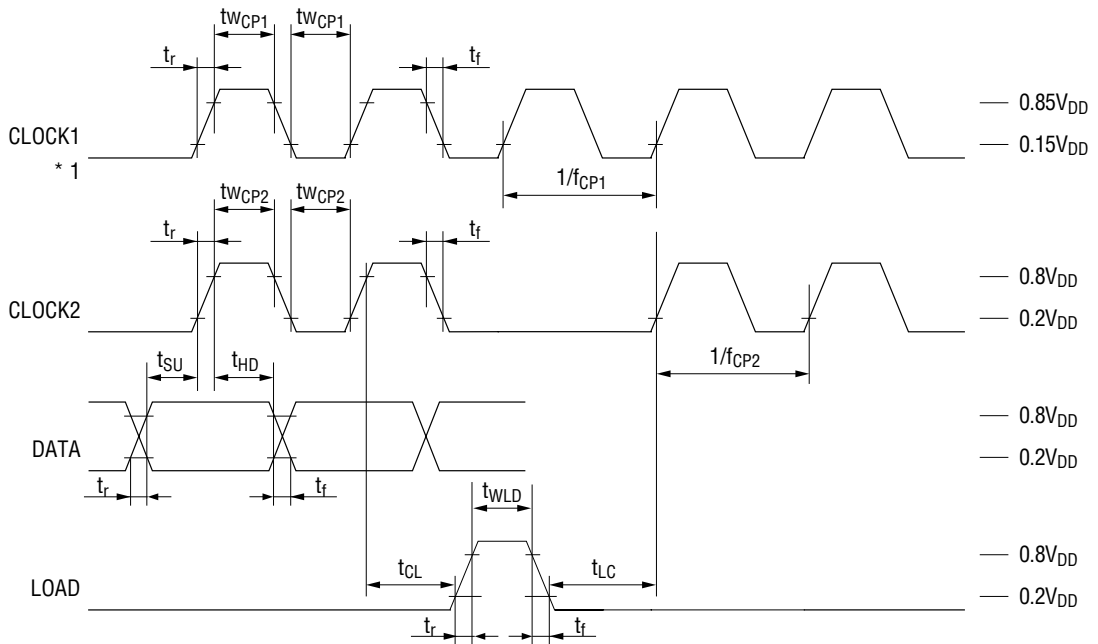
Parameter	Symbol	Condition	Min.	Max.	Unit	Applicable pin
"H" Input Voltage	V _{IH1}	—	0.8V _{DD}	V _{DD}	V	LOAD, CLOCK2 DATA, <u>BLANK</u>
"L" Input Voltage	V _{IL1}	—	0.0	0.2V _{DD}	V	LOAD, CLOCK2 DATA, <u>BLANK</u>
"H" Input Current	I _{IH1}	V _{IN} =V _{DD}	-1.0	1.0	μA	LOAD, CLOCK2 DATA
	I _{IH2}	V _{IN} =V _{DD}	-3.0	3.0	μA	<u>BLANK</u>
"L" Input Current	I _{IL1}	V _{DD} =5.5 V, V _{IN} =0 V	-1.0	1.0	μA	LOAD, CLOCK2 DATA
	I _{IL2}	V _{DD} =5.5 V, V _{IN} =0 V	-160	-20	μA	<u>BLANK</u>
ON Resistance (SEG)	R _{ON V0}	I _O =-10 μA	—	60	kΩ	SEG1-50
	R _{ON V1}	I _O =±10 μA				
	R _{ON V2}	I _O =±10 μA				
	R _{ON V3}	I _O =10 μA				
ON Resistance (COM)	R _{ON V0}	I _O =-30 μA	—	20	kΩ	COM1-4
	R _{ON V1}	I _O =±30 μA				
	R _{ON V2}	I _O =±30 μA				
	R _{ON V3}	I _O =30 μA				
Dynamic Supply Current	I _{DD}	f _{CP2} =1 MHz 1/4 duty COM1-4: No Load SEG1-50: No Load	—	1.00	mA	V _{DD}

* 1: For input data, input a logic "0" and a logic "1" to LCD display bits alternately.
 External resistor for oscillation : R_O = 62 kΩ
 External capacitor for oscillation : C_O = 0.01 μF
 The t_r and t_f time for the CLOCK2, DATA, and LOAD pins must be less than 20ns.
 The current that flows through the external resistor and capacitor is not included.

AC Characteristics

($T_a = -40$ to $+85^\circ\text{C}$, $V_{DD} = 4.5$ to 5.5 V, $V_{DD} - V_{LC3} = 3.7$ V to V_{DD})

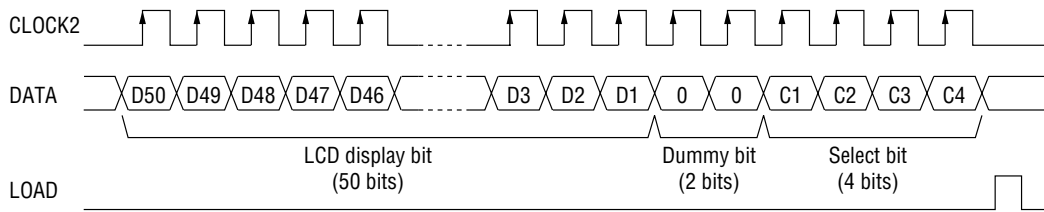
Parameter	Symbol	Condition	Min.	Max.	Unit
System Clock Frequency	f_{CP1}	Not applied to MSM9004-04	1.5	4.3	kHz
System Clock Pulse Width	t_{WCP1}	Not applied to MSM9004-04	1.0	—	μs
Data Clock Frequency	f_{CP2}	—	—	2.0	MHz
Data Clock Pulse Width	t_{WCP2}	—	200	—	ns
Data Setup Time	t_{SU}	—	100	—	ns
Data Hold Time	t_{HD}	—	100	—	ns
Load Pulse Width	t_{WLD}	—	200	—	ns
Clock-Load Time	t_{CL}	—	200	—	ns
Load-Clock Time	t_{LC}	—	200	—	ns
Rise Time	t_r	—	—	100	ns
Fall Time	t_f	—	—	100	ns



*1 Not applied to MSM9004-04.

FUNCTIONAL DESCRIPTION

Display Data Input



Note: Always set a logic "0" to the dummy bit.

LCD Display bit - LCD Panel Lighting Status Correspondence Table

LCD display bit (D1 to D50)	LCD panel lighting status
1	ON
0	OFF

LCD Display bit - Segment Output Correspondence Table

LCD display bit	D1	D2	-----	D46	D47	D48	D49	D50
Segment output	SEG1	SEG2	-----	SEG46	SEG47	SEG48	SEG49	SEG50

Select bit - Data Latch Correspondence Table

C4	C3	C2	C1	Display data writing data latch	Corresponding common output
0	0	0	1	Data latch 1	COM1
0	0	1	0	Data latch 2	COM2
0	1	0	0	Data latch 3	COM3
1	0	0	0	Data latch 4	COM4

Select bits C1 to C4 select Data latch 1 to Data latch 4 corresponding to COM 1 to COM 4 independently. If logic "1s" are selected to multiple select bits, the LCD display bits are written to all corresponding data latches.

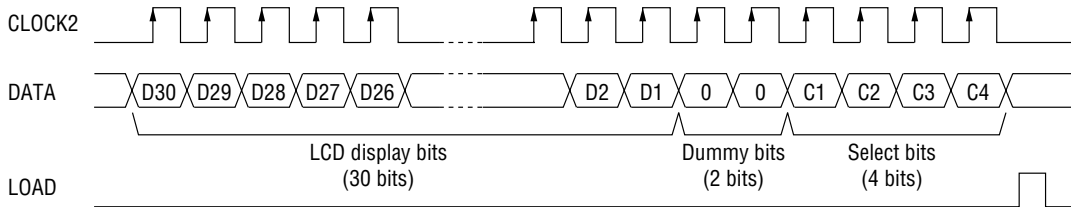
Example:

When logic "1s" are set to all select bits C1 to C4, and logic "0s" are set to all LCD display bits D1 to D50, logic "0s" are written to Data latch 1 to Data latch 4 by one data input. This leads to turning off the entire LCD panel.

How to Decrease Transfer Time When Unused Segments Exist

The MSM9004-03/04 can operate even if segment data is not full. Input data as in the figure shown below. In this case, the last 4 bits are the select bits.

Transfer example: for 30 segments



Note: Always set "0" to the dummy bit.

Recommended RC Values for MSM9004-04 Oscillation Circuit

Shown below are the recommended oscillation resistor (R_O) and capacitor (C_O) values for providing frame frequency f_{FRM} of approximately 62 Hz to 190 Hz.

- $R_O = 62\text{ k}\Omega \pm 5\%$
- $C_O = 0.01\text{ }\mu\text{F} \pm 5\%$

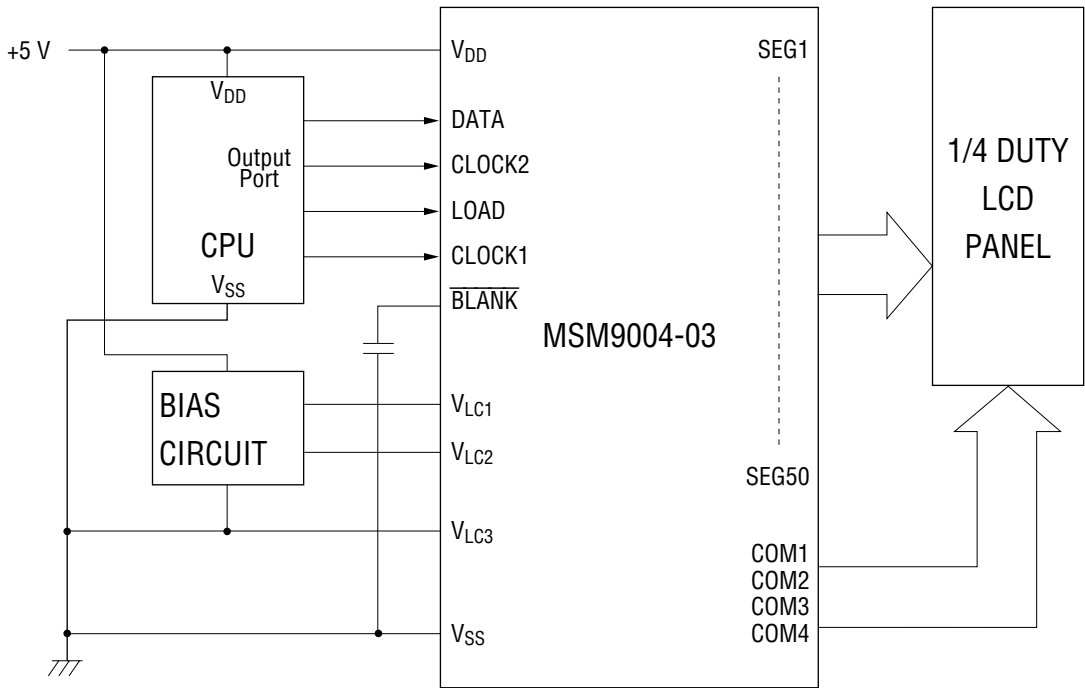
(Resistance and capacitance variations due to temperature change are not included.)

For requirements of supply voltage (V_{DD}), ambient temperature range (T_a), and frame frequency (f_{FRM}):

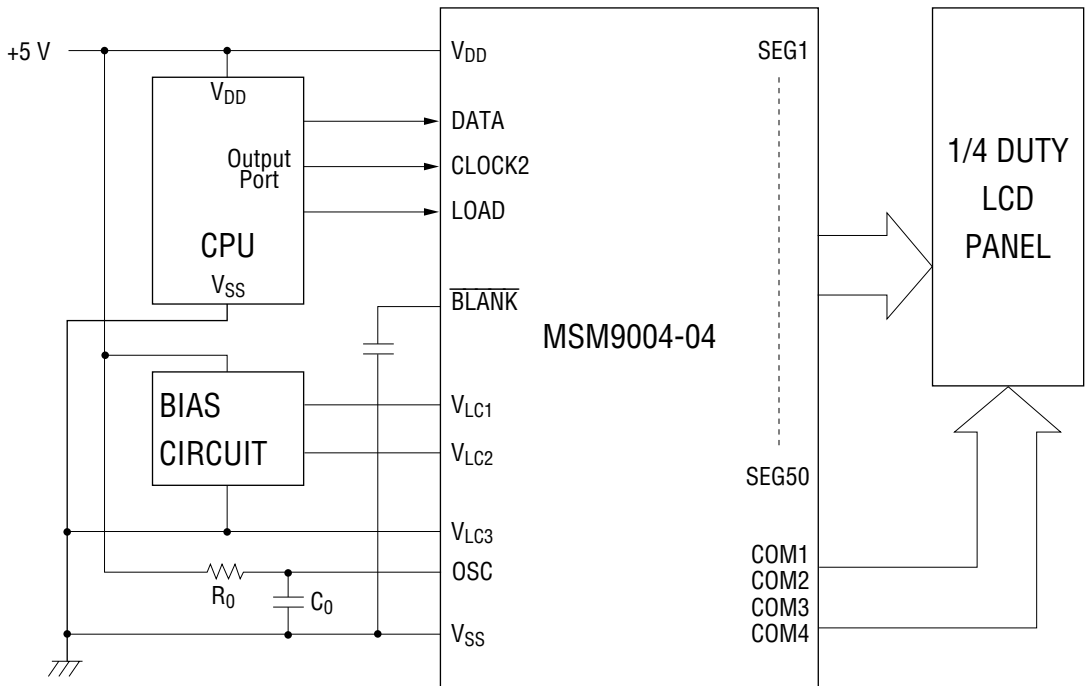
- $V_{DD} = 5.0\text{ V}$
- $T_a = -40\text{ to }+85^\circ\text{C}$
- $f_{FRM} = \frac{1}{24}$ oscillation frequency

APPLICATION CIRCUITS

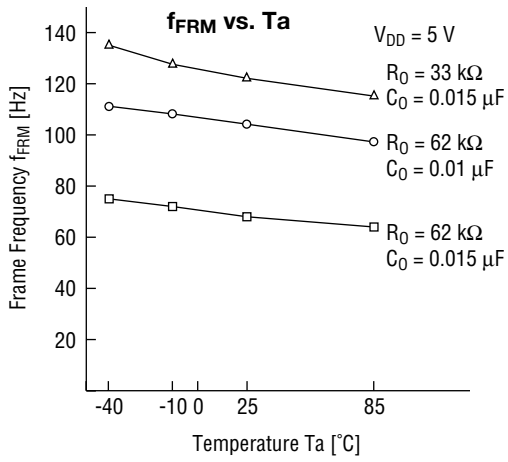
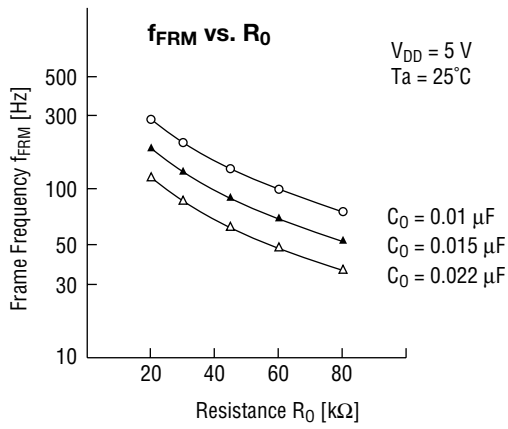
MSM9004-03



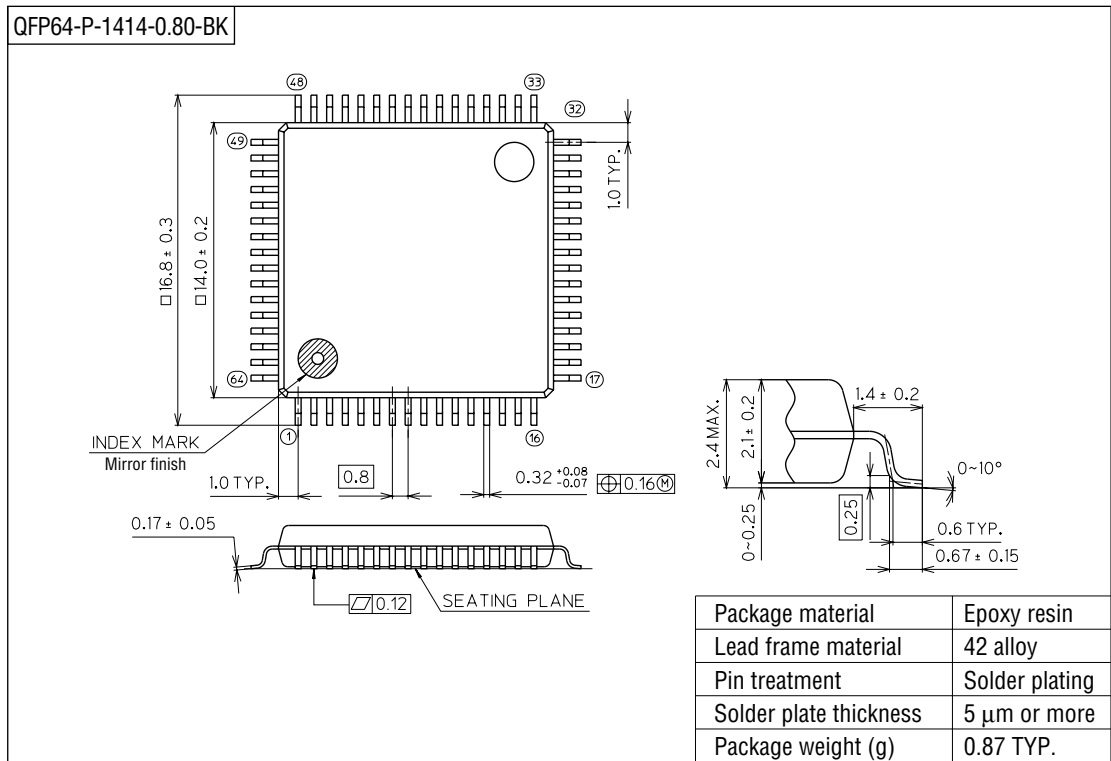
MSM9004-04



REFERENCE DATA



PACKAGE DIMENSIONS



Notes for Mounting the Surface Mount Type Package

The SOP, QFP, TSOP, SOJ, QFJ (PLCC), SHP and BGA are surface mount type packages, which are very susceptible to heat in reflow mounting and humidity absorbed in storage. Therefore, before you perform reflow mounting, contact Oki's responsible sales person for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).