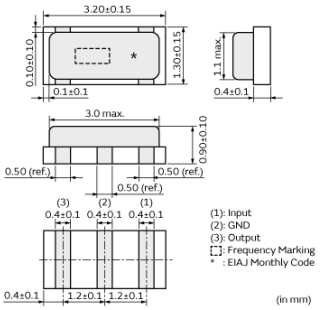
Note: This datasheet may be out of date.
Please download the latest datasheet of CSTNE14M7V53C000R0 from the official website of Murata Manufacturing Co., Ltd.
https://www.murata.com/en-us/products/productdetail?partno=CSTNE14M7V53C000R0

CSTNE14M7V53C000R0

In Production | Recommended | RoHS | REACH | AEC-Q200

Appearance & Shape





Features

MURATA's package technology expertise has enabled the development of the Chip CERALOCK(R) with built-in load capacitors.
This diverse series owes its development to MURATA's original mass production techniques and high reliability, and has achieved importance in the worldwide automotive market.

Features
1. High reliability and available for a wide temperature range.
2. Oscillation circuits do not require external load capacitors.
3. Available for a wide frequency range.
4. Extremely small and have a low profile.
5. No adjustment is necessary for oscillation circuits.
6. Stable supply is ensured due to not using precious metal(Palladium).

Applications

Automotive Usage	Powertrain/Safety
Other Usage	Automotive

Packaging Information

Packaging	Specifications	Minimum Order Quantity
R0	180mm Embossed Tape	3000

Attention
1.This datasheet is downloaded from the website of Murata Manufacturing Co., Ltd. Therefore, it's specifications are subject to change or our products in it may be discontinued without advance notice. Please check with our sales representatives or product engineers before ordering.
2.This datasheet has only typical specifications because there is no space for detailed specifications.
Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering.

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CSTNE14M7V53C000R0

Specifications

Shape	SMD
Frequency	14.746MHz
Frequency Tolerance	±0.50%
Operating Temperature Range	-40℃ to 125℃
Frequency Shift by Temperature	±0.15%
Frequency Aging	±0.10%
Wash	available
Resonant Impedance	40Ω
Built-in Load Capacitance	15pF

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