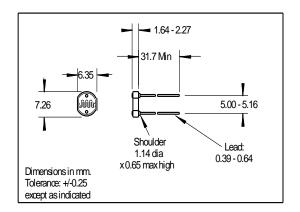
TO-5 Ceramic Photocell

NSL-5522





FEATURES

DESCRIPTION

APPLICATIONS

- Passive resistance output
- Ceramic package

The NSL-5522 is a CdS photoconductive cell on a TO-5 ceramic substrate. The photocell is encapsulated with epoxy for moisture resistance.

Industrial

ABSOLUTE MAXIMUM RATING

(TA)= 23°C UNLESS OTHERWISE NOTED

SYMBOL	PARAMETER	MIN	MAX	UNITS
V_P	Voltage (peak AC or DC)		120	V
P _d	Power Dissipation @ 25°C (1)		125	mW
T _{Op}	Operating Temperature	-60	+75	°C
T _{Stg}	Storage Temperature	-60	+75	°C
T _S	Soldering Temperature (2)		+260	°C

Note:

- (1) Derate linearly to 0 at 75°C
- (2) >0.08" from case for <5 sec.
- (3) Cells light adapted at 30 to 50 Ftc for 16 hrs minimum prior to electrical tests.

RELIABILITY

Contact API for recommendations on specific test conditions and procedures.

ELECTRO-OPTICAL CHARACTERISTICS

(TA)= 23°C, UNLESS OTHERWISE NOTED

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
R_L	Light Resistance	2 ftc., 2854°K (3)	6.7	10	13.3	ΚΩ
		100 ftc., 2854°K		700		Ω
R_D	Dark Resistance	5 sec after removal of test light.	0.67			МΩ
λ_{P}	Spectral Peak			550		nm