

# CT2301-R3

# **P-Channel Enhancement MOSFET**

#### **Features**

- Drain-Source Breakdown Voltage V<sub>DSS</sub> 20 V
- Drain-Source On-Resistance  $R_{DS(ON)}\,85m\Omega,\,at\,V_{GS}\text{=-}4.5V,\,I_{DS}\text{=-}3.0A$   $R_{DS(ON)}\,100m\Omega,\,at\,V_{GS}\text{=-}2.5V,\,I_{DS}\text{=-}2.0A$
- Continuous Drain Current at T<sub>A</sub>=25 $^{\circ}$ C I<sub>D</sub> = 3.0A
- Advanced high cell density Trench Technology
- RoHS Compliance & Halogen Free

## **Applications**

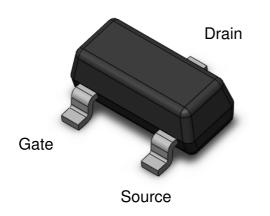
- Power Management
- Portable Equipment
- Battery Powered System
- Load Switch

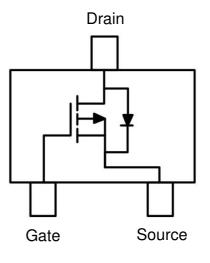
### **Package Outline**

### Description

The CT2301-R3 uses high performance Trench Technology to provide excellent  $R_{DS(ON)}$  and low gate charge which is suitable for most of the synchronous buck converter applications .

## Schematic







## Absolute Maximum Rating at 25°C

Symbol	Parameters	Ratings	Units	Notes
V <sub>DS</sub>	Drain-Source Voltage	-20	V	
V <sub>GS</sub>	Gate-Source Voltage	±12	V	
ID	Continuous Drain Current @TA=25°C	-3.0	А	1
I <sub>DM</sub>	Pulsed Drain Current	-12	A	1
$P_D$	Total Power Dissipation	1.25	W	2
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C	
$T_J$	Operating Junction Temperature Range	-55 to 150	°C	

### **Thermal Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
D	Thermal Resistance			200		°C /W	14
R <sub>ØJA</sub>	Junction-Ambient (t=10s)		-	200	-	°C /W	1,4



## Electrical Characteristics T<sub>A</sub> = 25 °C (unless otherwise specified)

#### **Static Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Bvdss	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> = - 0.25µA	-20	-	-	V	
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}=-20V,V_{GS}=0V$	-	-	-1	μA	
Igss	Gate-Source Leakage Current	$V_{GS} = \pm 12$ `V, $V_{DS} = 0$ V	-	-	±100	nA	

#### **On Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Deserve	Drain Course On Desistance	$V_{GS} = -4.5V, I_D = -3.0A$	-	85	100	m	2
Rds(on)	Drain-Source On-Resistance	$V_{GS} = -2.5V, I_D = -2.0A$	-	100	150	m	3
$V_{\text{GS(TH)}}$	Gate-Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = -0.25 \mu A$	-0.4	-	-0.9	V	3

### **Dynamic Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Ciss	Input Capacitance	$V_{DS} = -8V$ ,	-	640	-		
Coss	Output Capacitance	$V_{GS} = 0V,$	-	59	-	pF	
C <sub>RSS</sub>	Reverse Transfer Capacitance	f=1MHz	-	70	-		

#### **Switching Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
T <sub>D(ON)</sub>	Turn-On Delay Time		-	4	-		
TR	Rise Time	$V_{\text{DS}} = -10V \ , \ V_{\text{GS}} = -4.5V, \label{eq:VDS}$	-	28.2	-	20	
T <sub>D(OFF)</sub>	Turn-Off Delay Time	$R_G=4.7\Omega,  I_D=-2.8A$	-	27.1	-	ns	
TF	Fall Time		-	9.2			
Q <sub>G</sub>	Total Gate Charge		-	7.65	-		
Q <sub>GS</sub>	Gate-Source Charge	$V_{DS} = -4.5V$ , $V_{GS} = -10V$ , $I_{D} = -2.8A$	-	1.1	-	nC	
Q <sub>GD</sub>	Gate-Drain (Miller) Charge	ID = -2.6A	-	1.95	-		

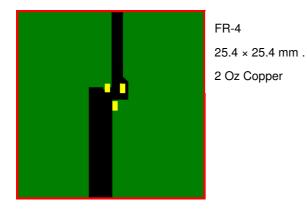


#### **Drain-Source Diode Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Vsd	Body Diode Forward Voltage	$V_{GS} = 0V$ , $I_{SD} = -2.8A$			1.2	V	
I <sub>SD</sub>	Body Diode Continuous Current				-2.8	А	1

Note:

- 1. The power dissipation is limited by 150  $^\circ\text{C}$  junction temperature.
- 2. Device mounted on a glass-epoxy board

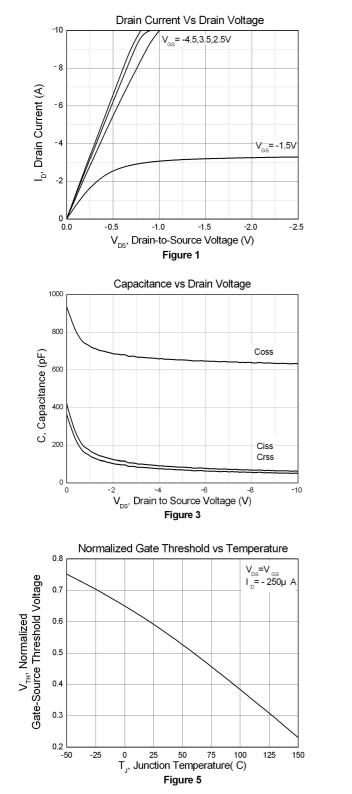


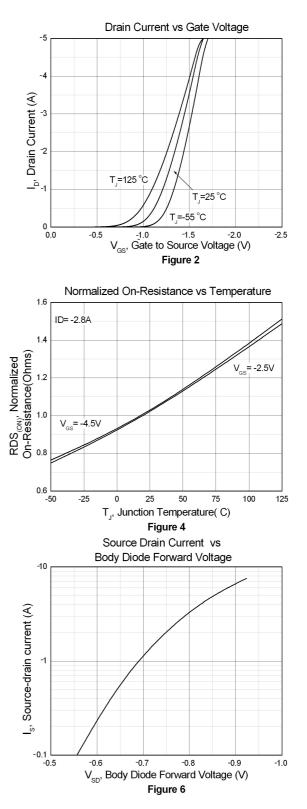
Actual Size

- 3. The data tested by pulsed , pulse width  $\,\leq\,$  300 $\mu s$  , duty cycle  $\,\leq\,$  2%
- 4. Thermal Resistance follow JESD51-3.



## **Typical Characteristic Curves**

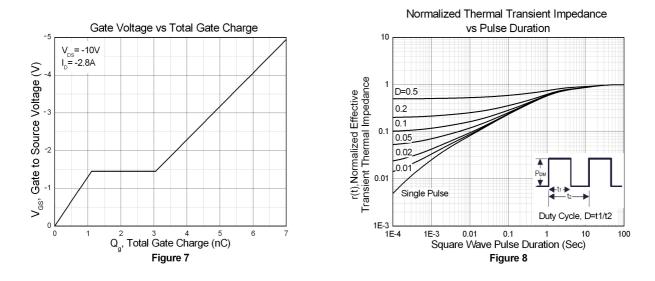






# CT2301-R3

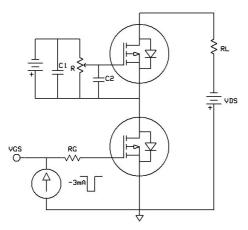
# **P-Channel Enhancement MOSFET**



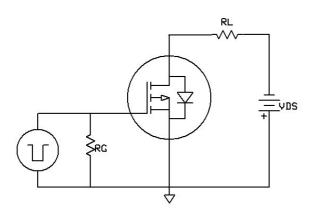


## **Test Circuits & Waveforms**

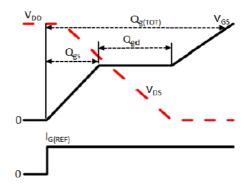
### Figure 9: Gate Charge Test Circuit



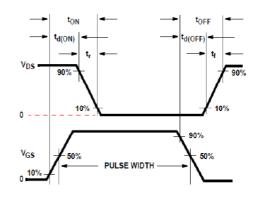
### Figure 11: Switching Time Test Circuit



### Figure 10: Gate Charge Waveform

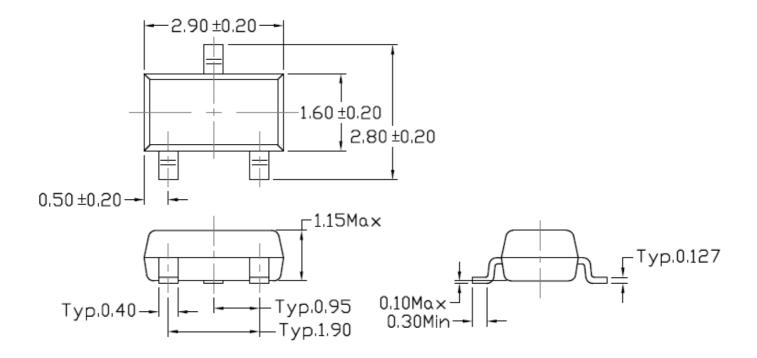


#### Figure 12: Switching Time Waveform



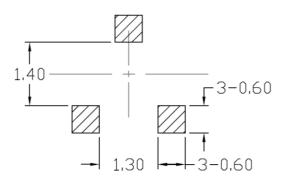


## Package Dimension (SC-59)



Note: Dimensions in mm

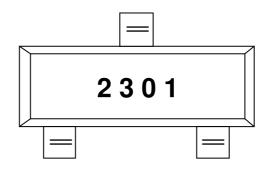
## Recommended pad layout for surface mount leadform



Note: Dimensions in mm



## **Marking Information**



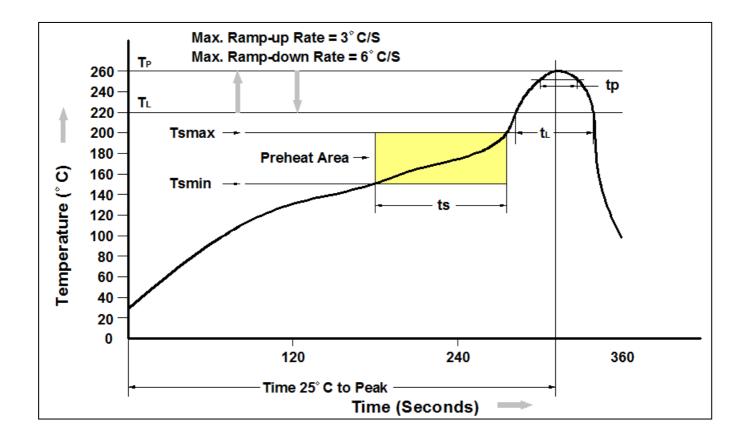
2301 : Device Number

## **Ordering Information**

Part Number	Description	Quantity
CT2301-R3	SC-59 Reel	3000 pcs



### **Reflow Profile**



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150 <i>°</i> C
Temperature Max. (Tsmax)	200 <i>°</i> C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds
Ramp-up Rate (t⊾ to tթ)	3℃/second max.
Liquidous Temperature (TL)	217°C
Time $(t_L)$ Maintained Above $(T_L)$	60 – 150 seconds
Peak Body Package Temperature	260 ℃ +0 ℃ / -5 ℃
Time (t <sub>P</sub> ) within 5℃ of 260℃	30 seconds
Ramp-down Rate (TP to TL)	6℃/second max
Time 25 ℃ to Peak Temperature	8 minutes max.



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