Silicon N Channel MOS Type (U-MOSⅢ)/Silicon Epitaxial Schottky Barrier Diode

# SSM5H11TU

### **DC-DC Converter Applications**

- 4.0-V drive
- Combined an N-ch MOSFET and a Schottky barrier diode in one package.
- Low R<sub>DS(ON)</sub> and Low V<sub>F</sub>

### **Absolute Maximum Ratings**

### MOSFET (Ta = 25°C)

| Characteristic          |       | Symbol                  | Rating | Unit |  |
|-------------------------|-------|-------------------------|--------|------|--|
| Drain-source voltage    |       | V <sub>DSS</sub>        | 30     | V    |  |
| Gate-source voltage     |       | V <sub>GSS</sub>        | ± 20   | V    |  |
| Drain current           | DC    | I <sub>D</sub>          | 1.6    | Α    |  |
|                         | Pulse | I <sub>DP</sub>         | 3.2    | Α    |  |
| Drain power dissipation |       | P <sub>D</sub> (Note 1) | 500    | mW   |  |
| Channel temperature     |       | T <sub>ch</sub>         | 150    | °C   |  |

### Schottky Barrier Diode (Ta = 25°C)

| Characteristics                      | Symbol             | Rating   | Unit |
|--------------------------------------|--------------------|----------|------|
| Repetitive peak reverse voltage      | $V_{RRM}$          | 30       | V    |
| Average forward current              | I <sub>F(AV)</sub> | 0.7      | Α    |
| Peak one cycle surge forward current | I <sub>FSM</sub>   | 2 (50Hz) | Α    |
| Junction temperature                 | Tj                 | 125      | °C   |

# 1: GATE 4: CATHODE 2: SOURCE 5: DRAIN 3: ANODE UFV JEDEC — JEITA — TOSHIBA 2-2R1A

Weight: 7 mg (typ.)

### MOSFET and Diode (Ta = 25°C)

| Characteristics           | Symbol           | Rating     | Unit |
|---------------------------|------------------|------------|------|
| Storage temperature range | T <sub>stg</sub> | -55 to 125 | °C   |

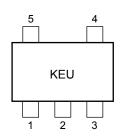
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

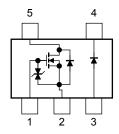
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on FR4 board (25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm, Cu pad: 645 mm<sup>2</sup>)

### Marking

### **Equivalent Circuit (top view)**





Start of commercial production 2008-04

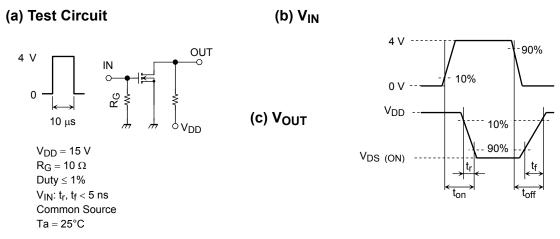
### **MOSFET**

### **Electrical Characteristics (Ta = 25°C)**

| Cha                            | racteristics   | Symbol               | Test Conditions  | Min | Тур. | Max  | Unit |
|--------------------------------|--|----------------------|--|-----|------|------|------|
| Drain-source breakdown voltage |  | V (BR) DSS           | $I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$                             | 30  | _    | _    | - V  |
|                                |  | V (BR) DSX           | $I_D = 1$ mA, $V_{GS} = -20$ V   | 15  |      | _    |      |
| Drain cutoff curre             | nt   | I <sub>DSS</sub>     | V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V                          | _   |      | 1    | μА   |
| Gate leakage curi              | rent   | I <sub>GSS</sub>     | $V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$                      | _   |      | ±1   | μА   |
| Gate threshold vo              | ltage  | V <sub>th</sub>      | $V_{DS} = 5 \text{ V}, I_D = 1 \text{ mA}$                             | 1.0 |      | 2.6  | V    |
| Forward transfer               | admittance   | Y <sub>fs</sub>      | $V_{DS} = 5 \text{ V}, I_D = 1A$ (Note 2)                              | 1.9 | 3.7  | _    | S    |
| 5                              |  | R <sub>DS</sub> (ON) | $I_D = 1 \text{ A}, V_{GS} = 10 \text{ V}$ (Note 2)                    | _   | 96   | 122  | mΩ   |
| Drain-source ON-resistance     | $I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}$ (Note 2) |                      | _  | 130 | 182  |      |      |
| Input capacitance              |  | C <sub>iss</sub>     |  | _   | 180  | _    |      |
| Output capacitance             |  | C <sub>oss</sub>     | $V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$       | _   | 34   | _    | pF   |
| Reverse transfer               | capacitance  | C <sub>rss</sub>     |  | _   | 27   | _    |      |
| Total Gate Charge              | e  | Qg                   |  | _   | 5.1  | _    |      |
| Gate-Source Cha                | arge   | Q <sub>gs</sub>      | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1.6 A, V <sub>GS</sub> = 10 V | _   | 3.9  | _    | nC   |
| Gate-Drain Charg               | ge   | Q <sub>gd</sub>      |  | _   | 1.2  | _    |      |
| Switching time                 | Turn-on time   | t <sub>on</sub>      | V <sub>DD</sub> = 15 V, I <sub>D</sub> = 0.5 A                         | _   | 9.5  | _    |      |
|                                | Turn-off time  | t <sub>off</sub>     | $V_{GS} = 0$ to 4 V, $R_G = 10 \Omega$                                 | _   | 9.0  | _    | ns   |
| Drain-source forw              | ard voltage  | V <sub>DSF</sub>     | $I_D = -1.6 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 2)                  | _   | -0.8 | -1.2 | V    |

Note 2: Pulse test

# **Switching Time Test Circuit**



### **Precaution**

 $V_{th}$  can be expressed as voltage between gate and source when the low operating current value is  $I_D = 1$  mA for this product. For normal switching operation,  $V_{GS\ (on)}$  requires a higher voltage than  $V_{th}$  and  $V_{GS\ (off)}$  requires a lower voltage than  $V_{th}$ .

(The relationship can be established as follows:  $V_{GS\ (off)} < V_{th} < V_{GS\ (on)}$ ) Be sure to take this into consideration when using the device.

### **Schottky Barrier Diode**

### **Electrical Characteristics (Ta = 25°C)**

| Characteristics                 | Symbol              | Test Condition         | Min | Тур. | Max  | Unit |
|---------------------------------|---------------------|------------------------|-----|------|------|------|
| Peak forward voltage            | V <sub>FM (1)</sub> | I <sub>F</sub> = 0.5 A | _   | 0.34 | 0.41 | V    |
| Peak forward voltage            | V <sub>FM (2)</sub> | I <sub>F</sub> = 0.7 A | _   | 0.37 | 0.44 | V    |
| Repetitive peak reverse current | I <sub>RRM</sub>    | V <sub>R</sub> = 15 V  | _   | 60   | 200  | μΑ   |
| Junction capacitance            | C <sub>T</sub>      | $V_R = 0 V, f = 1 MHz$ |     | 139  |      | pF   |

### **Precaution**

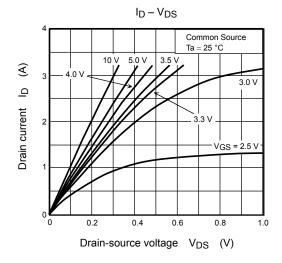
The Schottky barrier diode in this device has large reverse current leakage compared to typical switching diodes. Thus, excessive operating temperature or voltage may cause thermal runaway. To avoid this problem, be sure to take both forward and reverse loss into consideration.

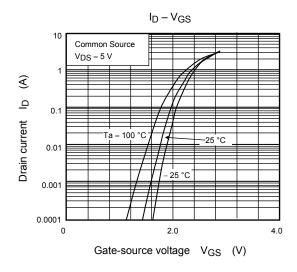
### **Handling Precaution**

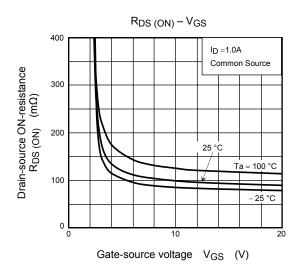
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

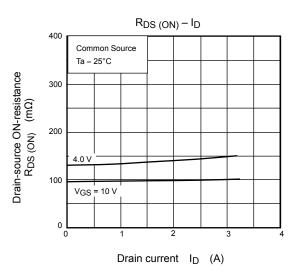
The Channel-to-Ambient thermal resistance  $R_{th\ (ch-a)}$  and the drain power dissipation  $P_D$  vary according to the board material, board area, board thickness and pad area. When using this device, be sure to take heat dissipation fully into account.

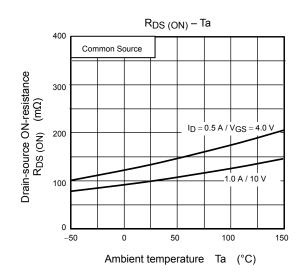
### **MOSFET**

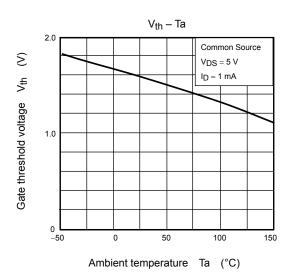






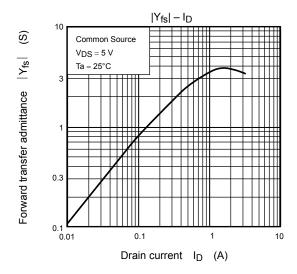


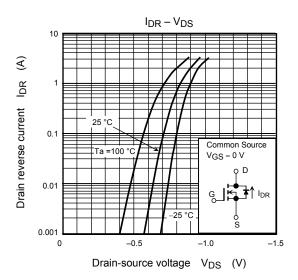


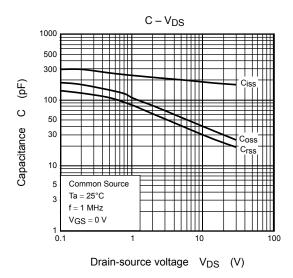


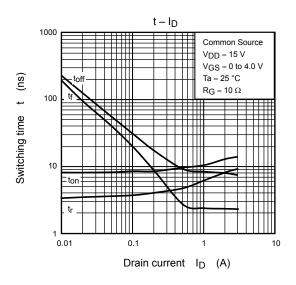
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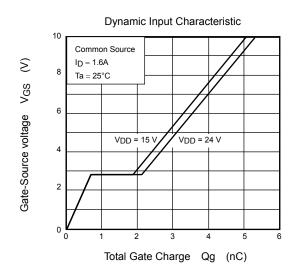
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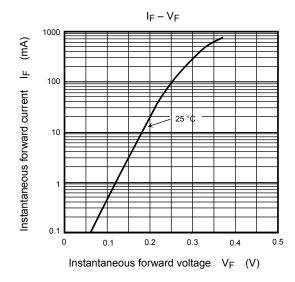


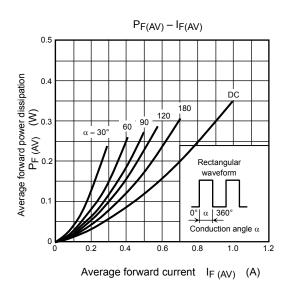


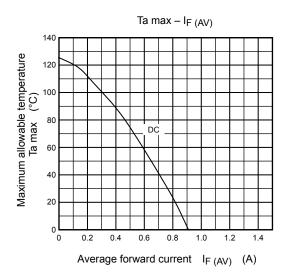


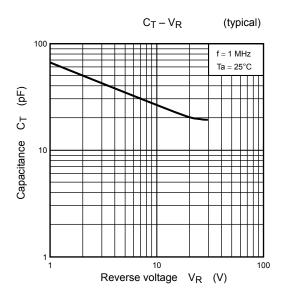


# **Schottky Barrier Diode**

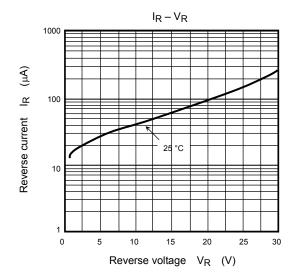


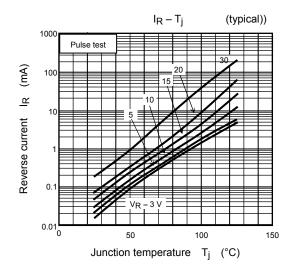


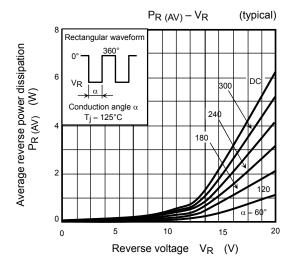




# **Schottky Barrier Diode**







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