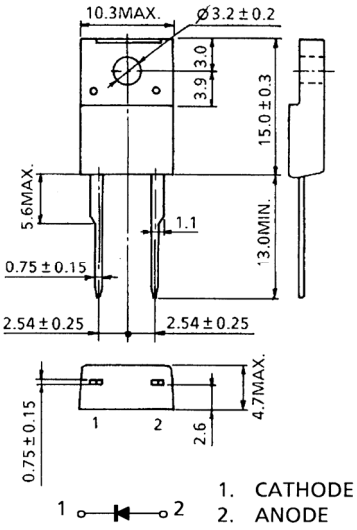
TOSHIBA HIGH EFFICIENCY RECTIFIER SILICON EPITAXIAL TYPE

5JLZ47A

SWITCHING MODE POWER SUPPLY APPLICATION
CONVERTER & CHOPPER APPLICATION

- Repetitive Peak Reverse Voltage : V_{RRM} = 600 V
- Peak Forward Voltage : V_{FM} = 4.0 V
- Ultra Fast Reverse-Recovery Time : t_{rr} = 35 ns

Unit: mm



JEDEC	—
JEITA	—
TOSHIBA	3-10C3A

Weight: 2.0 g (typ.)

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

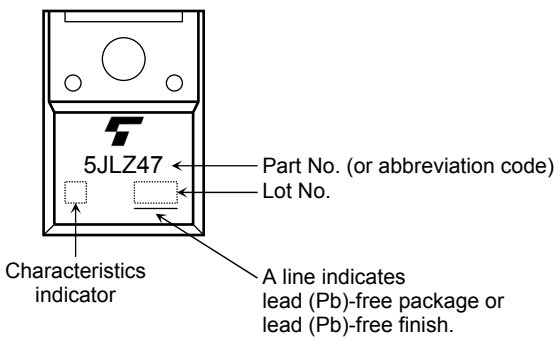
CHARACTERISTIC	SYMBOL	RATING	UNIT
Repetitive Peak Reverse Voltage	V_{RRM}	600	V
Average Forward Current	$I_{F\ (AV)}$	5	A
Peak One Cycle Surge Forward Current (Non−Repetitive)	I_{FSM}	40	A
Junction Temperature	T_j	−40~150	°C
Storage Temperature Range	T_{stg}	−40~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Peak Forward Voltage	V_{FM}	I_{FM} = 5A	—	—	4.0	V
Repetitive Peak Reverse Current	I_{RRM}	V_{RRM} = 600V	—	—	50	μA
Reverse Recovery Time	t_{rr}	I_F = 2A, di / dt = −20A / μs	—	—	35	ns
Thermal Resistance	$R_{th\ (j-c)}$	DC	—	—	4.5	°C / W

MARKING



Abbreviation Code	Part No.
5JLZ47	5JLZ47A

Handling Precaution

The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

V_{RRM}: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the absolute maximum rating of V_{RRM} for a DC circuit and be no greater than 50% of that of V_{RRM} for an AC circuit. V_{RRM} has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

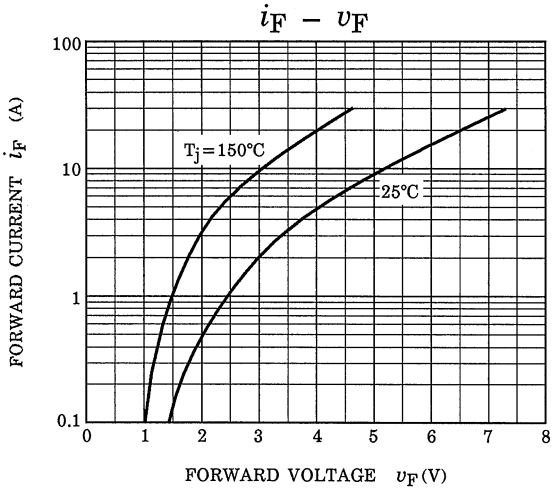
$I_{F(AV)}$: We recommend that the worst case current be no greater than 80% of the absolute maximum rating of $I_{F(AV)}$. Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Tamax-$I_{F(AV)}$ curve.

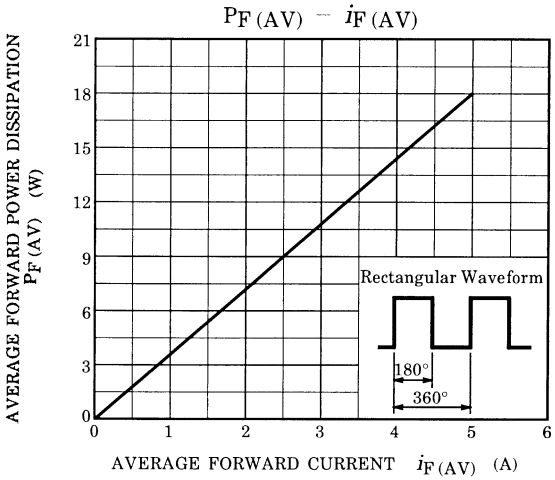
This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle 180. Therefore, this is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

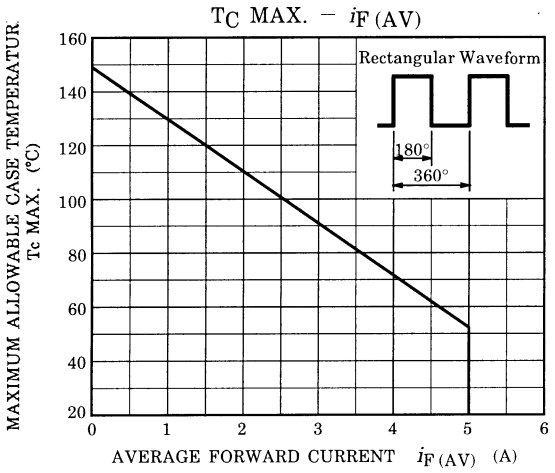
We recommend that a device be used at a Tj of below 120°C under the worst load and heat radiation conditions.

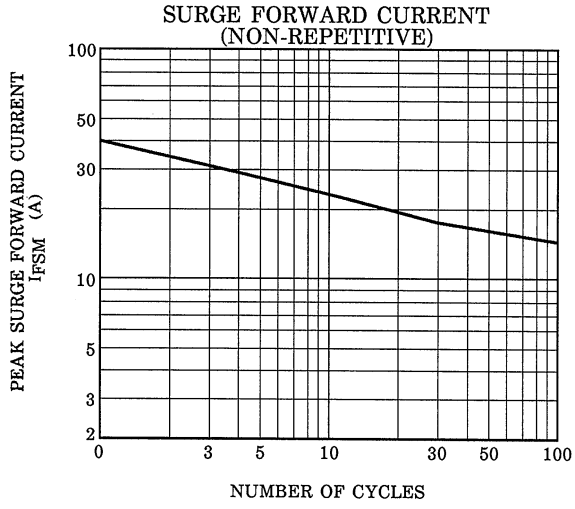
Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

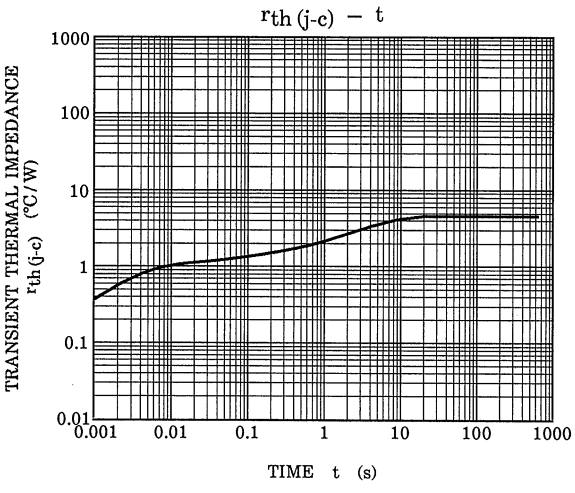
Please refer to the Rectifiers databook for further information.

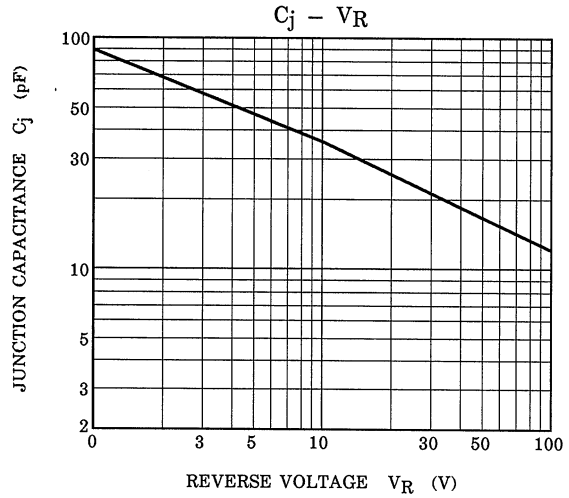

$i_F - v_F$
FORWARD CURRENT i_F (A)
100
10
1
0.1
T_j=150°C
25°C
0 1 2 3 4 5 6 7 8
FORWARD VOLTAGE v_F (V)


$P_{F(AV)} - i_{F(AV)}$
AVERAGE FORWARD POWER DISSIPATION $P_{F(AV)}$ (W)
21 18 15 12 9 6 3 0
Rectangular Waveform
180°
360°
0 1 2 3 4 5 6
AVERAGE FORWARD CURRENT $i_{F(AV)}$ (A)


T_C MAX. – $i_{F(AV)}$
MAXIMUM ALLOWABLE CASE TEMPERATUR Tc MAX. (°C)
160 140 120 100 80 60 40 20
Rectangular Waveform
180°
360°
0 1 2 3 4 5 6
AVERAGE FORWARD CURRENT $i_{F(AV)}$ (A)


SURGE FORWARD CURRENT
(NON-REPETITIVE)
PEAK SURGE FORWARD CURRENT I_{FSM} (A)
100 50 30 10 5 3 2
0 3 5 10 30 50 100
NUMBER OF CYCLES


$r_{th(j-c)} - t$
TRANSIENT THERMAL IMPEDANCE $r_{th(j-c)}$ (°C/W)
1000 100 10 1 0.1 0.01
0.001 0.01 0.1 1 10 100 1000
TIME t (s)


$C_j - V_R$
JUNCTION CAPACITANCE C_j (pF)
100 50 30 10 5 3 2
0 3 5 10 30 50 100
REVERSE VOLTAGE V_R (V)

RESTRICTIONS ON PRODUCT USE

20070701-EN

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
  In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the “Handling Guide for Semiconductor Devices,” or “TOSHIBA Semiconductor Reliability Handbook” etc.
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