Data Sheet | March 1, 2004 | FN7219.1

Laser Driver Oscillator

The EL6204 is a push-pull oscillator used to reduce laser noise. It uses the standard interface to existing ROM controllers. The frequency and amplitude are each set with a separate resistor connected to ground. The tiny package and harmonic reduction allow the part to be placed close to a laser with low RF emissions. An auto turn-off feature allows it to easily be used on combo CD-RW plus DVD-ROM pick-ups.

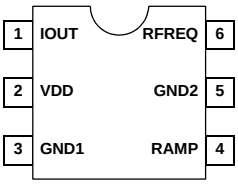
If the APC current is reduced such that the average laser voltage drops to less than 1.1V, the output and oscillator are disabled, reducing power consumption to a minimum.

The current drawn by the oscillator consists of a small utility current, plus the peak output amplitude in the positive cycle. In the negative cycle the oscillator subtracts peak output amplitude from the laser APC current.

The EL6204 part is available in the space-saving 6-pin SOT-23 package and is specified for operation from 0°C to +70°C.

Pinout

EL6204
(6-PIN SOT-23)
TOP VIEW



Features

- Low power dissipation
- User-selectable frequency from 60MHz to 600MHz controlled with a single resistor
- User-specified amplitude from $10mA_{PK-PK}$ to $100mA_{PK-PK}$ controlled with a single resistor
- Auto turn-off threshold
- Soft edges for reduced EMI
- Small 6-pin SOT-23 package

Applications

- DVD players
- DVD-ROM drives
- CD-RW drives
- MO drives
- General purpose laser noise reduction
- Local oscillators

Ordering Information

PART NUMBER	PACKAGE	TAPE & REEL	PKG. DWG. #
EL6204CW-T7	6-Pin SOT-23	7” (3K pcs)	MDP0038
EL6204CW-T7A	6-Pin SOT-23	7” (250 pcs)	MDP0038

CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures.
1-888-INTERSIL or 321-724-7143 | Intersil (and design) is a registered trademark of Intersil Americas Inc.
Copyright © Intersil Americas Inc. 2004. All Rights Reserved. Elantec is a registered trademark of Elantec Semiconductor, Inc.
All other trademarks mentioned are the property of their respective owners.

Absolute Maximum Ratings (T_A = 25°C)

Voltages Applied to:

- V_{DD} . . . . . -0.5V to +6.0V
- I_{OUT} . . . . . -0.5V to +6.0V
- R_{FREQ}, R_{AMP} . . . . . -0.5V to +6.0V

Operating Ambient Temperature Range . . . . . 0°C to +70°C
Maximum Junction Temperature . . . . . +150°C
Storage Temperature Range . . . . . -65°C to +150°C
Output Current . . . . . $100mA_{PK-PK}$
Power Dissipation (max) . . . . . See Curves

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

Supply & Reference Voltage Characteristics

V_{DD} = +5V, T_A = 25°C, R_L = 10Ω, R_{FREQ} = 5210Ω (F_{OSC} = 350MHz), R_{AMP} = 2540Ω (I_{OUT} = $50mA_{P-P}$ measured at 60MHz), V_{OUT} = 2.2V

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
PSOR	Power Supply Operating Range		4.5		5.5	V
I_{SO}	Supply Current Disabled	$V_{OUT} < V_{CUTOFF}$		550	750	µA
I_{STYP}	Supply Current Typical Conditions	R_{FREQ} = 5.21kΩ, R_{AMP} = 2.54kΩ		18.5	22	mA
I_{SLO}	Supply Current Low Conditions	R_{FREQ} = 30.5kΩ, R_{AMP} = 12.7kΩ		4.75		mA
I_{SHI}	Supply Current High Conditions	R_{FREQ} = 3.05kΩ, R_{AMP} = 1.27kΩ		32		mA
V_{FREQ}	Voltage at R_{FREQ} Pin			1.27		V
V_{RAMP}	Voltage on RAMP Pin			1.27		V
V_{CUTOFF}	Monitoring Voltage of I_{OUT} Pin		1.1		1.4	V

Oscillator Characteristics

V_{DD} = +5V, T_A = 25°C, R_L = 10Ω, R_{FREQ} = 5210Ω (F_{OSC} = 350MHz), R_{AMP} = 2540Ω (I_{OUT} = $50mA_{P-P}$ measured at 60MHz), V_{OUT} = 2.2V

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
F_{OSC}	Frequency Tolerance	Unit-unit frequency variation	300	350	400	MHz
F_{HIGH}	Frequency Range High	R_{FREQ} = 3.05kΩ		600		MHz
F_{LOW}	Frequency Range Low	R_{FREQ} = 30.5kΩ		60		MHz
TC_{OSC}	Frequency Temperature Sensitivity	0°C to +70°C ambient		50		ppm/°C
$PSRR_{OSC}$	Frequency Change ΔF/F	V_{DD} from 4.5V to 5.5V		1		%

Driver Characteristics

V_{DD} = +5V, T_A = 25°C, R_L = 10Ω, R_{FREQ} = 30.5kΩ (F_{OSC} = 60MHz), R_{AMP} = 2540Ω (I_{OUT} = $50mA_{P-P}$ measured at 60MHz), V_{OUT} = 2.2V

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
AMP_{HIGH}	Amplitude Range High	R_{AMP} = 1.27kΩ		100		mA_{P-P}
AMP_{LOW}	Amplitude Range Low	R_{AMP} = 12.7kΩ		10		mA_{P-P}
IOS_{NOM}	Offset Current @ 2.2V	R_{FREQ} = 5210Ω, V_{OUT} = 2.2V		-4		mA
IOS_{HIGH}	Offset Current @ 2.8V	R_{FREQ} = 5210Ω, V_{OUT} = 2.8V		-4.8		mA
IOS_{LOW}	Offset Current @ 1.8V	R_{FREQ} = 5210Ω, V_{OUT} = 1.8V		-3.5		mA
I_{OUTP-P}	Output Current Tolerance	Defined as one standard deviation		2		%
Duty Cycle	Output Push Time/Cycle Time	R_{FREQ} = 5210Ω		43		%
$PSRR_{AMP}$	Amplitude Change of Output ΔI/I	V_{DD} from 4.5V to 5.5V		-54		dB
T_{ON}	Auto Turn-on Time	Output voltage step from 0V to 2.2V		15		µs
T_{OFF}	Auto Turn-off Time	Output voltage step from 2.2V to 0V		0.5		µs
$IOUT_N$	Output Current Noise Density	R_{FREQ} = 5210Ω, measured @ 10MHz		2.5		nA/√Hz

Pin Descriptions

PIN NAME	PIN TYPE	PIN DESCRIPTION
1	IOUT	Current output to laser diode
2	VDD	Positive power for laser driver (4.5V - 5.5V)
3	GND1	Chip ground pin (0V for output)
4	RAMP	Set pin for output current amplitude
5	GND2	Chip ground pin (0V for RFREQ, RAMP)
6	RFREQ	Set pin for oscillator frequency

Recommended Operating Conditions

V_{DD} . . . . . 5V ±10%
V_{OUT} . . . . . 2V - 3V
R_{FREQ} . . . . . 3kΩ (min)
R_{AMP} . . . . . 1.25kΩ (min)
F_{OSC} 60-600MHz
I_{OUT} . . . . . 10-100mA_{PK-PK}

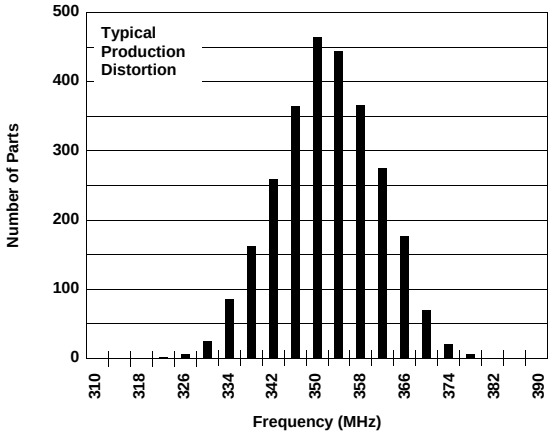
I_{OUT} Control

V_{OUT}	I_{OUT}
Less than V_{CUTOFF}	OFF
More than V_{CUTOFF}	Normal Operation

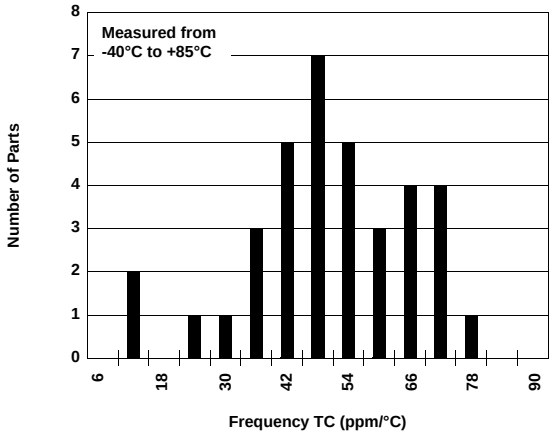
Typical Performance Curves

V_{DD} = 5V, T_A = 25°C, R_L = 10Ω, R_{FREQ} = 5.21kΩ, R_{AMP} = 2.54kΩ, V_{OUT} = 2.2V unless otherwise specified.

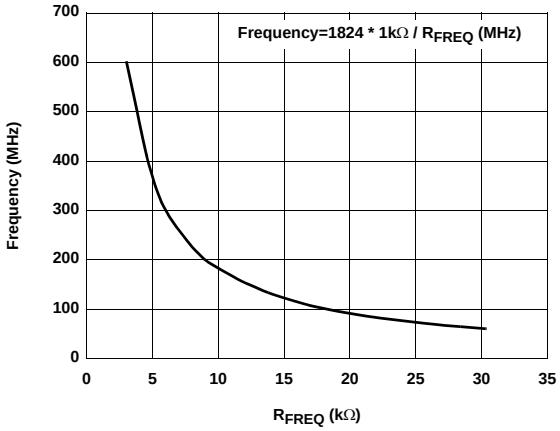
Frequency Distribution



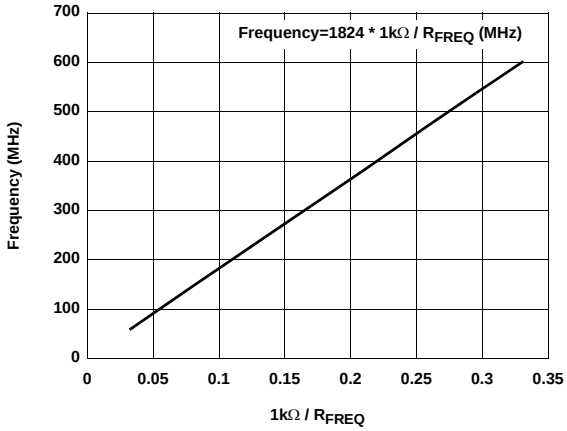
Frequency Drift with Temperature



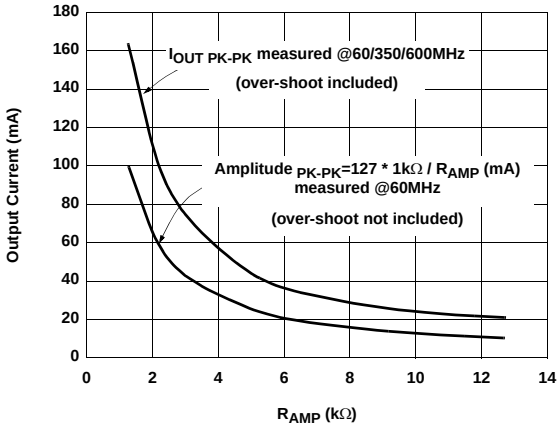
Frequency vs R_{FREQ}



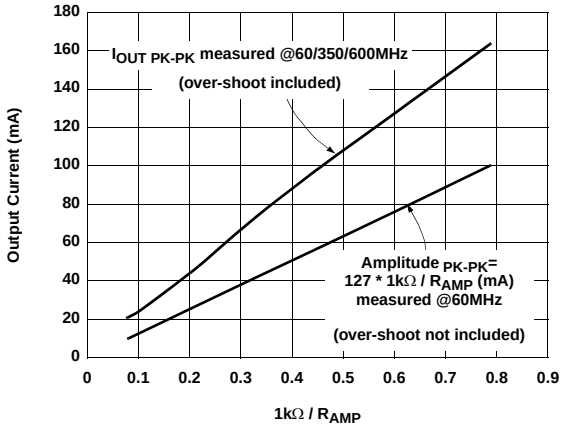
Frequency vs 1 / R_{FREQ}



Output Current vs R_{AMP}

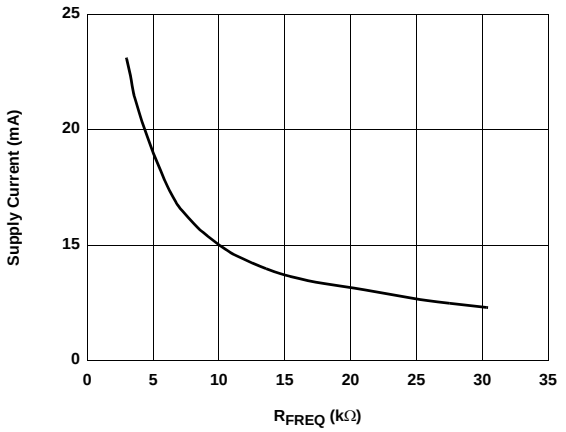


Output Current vs 1 / R_{AMP}

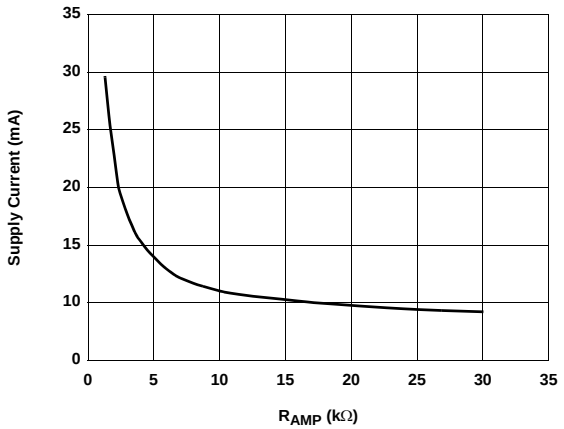


Typical Performance Curves (Continued)

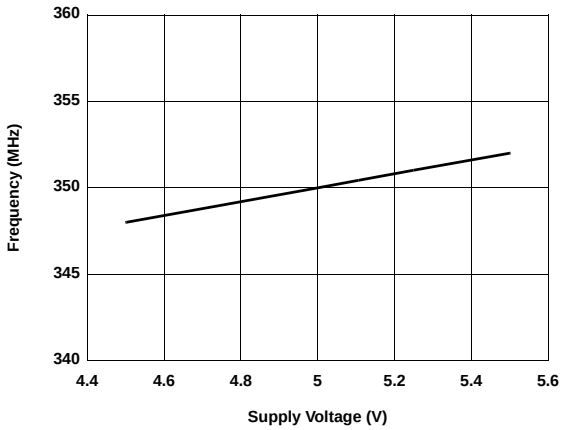
Supply Current vs R_{FREQ}



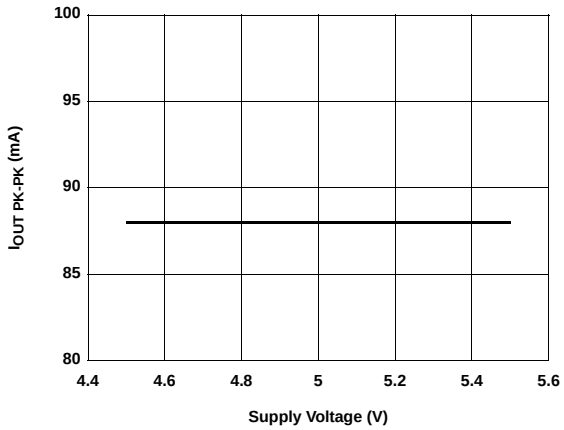
Supply Current vs R_{AMP}



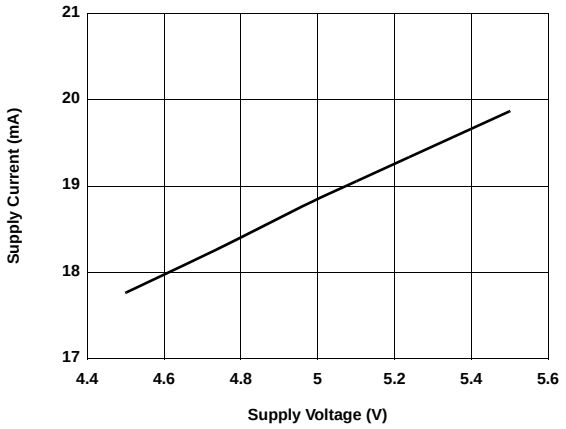
Frequency vs Supply Voltage



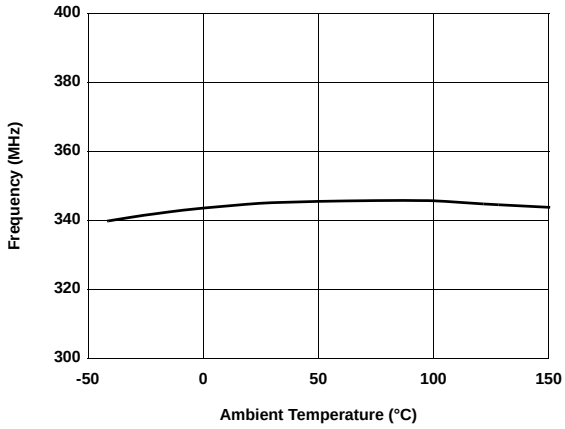
Peak-to-Peak Output Current vs Supply Voltage



Supply Current vs Supply Voltage

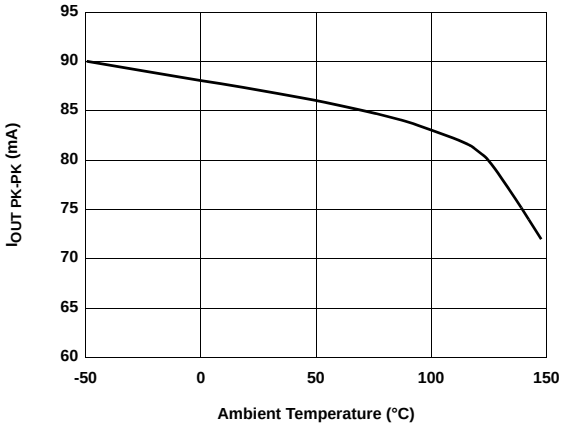


Frequency vs Temperature

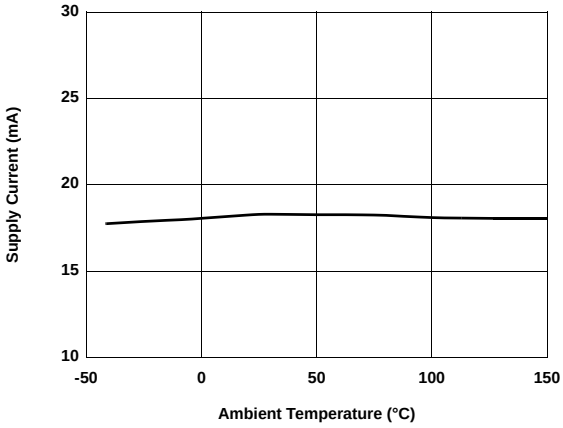


Typical Performance Curves (Continued)

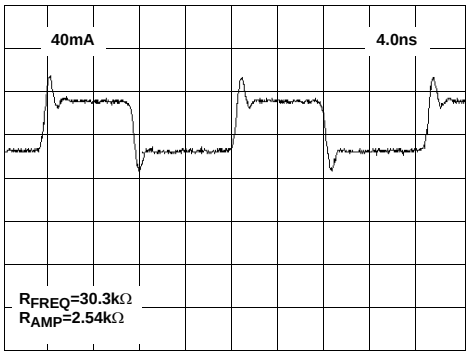
Peak-to-Peak Output Current vs Temperature



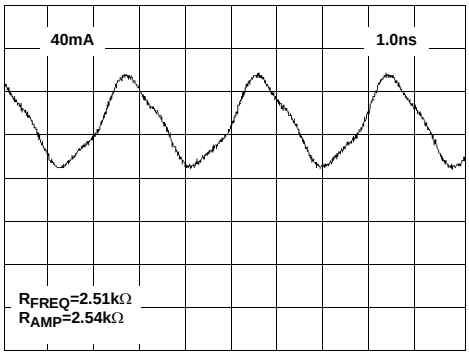
Supply Current vs Temperature



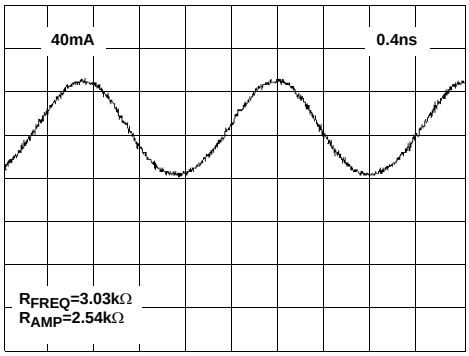
Output Current @ 60MHz



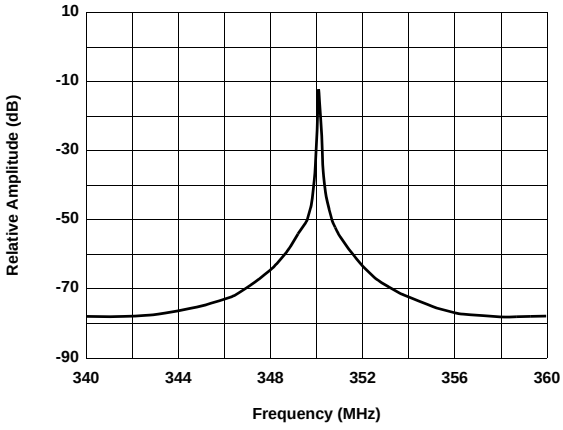
Output Current @ 350MHz



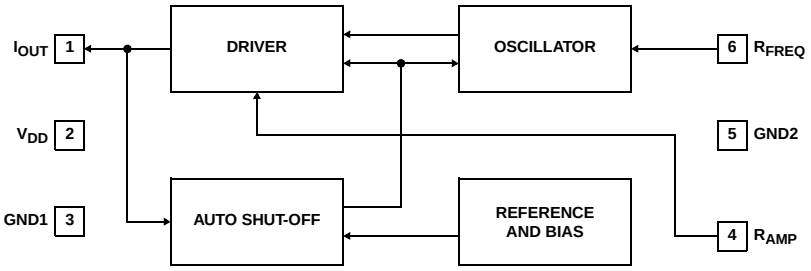
Output Current @ 600MHz



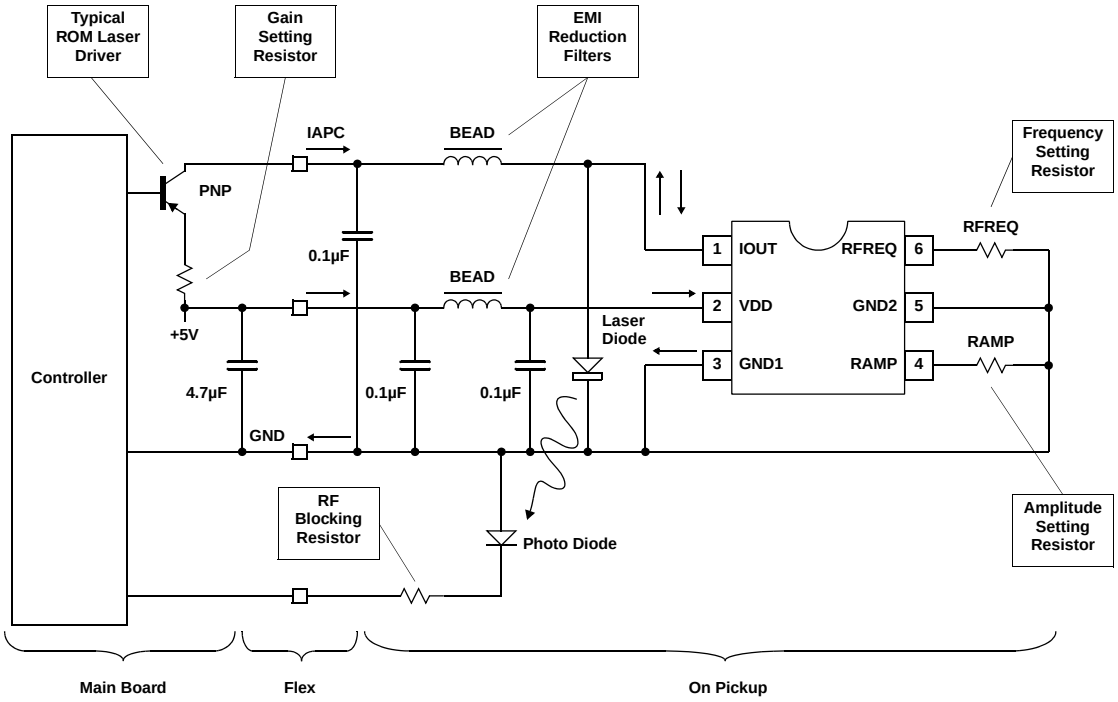
Output Spectrum-Wideband

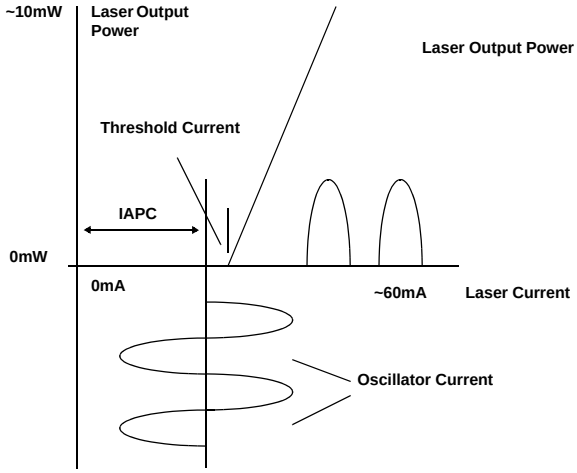


Block Diagram



Typical Application Circuit





Applications Information

Product Description

The EL6204 is a solid state, low-power, high-speed laser modulation oscillator with external resistor-adjustable operating frequency and output amplitude. It is designed to interface easily to laser diodes to break up optical feedback resonant modes and thereby reduce laser noise. The output of the EL6204 is composed of a push-pull current source, switched alternately at the oscillator frequency. The output and oscillator are automatically disabled for power saving when the average laser voltage drops to less than 1.1V. The EL6204 has the operating frequency from 60MHz to 600MHz and the output current from $10mA_{P-P}$ to $100mA_{P-P}$. The supply current is only 18.5mA for the output current of $50mA_{P-P}$ at the operating frequency of 350MHz.

Theory of Operation

A typical semiconductor laser will emit a small amount of incoherent light at low values of forward laser current. But after the threshold current is reached, the laser will emit coherent light. Further increases in the forward current will cause rapid increases in laser output power. A typical threshold current is 35mA and a typical slope efficiency is 0.7mW/mA.

When the laser is lasing, it will often change its mode of operation slightly, due to changes in current, temperature, or optical feedback into the laser. In a DVD-ROM, the optical feedback from the moving disk forms a significant noise factor due to feedback-induced mode hopping. In addition to the mode hopping noise, a diode laser will roughly have a constant noise level regardless of the power level when a threshold current is exceeded.

The oscillator is designed to produce a low noise oscillating current that is added to the external DC current. The effective AC current is to cause the laser power to change at the oscillator frequency. This change causes the laser to go through rapid mode hopping. The low frequency component of laser power noise due to mode hopping is translated up to sidebands around the oscillator frequency by this action. Since the oscillator frequency can be filtered out of the low frequency read and serve channels, the net result is that the laser noise seems to be reduced. The second source of laser noise reduction is caused by the increase in the laser power above the average laser power during the pushing-current time. The signal-to-noise ratio (SNR) of the output power is better at higher laser powers because of the almost constant noise power when a threshold current is exceeded. In addition, when the laser is off during the pulling-current time, the noise is also very low.

R_{AMP} and R_{FREQ} Value Setting

The laser should always have a forward current during operation. This will prevent the laser voltage from collapsing, and ensure that the high frequency components reach the junction without having to charge the junction capacitance.

Generally it is desirable to make the oscillator currents as large as possible to obtain the greatest reduction in laser noise. But it is not a trivial matter to determine this critical value. The amplitude depends on the wave shape of the oscillator current reaching the laser junction.

If the output current is sinusoidal, and the components in the output circuit are fixed and linear, then the shape of the current will be sinusoidal. But the amount of current reaching the laser junction is a function of the circuit parasitics. These parasitics can result in a resonant increase in output depending on the frequency due to the junction capacitance and layout. Also, the amount of junction current causing laser emission is variable with frequency due to the junction capacitance. In conclusion, the sizes of the R_{AMP} and R_{FREQ} resistors must be determined experimentally. A good starting point is to take a value of R_{AMP} for a peak-to-peak current amplitude less than the minimum laser threshold current and a value of R_{FREQ} for an output current close to a sinusoidal wave form (refer to the proceeding performance curves).

R_{AMP} and R_{FREQ} Pin Interfacing

Figure 1 shows an equivalent circuit of pins associated with the R_{AMP} and R_{FREQ} resistors. V_{REF} is roughly 1.27V for both R_{AMP} and R_{FREQ}. The R_{AMP} and R_{FREQ} resistors should be connected to the non-load side of the power ground to avoid noise pick-up. These resistors should also return to the EL6204's ground very directly to prevent noise pickup. They also should have minimal capacitance to ground. Trimmer resistors can be used to adjust initial operating points.

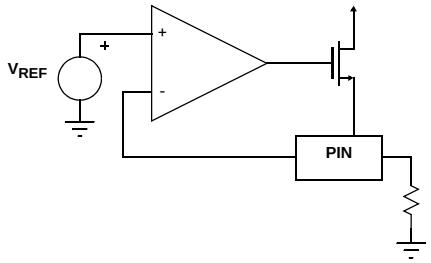


FIGURE 1. R_{AMP} AND R_{FREQ} PIN INTERFACE

External voltage sources can be coupled to the R_{AMP} and R_{FREQ} pins to effect frequency or amplitude modulation or adjustment. It is recommended that a coupling resistor of 1k be installed in series with the control voltage and mounted directly next to the pin. This will keep the inevitable high-frequency noise of the EL6204's local environment from propagating to the modulation source, and it will keep parasitic capacitance at the pin minimized.

Supply Bypassing and Grounding

The resistance of bypass-capacitors and the inductance of bonding wires prevent perfect bypass action, and 150mV$_{P-P}$ noise on the power lines is common. There needs to be a lossy bead inductance and secondary bypass on the supply side to control signals from propagating down the wires. Figure 2 shows the typical connection.

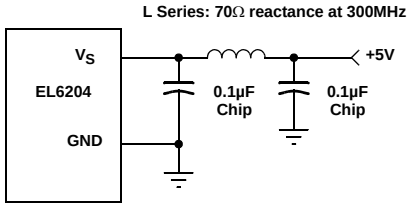


FIGURE 2. RECOMMENDED SUPPLY BYPASSING

Also important is circuit-board layout. At the EL6204's operating frequencies, even the ground plane is not low-impedance. High frequency current will create voltage drops in the ground plane. Figure 3 shows the output current loops.

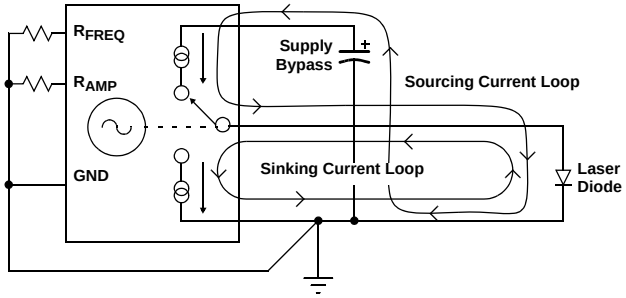


FIGURE 3. OUTPUT CURRENT LOOPS

For the pushing current loop, the current flows through the bypass capacitor, into the EL6204 supply pin, out the I_{OUT} pin to the laser, and from the laser back to the decoupling capacitor. This loop should be small.

For the pulling current loop, the current flows into the I_{OUT} pin, out of the ground pin, to the laser cathode, and from the laser diode back to the I_{OUT} pin. This loop should also be small.

Power Dissipation

With the high output drive capability, the EL6204 is possible to exceed the 125°C “absolute-maximum junction temperature” under certain conditions. Therefore, it is important to calculate the maximum junction temperature for the application to determine if the conditions need to be modified for the oscillator to remain in the safe operating area.

The maximum power dissipation allowed in a package is determined according to:

$$P_{DMAX} = \frac{T_{JMAX} - T_{AMAX}}{\Theta_{JA}}$$

where

P_{DMAX} = Maximum power dissipation in the package

T_{JMAX} = Maximum junction temperature

T_{AMAX} = Maximum ambient temperature

θ_{JA} = Thermal resistance of the package

The supply current of the EL6204 depends on the peak-to-peak output current and the operating frequency which are determined by resistors R_{AMP} and R_{FREQ}. The supply current can be predicted approximately by the following equation:

$$I_{SUP} = \frac{31.25mA \times 1k\Omega}{R_{AMP}} + \frac{30mA \times 1k\Omega}{R_{FREQ}} + 0.6mA$$

The power dissipation can be calculated from the following equation:

$$P_D = V_{SUP} \times I_{SUP}$$

Here, V_{SUP} is the supply voltage. Figures 4 and 5 provide a convenient way to see if the device will overheat. The maximum safe power dissipation can be found graphically, based on the package type and the ambient temperature. By using the previous equation, it is a simple matter to see if P_D exceeds the device's power derating curve. To ensure proper operation, it is important to observe the recommended derating curve shown in Figures 4 and 5. A flex circuit may have a higher θ_{JA}, and lower power dissipation would then be required.

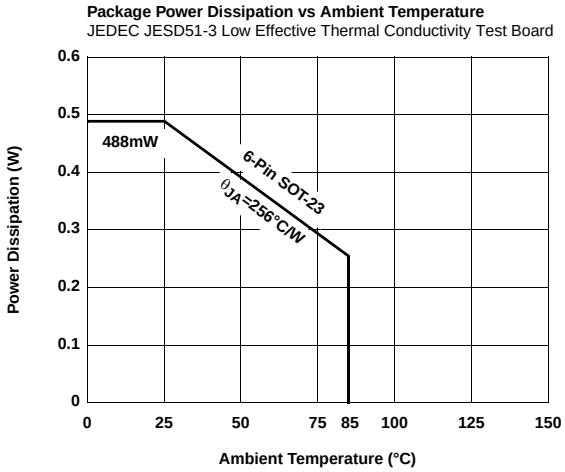


FIGURE 4. PACKAGE POWER DISSIPATION vs AMBIENT TEMPERATURE

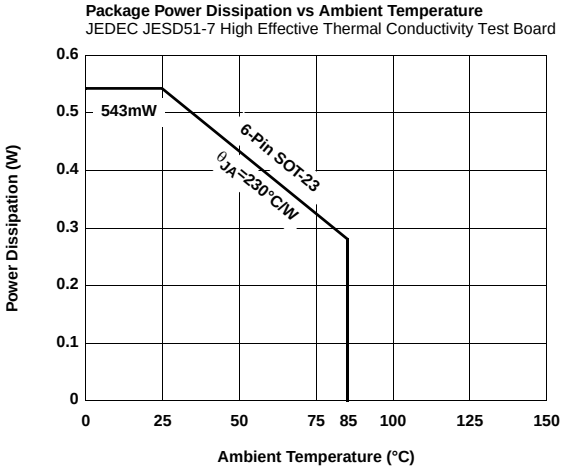


FIGURE 5. PACKAGE POWER DISSIPATION vs AMBIENT TEMPERATURE

All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems.
Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

Intersil products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com