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Part Number: [0780658001](#)
Status: **Active**
Description: 1.00mm (.039") Pitch DDR2 DIMM Socket, Very Low Profile, Vertical, Press-fit, 1.8V Center Keys, 3.30mm (.130") Tail Length, Black Latches, 240 Circuits, 0.76µm (30µm) Gold(Au) Plating, Lead-free

Documents:
[3D Model](#) [Product Specification PS-78065-001 \(PDF\)](#)
[Drawing \(PDF\)](#) [RoHS Certificate of Compliance \(PDF\)](#)

Agency Certification

CSA LR19980
UL E29179

General

Product Family Memory Module Sockets
Series [78065](#)
Comments Latches in Black Color
Component Type Memory Module
JEDEC Outline MO-237
Product Name DDR2 DIMM

Physical

Circuits (Loaded) 240
Color - Resin Black
Durability (mating cycles max) 25
Entry Angle Vertical (Top Entry)
Flammability 94V-0
Keying to Mating Part Yes
Material - Metal Phosphor Bronze
Material - Plating Mating Gold
Material - Plating Termination Tin
Material - Resin High Temperature Thermoplastic
PC Tail Length (in) 0.130 In
PC Tail Length (mm) 3.30 mm
PCB Locator No
PCB Retention Yes
PCB Thickness Recommended (in) 0.140 In
PCB Thickness Recommended (mm) 3.60 mm
Packaging Type Tray
Pitch - Mating Interface (in) 0.039 In
Pitch - Mating Interface (mm) 1.00 mm
Plating min: Mating (µin) 30
Plating min: Mating (µm) 0.76
Plating min: Termination (µin) 15.2
Plating min: Termination (µm) 0.38
Temperature Range - Operating -55°C to +85°C
Termination Interface: Style Through Hole - Compliant Pin

Electrical

Current - Maximum per Contact 0.5A
Voltage - Maximum 30V
Voltage Key 1.8V, Center

Material Info

Reference - Drawing Numbers

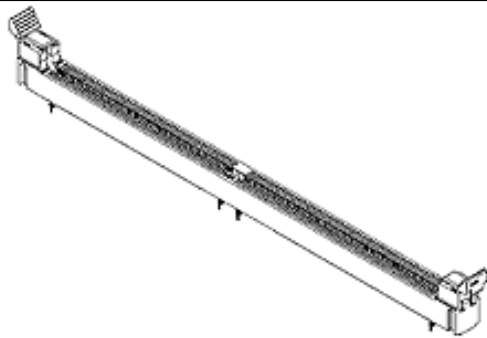


image - Reference only

Series

EU RoHS
ELV and RoHS
Compliant
REACH SVHC
Contains SVHC: No
Halogen-Free
Status
Not Halogen-Free

China RoHS



Need more information on product environmental compliance?

Email productcompliance@molex.com
For a multiple part number RoHS Certificate of Compliance, [click here](#)

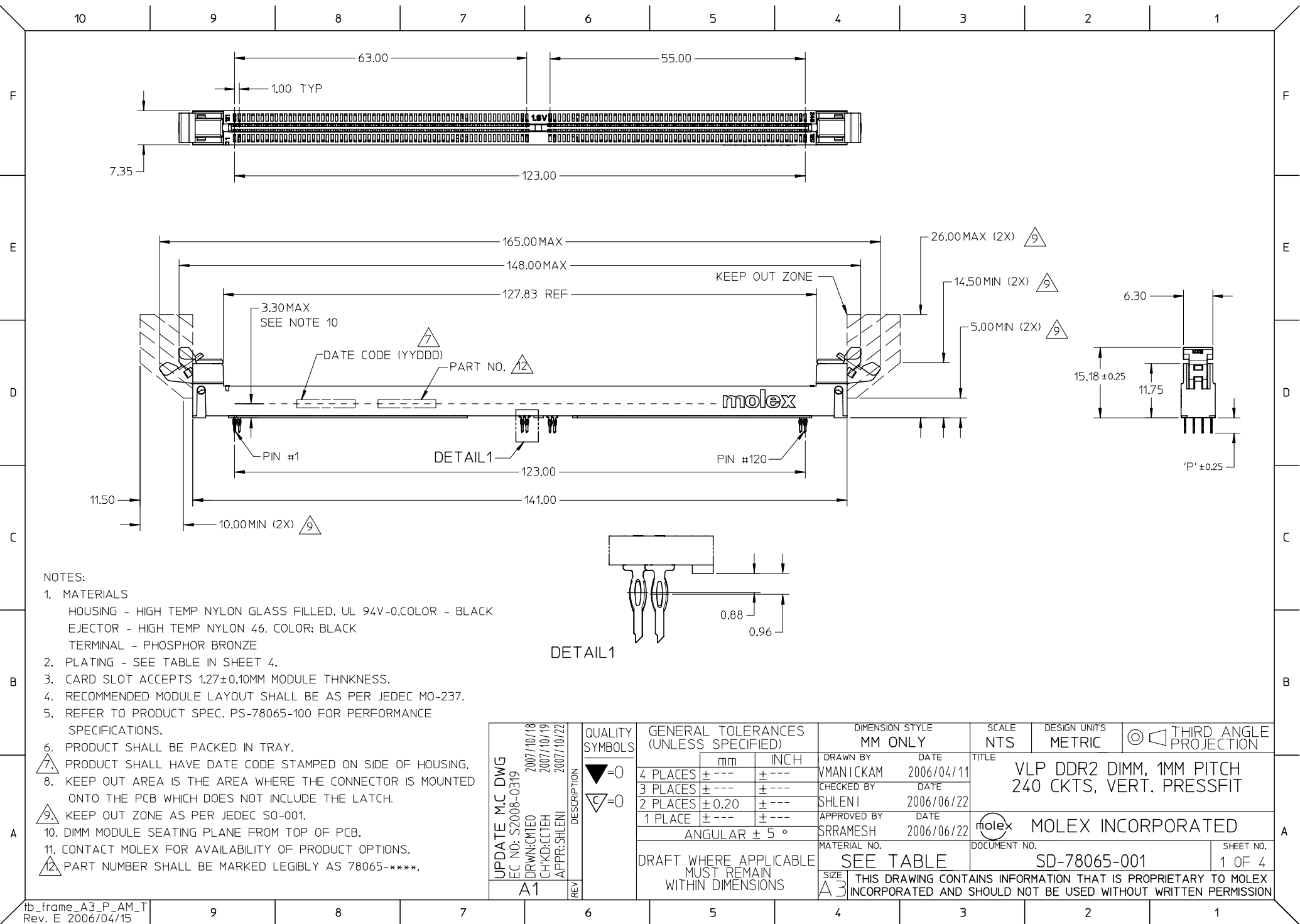
Please visit the [Contact Us](#) section for any non-product compliance questions.

Search Parts in this Series
[78065Series](#)

Mates With
JEDEC MO-237 modules

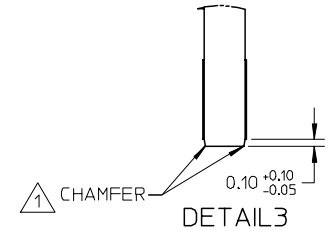
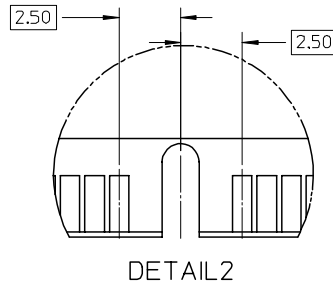
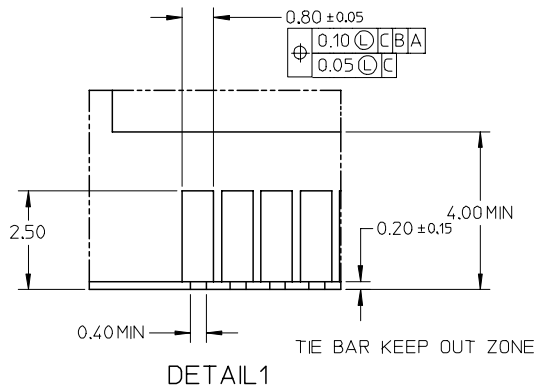
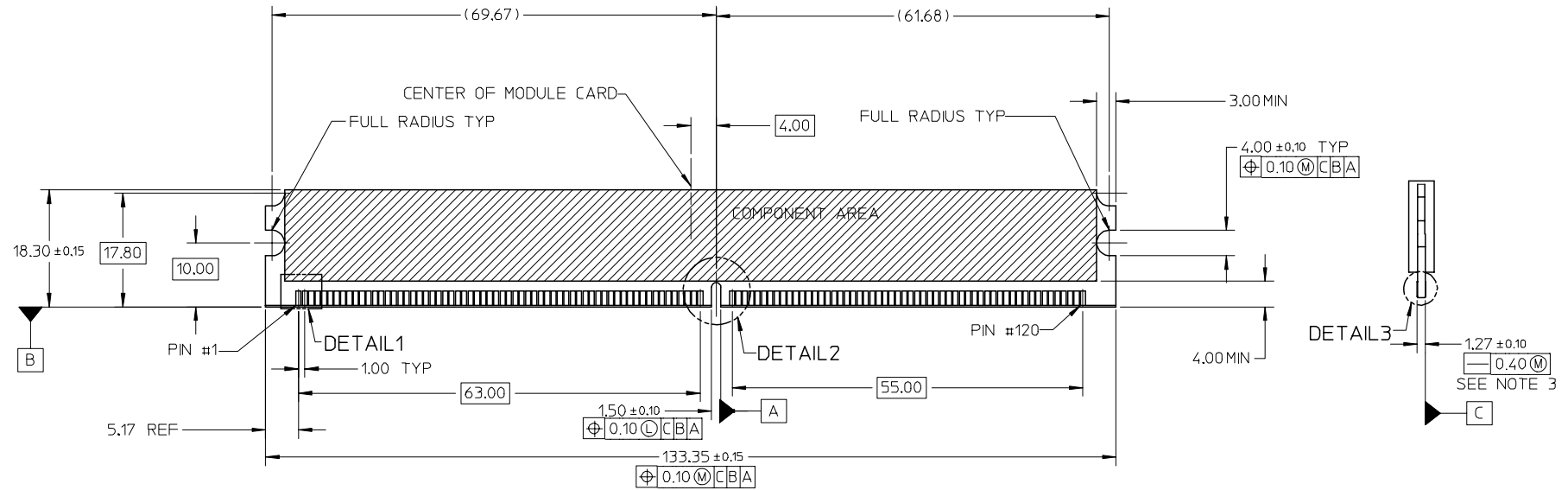
This document was generated on 04/13/2010

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10 9 8 7 6 5 4 3 2 1

RECOMMENDED MODULE LAYOUT



KEY LOCATION	VOLTAGE
CENTER	1.8V

NOTES:
 1. MODULE CARD USED FOR TESTS ARE WITH 0.10MM CHAMFER.

UPDATE M.C DWG		QUALITY SYMBOLS		GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
EC NO: S2008-0319	2007/10/18	DRWN:CMTEO	2007/10/19	CHKD:CTEH	2007/10/22	APPR:SHLENI	2007/10/22				
4 PLACES ± ---		mm		INCH		DRAWN BY VMANICKAM		DATE 2006/04/11		TITLE VLP DDR2 DIMM, 1MM PITCH	
3 PLACES ± ---		mm		INCH		CHECKED BY SHLENI		DATE 2006/06/22		240 CKTS, VERT. PRESSFIT	
2 PLACES ± 0.20		mm		INCH		APPROVED BY SRRAMESH		DATE 2006/06/22		MOLEX INCORPORATED	
1 PLACE ± ---		mm		INCH		MATERIAL NO.		DOCUMENT NO.		SD-78065-001	
ANGULAR ± 5 °		mm		INCH		SEE TABLE		SHEET NO. 2 OF 4			
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

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F

E

D

C

B

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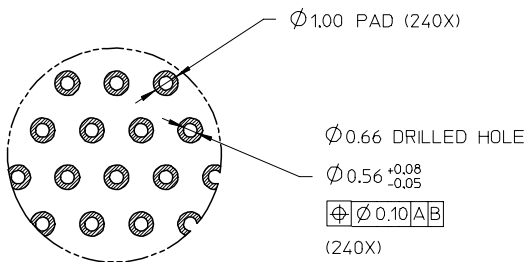
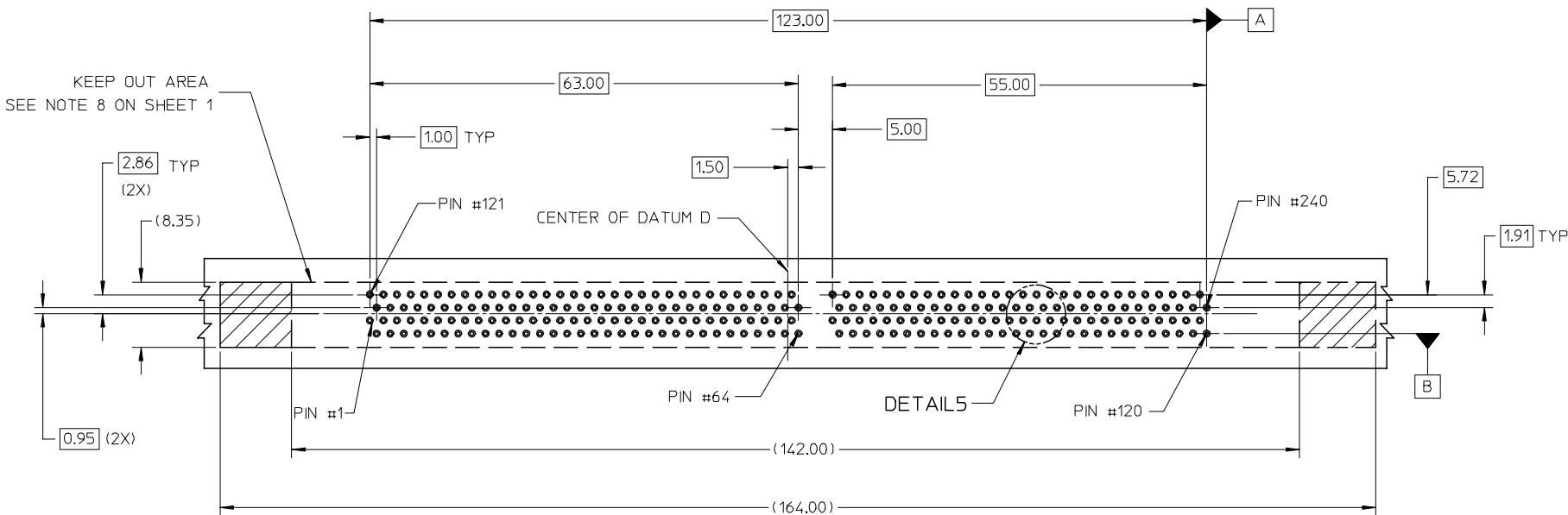
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B

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(SEE RECOMMENDED THICKNESS IN TABLE)
RECOMMEND PCB LAYOUT
CONNECTOR SIDE



DETAIL5

UPDATE M.C DWG EC NO: S2008-0319 DRWN:CMTEO 2007/10/18 CHKD:CTEH 2007/10/19 APPR:SHLENI 2007/10/22 A1		DESCRIPTION	QUALITY SYMBOLS ▽=0 ▽C=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
								TITLE		VLP DDR2 DIMM, 1MM PITCH 240 CKTS, VERT. PRESSFIT	

9 8 7 6 5 4 3 2 1

	10	9	8	7	6	5	4	3	2	1																																																								
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