

Side Face Silicon Phototransistor PT5529B/L2/F354



Features

- Fast response time
- High photo sensitivity
- Pb free
- This product itself will remain within RoHS compliant version.
- Compliance with EU REACH
- Compliance Halogen Free .(Br <900 ppm ,Cl <900 ppm , Br+Cl < 1500 ppm)

Description

- PT5529B/L2/F354 is a high speed and high sensitive dual phototransistor molded in a black plastic package with plat side view.
- The device is spectrally matched with IR emitters.

Applications

- Mouse
- Optoelectronic Switch
- Photo Interrupter

Device Selection Guide

Chip Materials	Lens Color
Si	Black

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Collector-Emitter Voltage	V _{CEO}	30	V
Emitter-Collector-Voltage	V _{ECO}	5	V
Collector Current	I _c	20	mA
Operating Temperature	T _{opr}	-25~+85	°C
Storage Temperature	T _{stg}	-40~ +85	°C
Lead Soldering Temperature	T _{sol}	260	°C
Power Dissipation at (or below) 25°C Free Air Temperature	P _c	75	mW

Notes: *1: Soldering time ≤ 5 seconds.

Electro-Optical Characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Collector – Emitter Breakdown Voltage	BV_{CEO}	30	-----	-----	V	$I_C=100\mu A$ $E_e=0mW/cm^2$
Emitter-Collector Breakdown Voltage	BV_{ECO}	5	-----	-----	V	$I_E=100\mu A$ $E_e=0mW/cm^2$
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	-----	-----	0.4	V	$I_C=2mA$ $E_e=1mW/cm^2$
Rise Time	t_r	-----	15	-----	μS	$V_{CE}=5V$ $I_C=1mA$ $R_L=1000\Omega$
Fall Time	t_f	-----	15	-----		
Collector Dark Current	I_{CEO}	-----	-----	100	nA	$E_e=0mW/cm^2$ $V_{CE}=20V$
On State Collector Current	$I_{C(on)}$	129	-----	1085	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
Rang Of Spectral Bandwidth	$\lambda_{0.5}$	760	-----	1100	nm	----
Wavelength of Peak Sensitivity	λ_P	-----	940	-----	nm	----

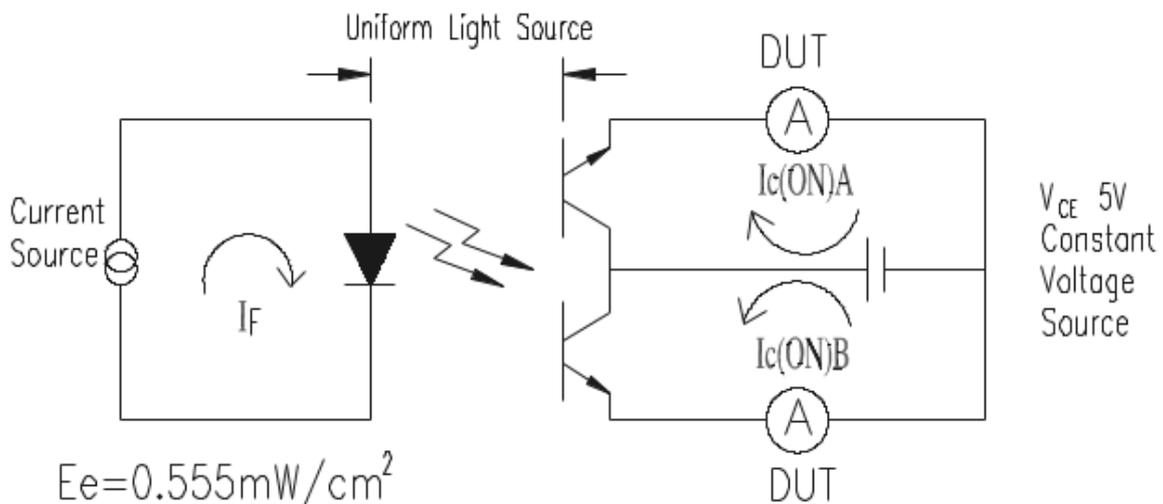
Test Method For On State Collector Current:

Condition: $E_e=0.555mW/cm^2$

$V_{CE}=5V$

Test Item: Collector Current [$I_{C(on)}$]

Unit: μA



To Distinguish Intensity:

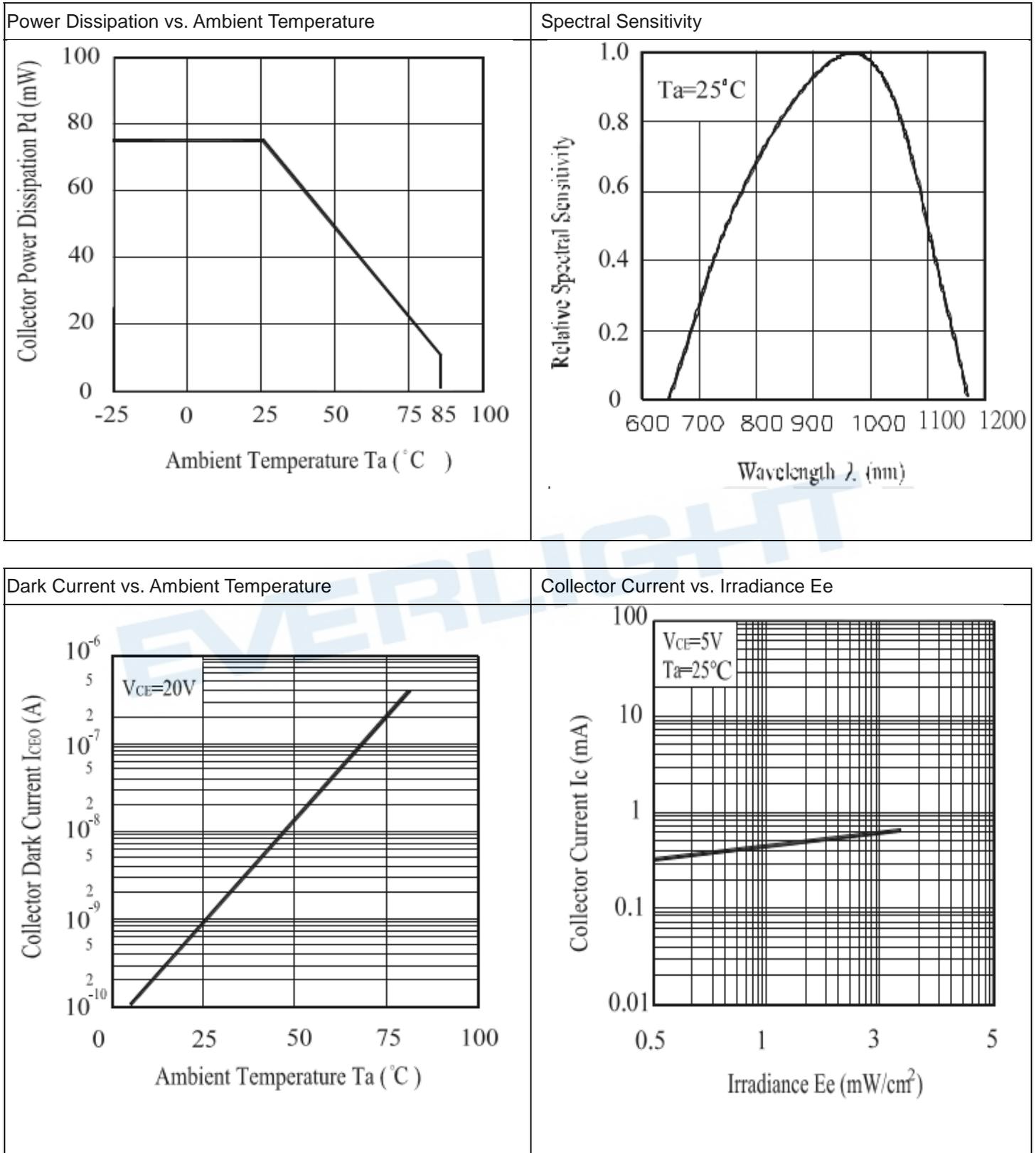
Condition: $V_{CE}=5V$, $E_e=0.555mW/cm^2$

Ranks

Ranks	Symbol	Min	Typ	Max	Unit	Text Condition
A1	IC(on)	129	-----	226	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A2	IC(on)	195	-----	306	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A3	IC(on)	262	-----	380	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A4	IC(on)	330	-----	461	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A5	IC(on)	398	-----	544	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A6	IC(on)	468	-----	625	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A7	IC(on)	536	-----	703	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A8	IC(on)	604	-----	785	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A9	IC(on)	673	-----	862	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A10	IC(on)	742	-----	944	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A11	IC(on)	812	-----	1018	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$
A12	IC(on)	882	-----	1085	μA	$E_e=0.555mW/cm^2$ $V_{CE}=5V$

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Typical Electro-Optical Characteristics Curver



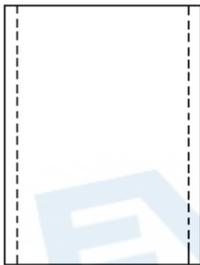
Label Explanation



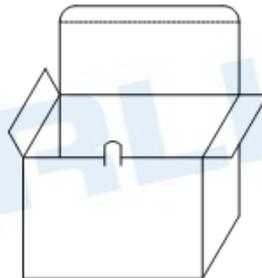
- CPN: Customer's Product Number
- P/N: Product Number
- QTY: Packing Quantity
- CAT: Luminous Intensity Rank
- HUE: Dom. Wavelength Rank
- REF: Forward Voltage Rank
- LOT No: Lot Number
- Reference: Identify Label Number

Packing Specification

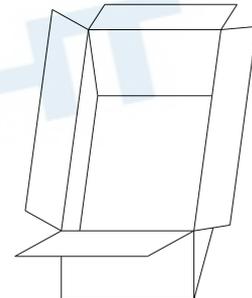
■ PE bag



■ Inner Carton



■ Outside Carton



Packing Quantity Specification

1. 1000PCS/1Bag, 10Bag/1Box
2. 10 Inner Cartons/1 Outside Carton

Notes

1. Lead Forming

- During lead formation, the leads should be bent at a point at least 3mm from the base of the epoxy bulb.
- Lead forming should be done before soldering.
- Avoid stressing the phototransistor package during leads forming. The stress to the base may damage the phototransistor's characteristics or it may break the phototransistors.
- Cut the phototransistor lead frames at room temperature. Cutting the lead frames at high temperatures may cause failure of the phototransistors.
- When mounting the phototransistors onto a PCB, the PCB holes must be aligned exactly with the lead position of the phototransistor. If the phototransistors are mounted with stress at the leads, it causes deterioration of the epoxy resin and this will degrade the phototransistors.

2. Storage

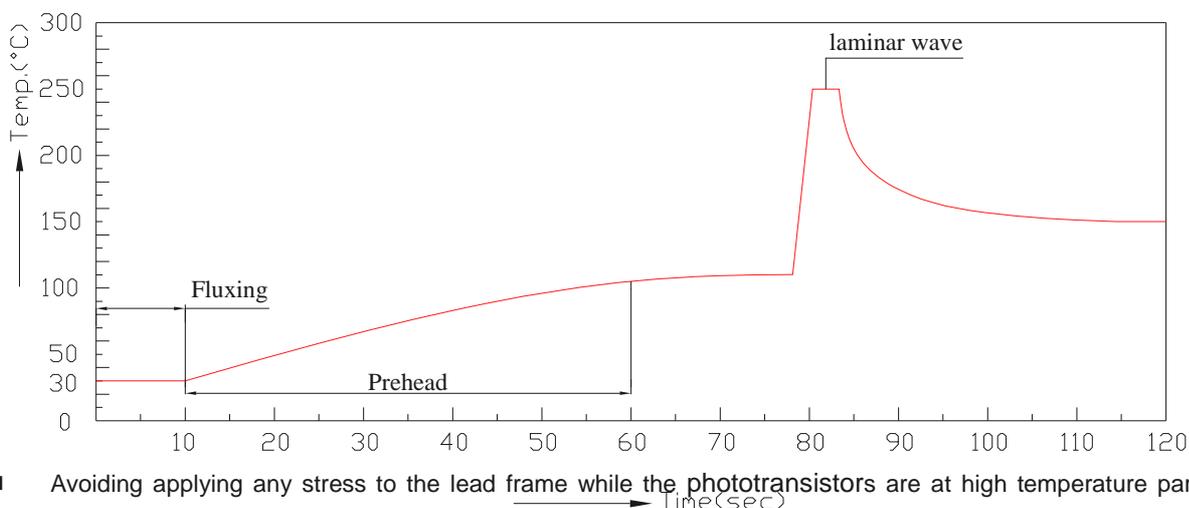
- The phototransistors should be stored at 30°C or less and 70%RH or less after being shipped from Everlight and the storage life limits are 3 months. If the phototransistors are stored for 3 months or more, they can be stored for a year in a sealed container with a nitrogen atmosphere and moisture absorbent material.
- Please avoid rapid transitions in ambient temperature, especially, in high humidity environments where condensation can occur.

3. Soldering

- Careful attention should be paid during soldering. When soldering, leave more than 3mm from solder joint to epoxy bulb, and soldering beyond the base of the tie bar is recommended.
- Recommended soldering conditions:

Hand Soldering		DIP Soldering	
Temp. at tip of iron	300°C Max. (30W Max.)	Preheat temp.	100°C Max. (60 sec Max.)
Soldering time	3 sec Max.	Bath temp. & time	260 Max., 5 sec Max
Distance	3mm Min. (From solder joint to epoxy bulb)	Distance	3mm Min. (From solder joint to epoxy bulb)

- Recommended soldering profile



- Avoiding applying any stress to the lead frame while the phototransistors are at high temperature particularly when soldering.
- Dip and hand soldering should not be done more than one time

- After soldering the phototransistors, the epoxy bulb should be protected from mechanical shock or vibration until the phototransistors return to room temperature
 - A rapid-rate process is not recommended for cooling the phototransistors down from the peak temperature.
 - Although the recommended soldering conditions are specified in the above table, dip or hand soldering at the lowest possible temperature is desirable for the phototransistors.
 - Wave soldering parameter must be set and maintain according to recommended temperature and dwell time in the solder wave.
4. Cleaning
- When necessary, cleaning should occur only with isopropyl alcohol at room temperature for a duration of no more than one minute. Dry at room temperature before use.
 - Do not clean the phototransistors by the ultrasonic. When it is absolutely necessary, the influence of ultrasonic cleaning on the LEDs depends on factors such as ultrasonic power and the assembled condition. Ultrasonic cleaning shall be pre-qualified to ensure this will not cause damage to the phototransistor
5. Heat Management
- Heat management of phototransistors must be taken into consideration during the design stage of phototransistor application. The current should be de-rated appropriately by referring to the de-rating curve found in each product specification.
 - The temperature surrounding the phototransistor in the application should be controlled. Please refer to the data sheet de-rating curve.
6. ESD (Electrostatic Discharge)
- Electrostatic discharge (ESD) or surge current (EOS) can damage phototransistors.
 - An ESD wrist strap, ESD shoe strap or antistatic gloves must be worn whenever handling phototransistors.
 - All devices, equipment and machinery must be properly grounded.
 - Use ion blower to neutralize the static charge which might have built up on surface of the phototransistors plastic lens as a result of friction between phototransistors during storage and handing.

DISCLAIMER

1. EVERLIGHT reserves the right(s) on the adjustment of product material mix for the specification.
2. The product meets EVERLIGHT published specification for a period of twelve (12) months from date of shipment.
3. The graphs shown in this datasheet are representing typical data only and do not show guaranteed values.
4. When using this product, please observe the absolute maximum ratings and the instructions for using outlined in these specification sheets. EVERLIGHT assumes no responsibility for any damage resulting from the use of the product which does not comply with the absolute maximum ratings and the instructions included in these specification sheets.
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