



3- Ω , 235-MHz Bandwidth, Dual SPDT Analog Switch

DESCRIPTION

The DG2515, DG2516 are low-voltage dual single-pole/double-throw monolithic CMOS analog switches. Designed to operate from 1.8 V to 5.5 V power supply, the DG2515, DG2516 achieves a bandwidth of 235 MHz while providing low on-resistance ($3\ \Omega$), excellent on-resistance matching ($0.2\ \Omega$) and flatness ($1\ \Omega$) over the entire signal range.

The DG2515, DG2516 offers the advantage of high linearity that reduces signal distortion, making ideal for audio, video, and USB signal routing applications. Additionally, the DG2515, DG2516 are 1.6 V logic compatible within the full operation voltage range.

Built on Vishay Siliconix's proprietary sub-micron high-density process, the DG2515, DG2516 brings low power consumption at the same time as reduces PCB spacing with the MSOP10 package.

As a committed partner to the community and the environment, Vishay Siliconix manufactures this product with the lead (Pb)-free device terminations. For analog switching products manufactured with 100 % matte tin device termination, the lead (Pb)-free "- E3" suffix is being used as a designator.

FEATURES

- 1.8 V to 5.5 V single supply operation
- Low R_{ON} : $3\ \Omega$ at 4.2 V
- 235 MHz, - 3 dB bandwidth
- Low off-isolation, - 51 dB at 10 MHz
- + 1.6 V logic compatible

BENEFITS

- High linearity
- Low power consumption
- High bandwidth
- Full rail signal swing range

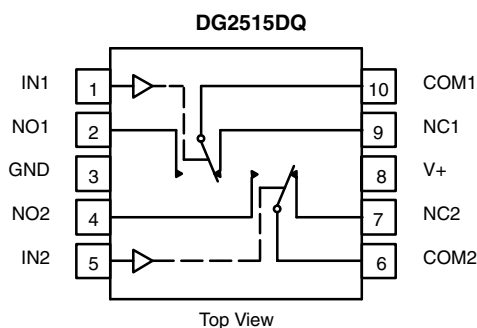
APPLICATIONS

- USB/UART signal switching
- Audio/video switching
- Cellular phone
- Media players
- Modems
- Hard drives
- PCMCIA

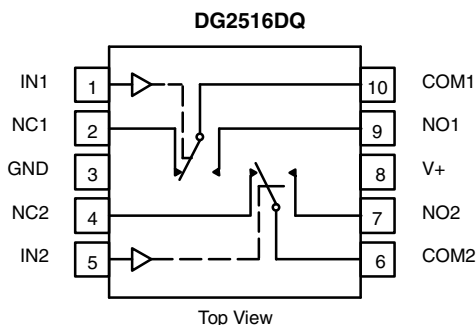


RoHS
COMPLIANT

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



TRUTH TABLE		
Logic	NC1 and NC2	NO1 and NO2
0	ON	OFF
1	OFF	ON



ORDERING INFORMATION		
Temp Range	Package	Part Number
- 40 °C to 85 °C	MSOP-10	DG2515DQ-T1-E3
		DG2516DQ-T1-E3

ABSOLUTE MAXIMUM RATINGS

Parameter	Limit	Unit
Reference to GND		
V+	- 0.3 to + 6	V
IN, COM, NC, NO ^a	- 0.3 to (V+ + 0.3)	
Continuous Current (Any terminal)	± 50	mA
Peak Current (Pulsed at 1 ms, 10 % Duty Cycle)	± 200	
Storage Temperature (D Suffix)	- 65 to 150	°C
Power Dissipation (Packages) ^b	MSOP-10 ^c	mW

Notes:

a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC board.

c. Derate 4.0 mW/°C above 70 °C.

SPECIFICATIONS V+ = 3 V

Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 3 V, ± 10 %, V _{IN} = 0.5 or 1.4 V ^e	Temp. ^a	Limits - 40 °C to 85 °C			Unit
				Min. ^b	Typ. ^c	Max. ^b	
Analog Switch							
Analog Signal Range ^d	V _{NO} , V _{NC} , V _{COM}		Full	0		V+	V
On-Resistance	R _{ON}	V+ = 2.7 V, V _{COM} = 1.5 V I _{NO/NC} = 10 mA	Room Full		3.2	4.5 5.0	Ω
R _{ON} Flatness	R _{ON} Flatness	V+ = 2.7 V, V _{COM} = 1.5, 2 V I _{NO/NC} = 10 mA	Room Full		1.0	1.4 16	
R _{ON} Match Between Channels	ΔR _{ON}	V+ = 2.7 V, V _{COM} = 1.5 V I _{NO/NC} = 10 mA	Room Full		0.1	0.3 0.4	
Switch Off Leakage Current ^f	I _{NO(off)} , I _{NC(off)}	V+ = 3.6 V, V _{NO} , V _{NC} = 0.3 V/3 V V _{COM} = 3 V/0.3 V	Room Full	- 1 - 10		1 10	nA
	I _{COM(off)}		Room Full	- 1 - 10		1 10	
Channel-On Leakage Current ^f	I _{COM(on)}	V+ = 3.6 V, V _{NO} , V _{NC} = V _{COM} = 0.3 V/3 V	Room Full	- 1 - 10		1 10	
Digital Control							
Input High Voltage ^d	V _{INH}		Full	1.4			V
Input Low Voltage	V _{INL}		Full			0.5	
Input Capacitance	C _{in}		Full		12		pF
Input Current	I _{INL} or I _{INH}	V _{IN} = 0 V or V+	Full	1		1	μA
Dynamic Characteristics							
Turn-On Time	t _{ON}	V+ = 2.7 V, V _{NO} or V _{NC} = 1.5 V R _L = 300 Ω, C _L = 35 pF	Room Full		30	70 100	ns
Turn-Off Time	t _{OFF}		Room Full		25	50 70	
Break-Before-Make Time	t _d	V _{NO} or V _{NC} = 1.5 V, R _L = 300 Ω, C _L = 35 pF	Full	1			
Charge Injection ^d	Q _{INJ}	C _L = 1 nF, V _{GEN} = 0 V, R _{GEN} = 0 Ω	Room		24		pC
- 3 dB Bandwidth	BW	0 dBm, C _L = 5 pF, R _L = 50 Ω	Room		235		MHz
Off-Isolation ^d	OIRR	R _L = 50 Ω, C _L = 5 pF	f = 1 MHz	Room	- 71		dB
			f = 10 MHz	Room	- 51		
Crosstalk ^d	X _{TALK}	R _L = 50 Ω, C _L = 5 pF	f = 1 MHz	Room	- 74		
			f = 10 MHz	Room	- 52		
N _O , N _C Off Capacitance ^d	C _{NO(off)}	V _{IN} = 0 or V+, f = 1 MHz	Room		17		pF
	C _{NC(off)}		Room		17		
Channel-On Capacitance ^d	C _{NO(on)}		Room		40		
	C _{NC(on)}		Room		40		
Power Supply							
Power Supply Current	I+	V _{IN} = 0 or V+	Full		0.01	1.0	μA



SPECIFICATIONS $V_+ = 5\text{ V}$							
Parameter	Symbol	Test Conditions Otherwise Unless Specified $V_+ = 5\text{ V}$, $\pm 10\%$, $V_{\text{IN}} = 0.8$ or 2.0 V^e	Temp. ^a	Limits - 40 °C to 85 °C			Unit
				Min. ^b	Typ. ^c	Max. ^b	
Analog Switch							
Analog Signal Range ^d	$V_{\text{NO}}, V_{\text{NC}}, V_{\text{COM}}$		Full	0		V_+	V
On-Resistance	R_{ON}	$V_+ = 4.2\text{ V}$, $V_{\text{COM}} = 3.5\text{ V}$, $I_{\text{NO/NC}} = 10\text{ mA}$	Room Full		3	4.0 4.3	Ω
R_{ON} Flatness	R_{ON} Flatness	$V_+ = 4.2\text{ V}$, $V_{\text{COM}} = 1, 2, 3.5\text{ V}$ $I_{\text{NO/NC}} = 10\text{ mA}$	Room Full		1.1	1.4 1.6	
R_{ON} Match Between Channels	ΔR_{ON}	$V_+ = 4.2\text{ V}$, $V_{\text{COM}} = 3.5\text{ V}$, $I_{\text{NO/NC}} = 10\text{ mA}$	Room Full		0.1	0.3 0.4	
Switch Off Leakage Current	$I_{\text{NO(off)}}, I_{\text{NC(off)}}$	$V_+ = 5.5\text{ V}$ $V_{\text{NO}}, V_{\text{NC}} = 1\text{ V}/4.5\text{ V}$, $V_{\text{COM}} = 4.5\text{ V}/1\text{ V}$	Room Full	- 1 - 10		1 10	nA
	$I_{\text{COM(off)}}$		Room Full	- 1 - 10		1 10	
Channel-On Leakage Current	$I_{\text{COM(on)}}$	$V_+ = 5.5\text{ V}$, $V_{\text{NO}}, V_{\text{NC}} = V_{\text{COM}} = 1\text{ V}/4.5\text{ V}$	Room Full	- 1 - 10		1 10	
Digital Control							
Input High Voltage ^d	V_{INH}		Full	2.0			V
Input Low Voltage	V_{INL}		Full			0.8	
Input Capacitance	C_{in}		Full		12		pF
Input Current	I_{INL} or I_{INH}	$V_{\text{IN}} = 0\text{ V}$ or V_+	Full	1		1	μA
Dynamic Characteristics							
Turn-On Time	t_{ON}	$V_+ = 4.2\text{ V}$, V_{NO} or $V_{\text{NC}} = 3\text{ V}$ $R_{\text{L}} = 300\ \Omega$, $C_{\text{L}} = 35\text{ pF}$	Room Full		25	50 70	ns
Turn-Off Time	t_{OFF}		Room Full		20	40 50	
Break-Before-Make Time	t_{d}	V_{NO} or $V_{\text{NC}} = 3\text{ V}$, $R_{\text{L}} = 300\ \Omega$, $C_{\text{L}} = 35\text{ pF}$	Full	1			
Charge Injection ^d	Q_{INJ}	$C_{\text{L}} = 1\text{ nF}$, $V_{\text{GEN}} = 0\text{ V}$, $R_{\text{GEN}} = 0\ \Omega$	Room		49		pC
- 3 dB Bandwidth	BW	0 dBm, $C_{\text{L}} = 5\text{ pF}$, $R_{\text{L}} = 50\ \Omega$	Room		235		MHz
Off-Isolation ^d	OIRR	$R_{\text{L}} = 50\ \Omega$, $C_{\text{L}} = 5\text{ pF}$	f = 1 MHz	Room	- 71		dB
			f = 10 MHz	Room	- 51		
Crosstalk ^d	X_{TALK}	$R_{\text{L}} = 50\ \Omega$, $C_{\text{L}} = 5\text{ pF}$	f = 1 MHz	Room	- 74		
			f = 10 MHz	Room	- 52		
Source Off Capacitance ^d	$C_{\text{NO(off)}}$	$V_{\text{IN}} = 0$ or V_+ , f = 1 MHz	Room		17		pF
	$C_{\text{NC(off)}}$		Room		17		
Channel-On Capacitance ^d	$C_{\text{NO(on)}}$		Room		40		
	$C_{\text{NC(on)}}$		Room		40		
Power Supply							
Power Supply Range	V_+			1.8		5.5	V
Power Supply Current	I_+	$V_{\text{IN}} = 0$ or V_+	Full		0.01	1.0	μA

Notes:

a. Room = 25 °C, Full = as determined by the operating suffix.

b. Typical values are for design aid only, not guaranteed nor subject to production testing.

c. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.

d. Guarantee by design, nor subjected to production test.

e. V_{IN} = input voltage to perform proper function.

f. Guaranteed by 5 V leakage testing, not production tested.

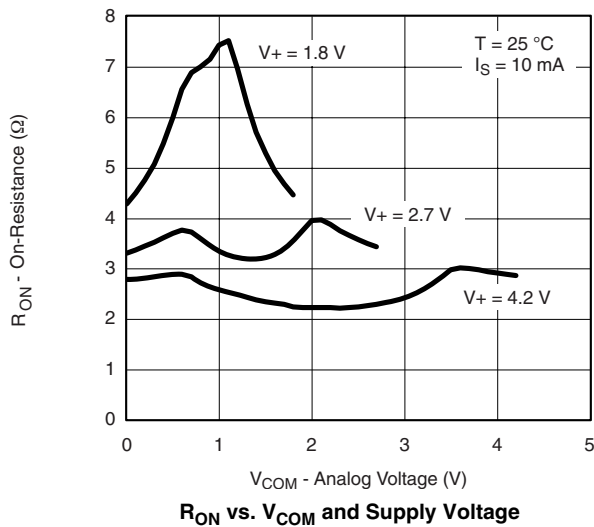
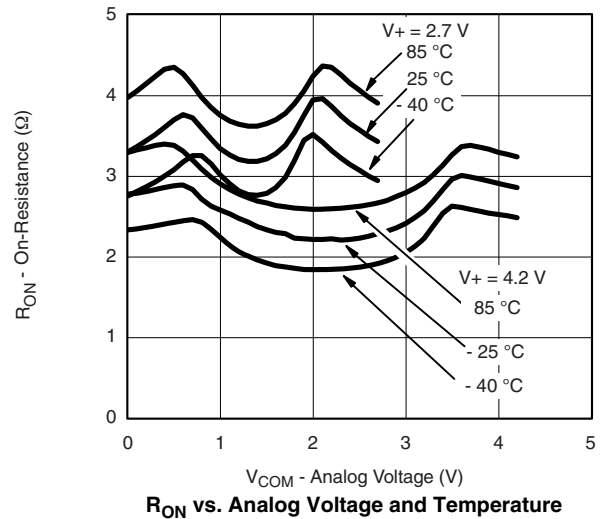
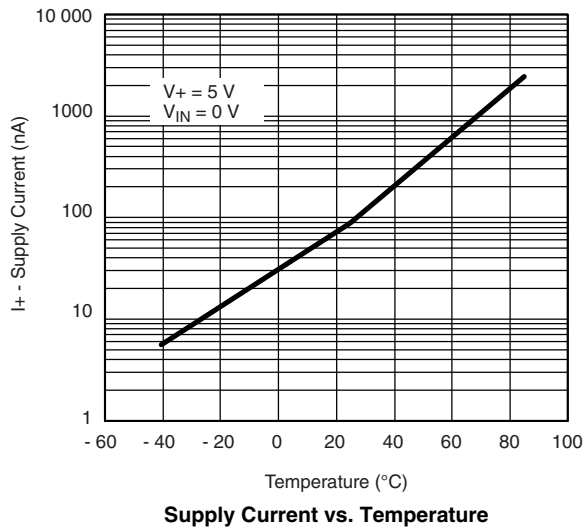
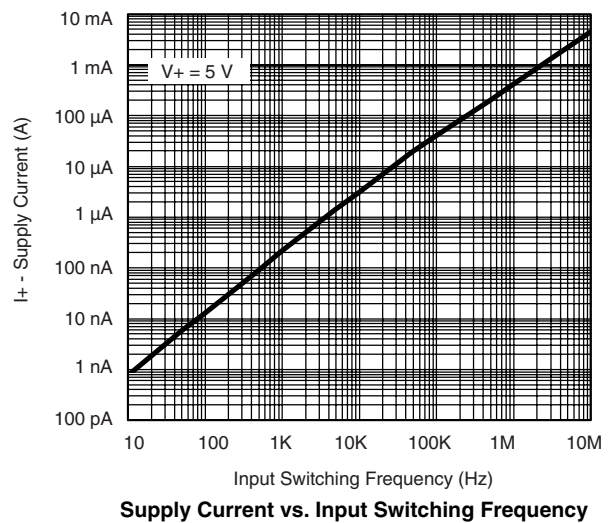
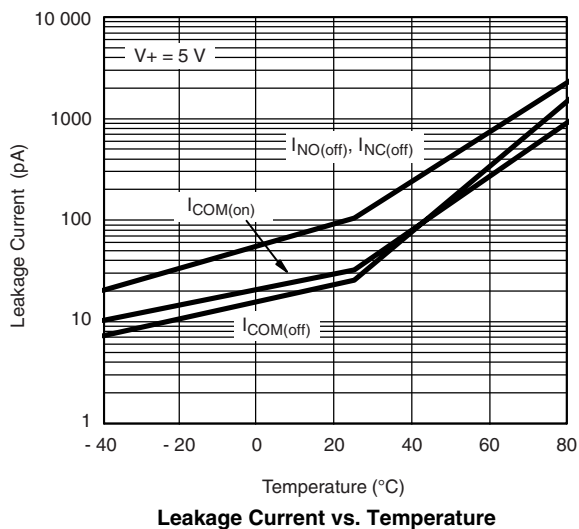
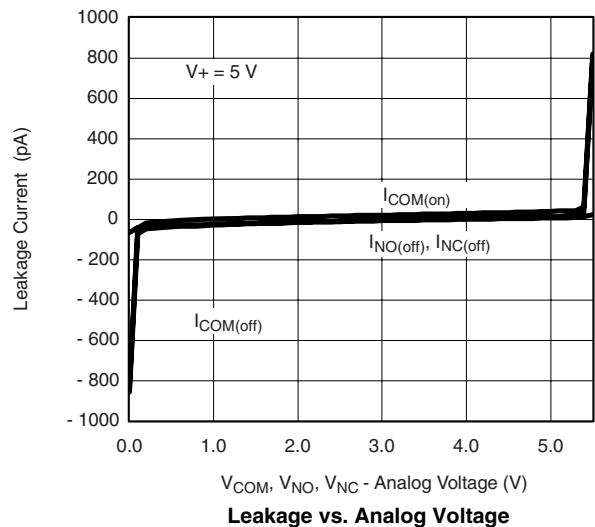
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DG2515, DG2516

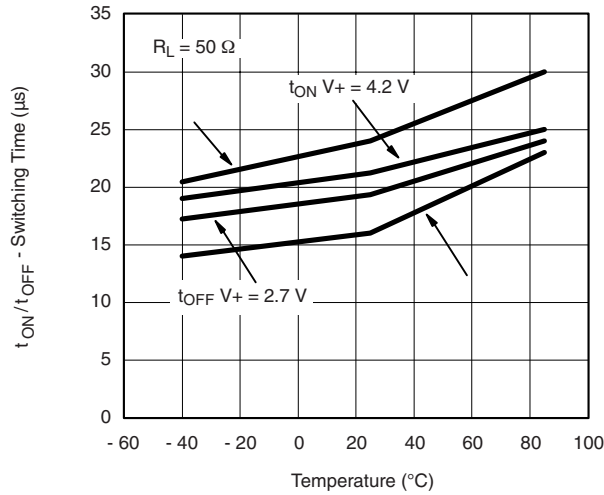
Vishay Siliconix



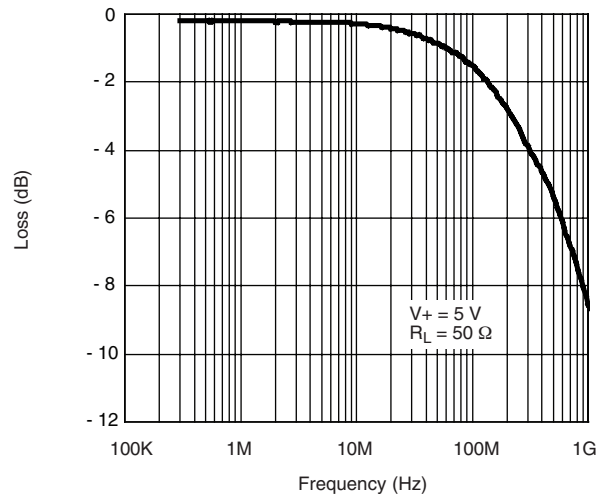
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



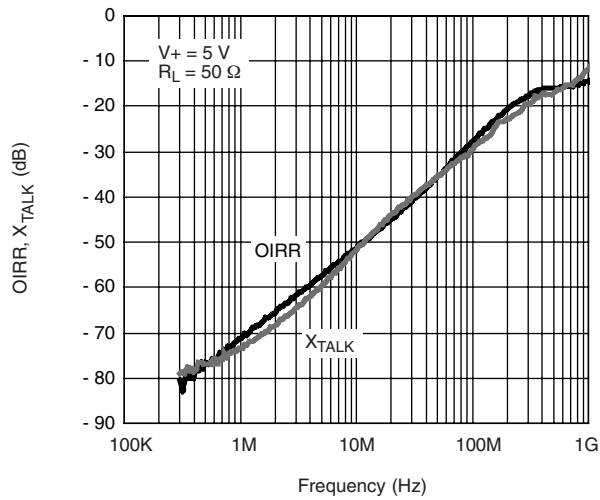
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



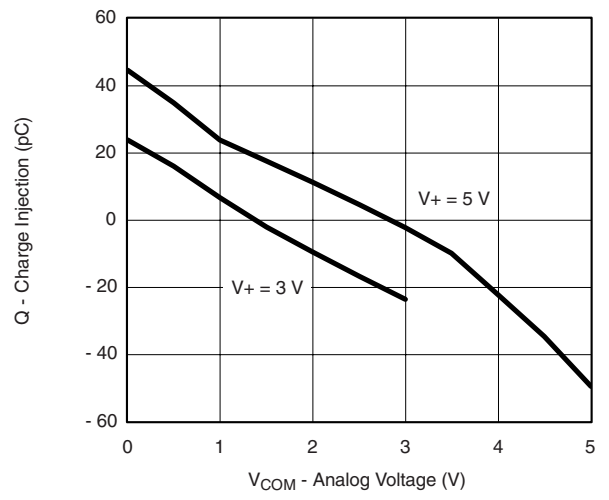
Switching Time vs. Temperature



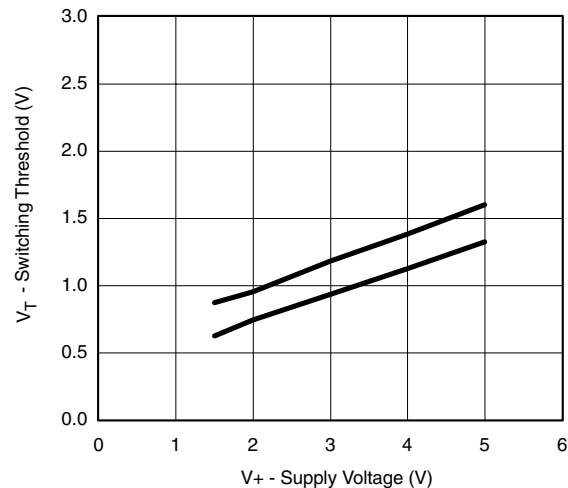
Insertion Loss vs. Frequency



Off-Isolation and Crosstalk vs. Frequency



Charge Injection vs. Analog Voltage



Switching Threshold vs. Supply Voltage

TEST CIRCUITS

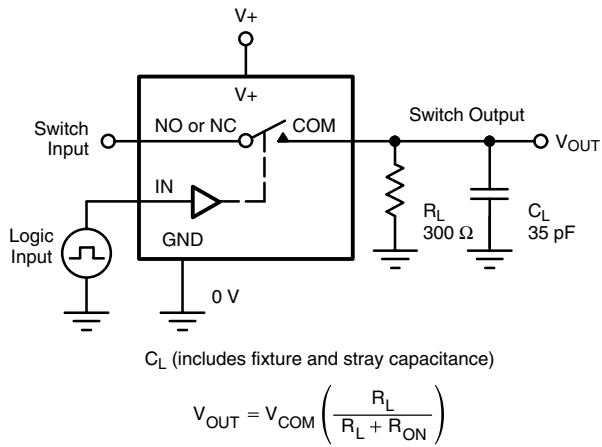
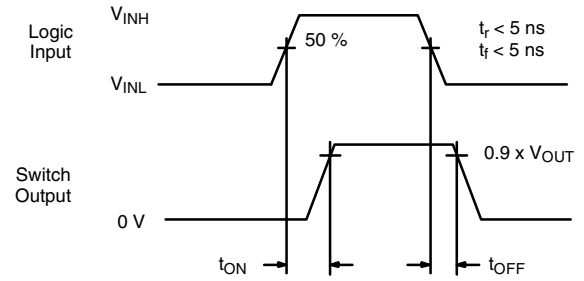


Figure 1. Switching Time

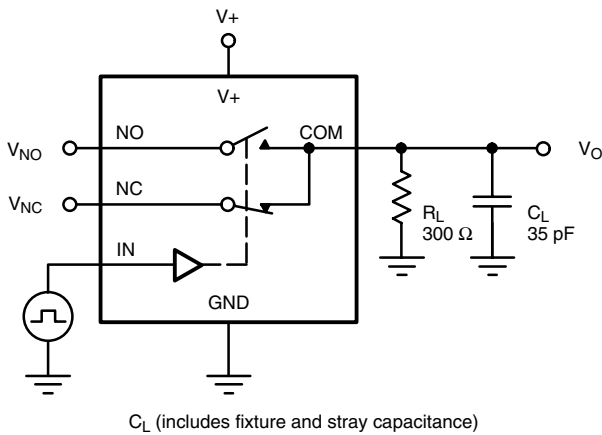
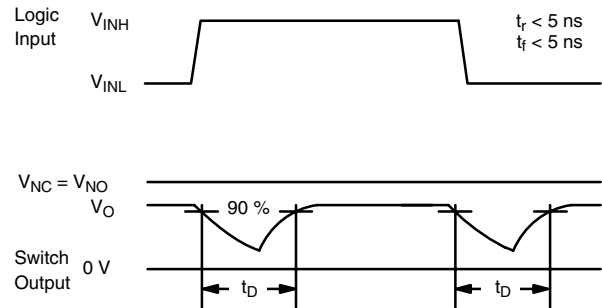
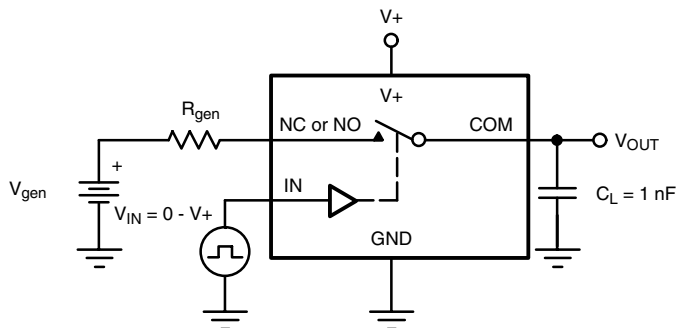
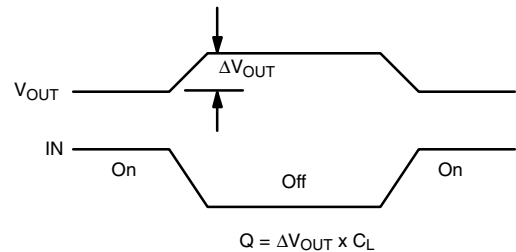


Figure 2. Break-Before-Make Interval



IN depends on switch configuration: input polarity determined by sense of switch.

Figure 3. Charge Injection

TEST CIRCUITS

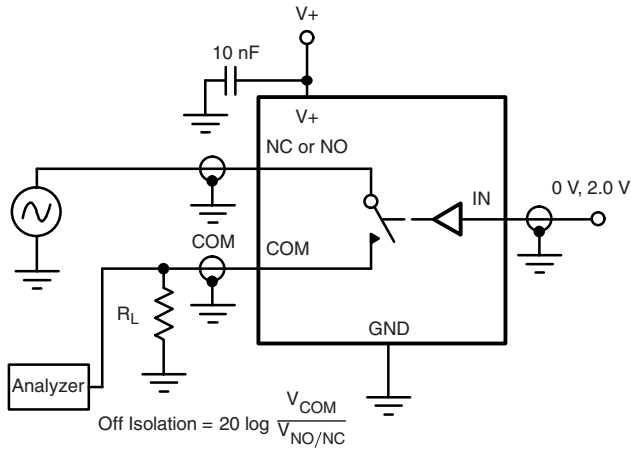


Figure 4. Off-Isolation

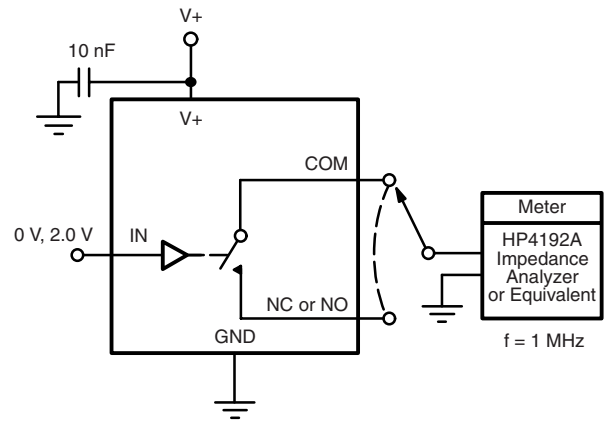


Figure 5. Channel Off/On Capacitance

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?73453>.



Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.