

MILITARY HIGH SPED 4K x 8 CMOS PROM/RPROM

KEY FEATURES

- **Ultra-Fast Access Time**
 - $t_{ACC} = 35$ ns
 - $t_{CS} = 20$ ns
- **Low Power Consumption**
- **Fast Programming**
- **DESC SMD No. 5962-906580**
- **Pin Compatible with 4K x 8 Bipolar PROMs**
- **Immune to Latch-UP**
 - Up to 200 mA
- **ESD Protection Exceeds 2000 V**
- **Available in 300 Mil DIP and CLLCC**

GENERAL DESCRIPTION

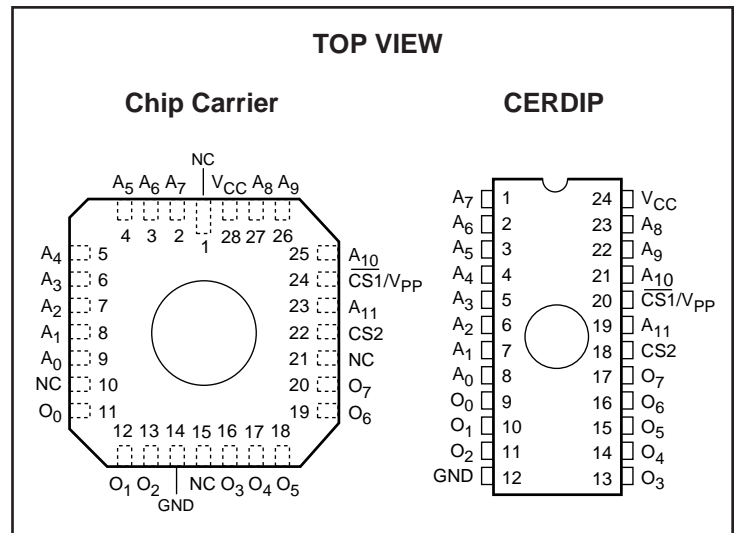
The WS57C43C is a High Performance 32K UV Erasable Electrically Re-Programmable Read Only Memory (RPROM). It is manufactured in an advanced CMOS technology which enables it to operate at Bipolar PROM speeds while consuming only 25% of the power required by its Bipolar counterparts. A further advantage of the WS57C43C over Bipolar PROM devices is the fact that it utilizes a proven EPROM technology. This enables the entire memory array to be tested for switching characteristics and functionality after assembly. Unlike devices which cannot be erased, every WS5743C in a windowed package is 100% tested with worst case test patterns both before and after assembly.

The WS57C43C is configured in the standard Bipolar PROM pinout which provides an easy upgrade path for systems which are currently using Bipolar PROMs, or its predecessor, the WS57C43B.

MODE SELECTION

| MODE \ PINS | $\overline{CS1}$ V_{PP} | CS2 | V_{CC} | OUTPUTS |
|----------------|------------------------------|----------|----------|-----------|
| Read | V_{IL} | V_{IH} | V_{CC} | D_{OUT} |
| Output Disable | V_{IH} | X | V_{CC} | High Z |
| Output Disable | X | V_{IL} | V_{CC} | High Z |
| Program | V_{PP} | X | V_{CC} | D_{IN} |
| Program Verify | V_{IL} | V_{IH} | V_{CC} | D_{OUT} |

PIN CONFIGURATION



PRODUCT SELECTION GUIDE

| PARAMETER | WS57C43C-35 | WS57C43C-45 | WS57C43C-55 | WS57C43C-70 |
|-------------------------------|-------------|-------------|-------------|-------------|
| Address Access Time (Max) | 35 ns | 45 ns | 55 ns | 70 ns |
| CS to Output Valid Time (Max) | 20 ns | 25 ns | 25 ns | 25 ns |

ORDERING INFORMATION

| PART NUMBER | SPEED (ns) | PACKAGE TYPE | PACKAGE DRAWING | OPERATING TEMPERATURE RANGE | WSI MANUFACTURING PROCEDURE |
|-----------------|------------|---------------------|-----------------|-----------------------------|-----------------------------|
| WS57C43C-35CMB* | 35 | 28 Pad CLLCC | C1 | Military | MIL-STD-883C |
| WS57C43C-35TMB* | 35 | 24 Pin Cerdip, 0.3" | T1 | Military | MIL-STD-883C |
| WS57C43C-45TMB* | 45 | 24 Pin Cerdip, 0.3" | T1 | Military | MIL-STD-883C |
| WS57C43C-55TMB* | 55 | 24 Pin Cerdip, 0.3" | T1 | Military | MIL-STD-883C |

NOTE: The actual part marking will not include the initials "WS."

*SMD product. See page 5-1 for DESC SMD numbers.

PROGRAMMING/ALGORITHMS/ERASURE/PROGRAMMERS

**REFER TO
PAGE 5-1**

The WS57C43C is programmed using Algorithm D shown on page 5-9.

For complete data sheet and electrical specifications see page 2-31.

