



Solid State Devices, Inc.

14701 Firestone Blvd * La Mirada, Ca 90638
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DESIGNER'S DATA SHEET

Part Number / Ordering Information ^{1/}

SFF130

└ Screening ^{2/}

— = Not Screen

TX = TX Level

TXV = TXV Level

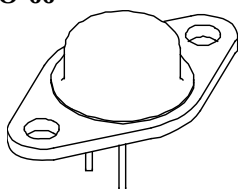
S = S Level

Package

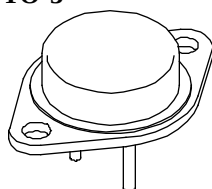
/3= TO-3

/66= TO-66

TO-66



TO-3



SFF130/3 & SFF130/66

14 AMP / 100 Volts

0.18 Ω

N-Channel Power MOSFET

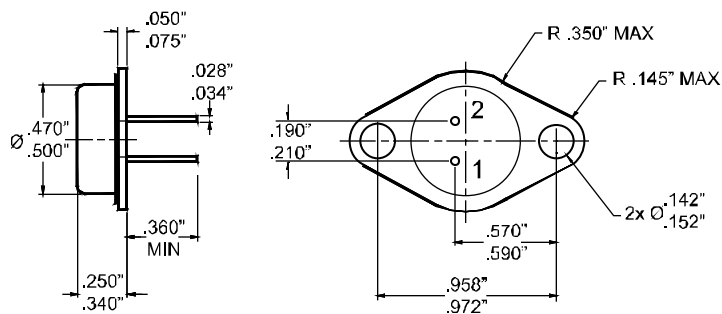
Features:

- Rugged Construction with Poly Silicon Gate
- Low $R_{DS(ON)}$ and High Transconductance
- Excellent High Temperature Stability
- Very Fast Switching Speed
- Fast Recovery and Superior dV/dt Performance
- Increased Reverse Energy Capability
- Low Input and Transfer Capacitance for Easy Paralleling
- Hermetically Sealed Package
- Available in both hot case and isolated versions
- Ideal for low power applications
- TX, TXV, Space Level Screening Available ^{2/}
- Replacement for IRFF130 & 2N6756 Types

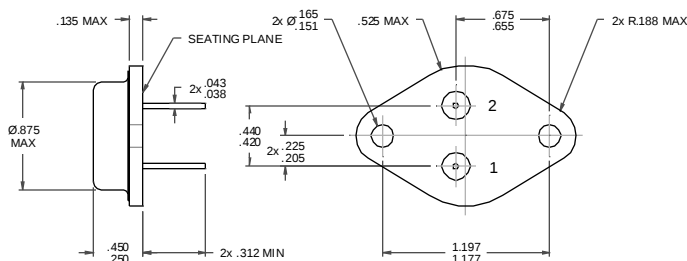
Maximum Ratings ^{3/}

		Symbol	Value	Units
Drain – Source Voltage		V_{DS}	100	Volts
Gate – Source Voltage		V_{GS}	± 20	Volts
Continuous Collector Current	$T_C = 25^\circ C$ $T_C = 100^\circ C$	I_D	14 9	Amps
Power Dissipation	$T_C = 25^\circ C$ $T_A = 25^\circ C$	P_D	25 19	Watts
Operating & Storage Temperature		Top & Tstg	-55 to +150	$^\circ C$
Thermal Resistance Junction to Case		$R_{\theta JC}$	5	$^\circ C/W$
Single Pulse Avalanche Energy		E_{AS}	75	mJ
Repetitive Avalanche Energy		E_{AR}	7.5	mJ

TO-66



TO-3



NOTES: 1/ For Ordering Information, Price, Operating Curves, and Availability- Contact Factory.

2/ Screened to MIL-PRF-19500/542.

3/ Unless Otherwise Specified, All Maximum Ratings and Electrical Characteristics @25°C.

NOTE: All specifications are subject to change without notification.
 SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: F00307B

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SFF130/3 & SFF130/66

Electrical Characteristics @ T _J = 25°C (Unless Otherwise Specified)		Symbol	Min	Typ	Max	Units
Drain to Source Breakdown Voltage (V _{GS} =0 V, I _D =250 μA)		BV _{DSS}	100	—	—	Volts
Temperature Coefficient of Breakdown Voltage		$\frac{\Delta BV_{DSS}}{\Delta T_j}$	—	130	—	mV/°C
Drain to Source On State Resistance (V _{GS} =10 V)	ID=9A ID=14A	R _{DS(on)}	—	0.13 0.14	0.18 0.21	Ω
Gate Threshold Voltage (V _{DS} =V _{GS} , I _D =250 μA)		V _{GS(th)}	2.0	2.8	4.0	V
Forward Transconductance (V _{DS} >I _{D(on)} X R _{DS(on)} Max, I _{DS} = 9A)		g _{fs}	4.6	7	—	mho
Zero Gate Voltage Drain Current (V _{DS} =80% max rated voltage, V _{GS} =0 V) (V _{DS} =80% rated V _{DS} , V _{GS} =0 V, T _A =125°C)		I _{DSS}	— —	— —	25 250	μA
Gate to Source Leakage Forward Gate to Source Leakage Reverse	At rated V _{GS}	I _{GSS}	— —	— —	+100 -100	nA
Total Gate Charge Gate to Source Charge Gate to Drain Charge	V _{GS} =10 Volts 50% rated V _{DS} Rated I _D	Q _g Q _{gs} Q _{gd}	12 1.5 5	17 3.7 7.0	35 10 20	nC
Turn on Delay Time Rise Time Turn on Delay Time Fall Time	V _{DD} =50% Rated V _{DS} 50% rated I _D R _G = 7.5Ω	t _{d(on)} t _r t _{d(off)} t _f	— — — —	9.5 42 22 25	35 80 60 45	nsec
Diode Forward Voltage (I _S = Rated I _D , V _{GS} =0 V, T _J =25°C)		V _{SD}	—	1	1.5	V
Diode Reverse Recovery Time Reverse Recovery Charge	T _J =25°C I _F =10A Di/dt=100A/μsec	t _{rr} Q _{RR}	— —	120 0.58	300 3	nsec μC
Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{GS} =0 Volts V _{DS} =25 Volts f=1 MHz	C _{iss} C _{oss} C _{rss}	— — —	650 250 44	— — —	pF

For thermal derating curves and other characteristics please contact SSDI Marketing Department.

Available Part Number:

SFF130/3; SFF130/66

PIN ASSIGNMENT (Standard)

Package	Drain	Source	Gate
TO-3	Case	Pin 2	Pin 1
TO-66	Case	Pin 2	Pin 1

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