

Hyperfast Rectifier, 15 A FRED Pt® G5



LINKS TO ADDITIONAL RESOURCES





PRIMARY CHARACTERISTICS				
I _{F(AV)}	15 A			
V _R	600 V			
V _F at I _F at 125 °C	1.5 V			
t _{rr} (typ.)	17 ns			
T _J max.	175 °C			
Package	TO-220 FullPAK 2L			
Circuit configuration	Single			

FEATURES

Best in class forward voltage drop and switching losses trade off



• Optimized for high speed operation

• 175 °C maximum operating junction temperature

RoHS COMPLIANT HALOGEN

FREE

Polyimide passivation

Fully isolated package (V_{INS} =2500 V_{RMS})

• True 2 pin package

• Designed and qualified according to JEDEC® - JESD 47

 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters. This device is specifically designed to improve as output rectifier for DC/DC stage in resonant converters and as PFC rectifier for aircon and industrial power supplies.

MECHANICAL DATA

Case: TO-220 FullPAK 2L

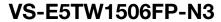
Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Repetitive peak reverse voltage	V _{RRM}		600	V	
Average rectified forward current in DC	I _{F(AV)}	T _C = 98 °C, DC	15	^	
Non-repetitive peak surge current	I _{FSM}	$T_C = 25$ °C, $t_p = 10$ ms, sine wave	175	А	
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	MBOL TEST CONDITIONS MIN. TYP.		TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-	.,	
Forward voltage	V _F	I _F = 15 A	-	2	2.8	V	
		I _F = 15 A, T _J = 125 °C	-	1.5	-		
Developed to the second		$V_R = V_R$ rated	-	-	10		
Reverse leakage current	IR	T _J = 125 °C, V _R = V _R rated	-	-	500	μA	
Junction capacitance	C _T V _R = 600 V		-	19	-	pF	
Series inductance	L _S	Measured to lead 5 mm from package body	-	8	-	nH	





DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A,dI}_F/c$	$I_F = 1.0 \text{ A,dI}_F/\text{dt} = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		17	-	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	19	-	ns
		T _J = 125 °C		-	35	-	
Dook roomen ourrent		T _J = 25 °C	I _F = 10 A dI _F /dt = 1000 A/μs V _B = 400 V	-	10	-	Α
Peak recovery current I _R	I _{RRM}	T _J = 125 °C		-	17	-	
Reverse recovery charge	0	T _J = 25 °C	''	-	97	-	nC
	Q_{rr}	T _J = 125 °C		-	345	-	
Boyeres resource time		T _J = 25 °C		-	21	-	20
Reverse recovery time	t _{rr}	T _J = 125 °C		-	39	-	ns
Peak recovery current I _{RRM}		T _J = 25 °C	I _F = 15 A dI _F /dt = 1000 A/μs V _R = 400 V	-	11	-	А
	IRRM	T _J = 125 °C		-	17	-	
Reverse recovery charge Q _{rr}		T _J = 25 °C		-	110	-	nC
	Q _{rr}	T _J = 125 °C		-	435	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	R_{thJC}		-	-	3.3	°C/W
Weight			-	2.0	-	g
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Marking device		Case style TO-220 FullPAK 2L	E5TW1506FP			



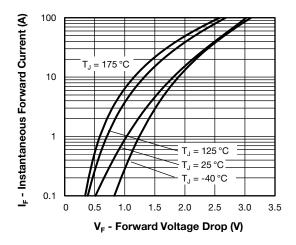


Fig. 1 - Forward Voltage Drop Characteristics

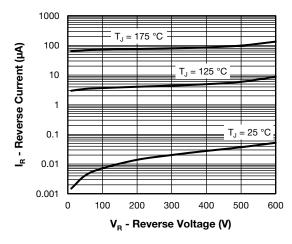


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

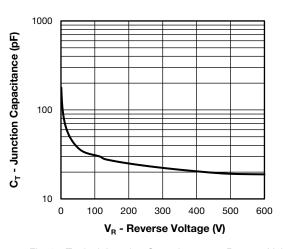


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

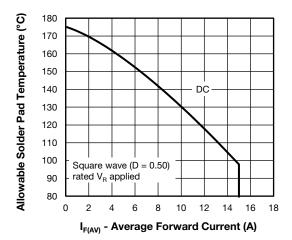


Fig. 4 - Maximum Allowable Case Temperature vs.
Average Forward Current

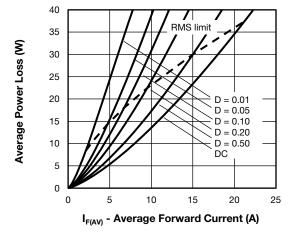


Fig. 5 - Forward Power Loss Characteristics



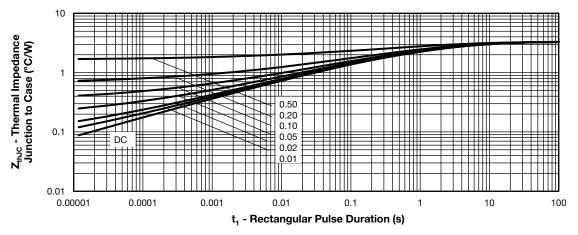


Fig. 6 - Transient Thermal Impedance, Junction to Case

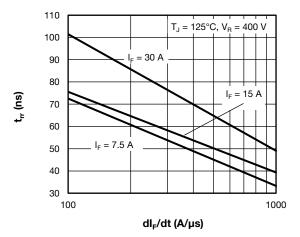


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

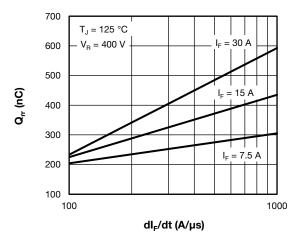


Fig. 8 - Typical Reverse Recovery Charge vs. dI_F/dt

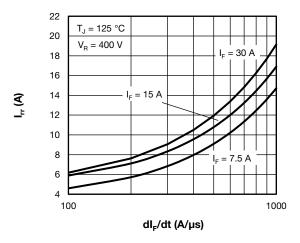


Fig. 9 - Typical Reverse Recovery Current vs. dl_F/dt

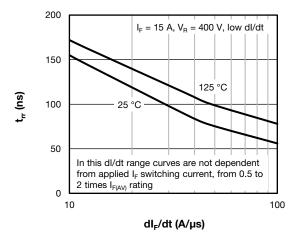


Fig. 10 - Typical Reverse Recovery Time vs. dI_F/dt

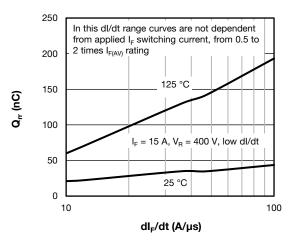


Fig. 11 - Typical Reverse Recovery Charge vs. dl_F/dt

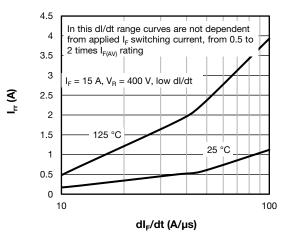


Fig. 12 - Typical Reverse Recovery Current vs. dl_F/dt

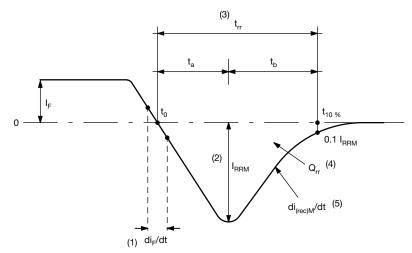


Fig. 13 - Reverse Recovery Waveform and Definitions

Notes

- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from t_0 , crossing point of negative going I_F , to point $t_{10\%}$, 0.1 I_{RRM}
- $^{(4)}$ Q_{rr} area under curve defined by t_0 and $t_{10}\,\%$

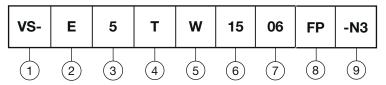
$$Q_{rr} = \int_{t_0}^{t_{10}\%} I(t)dt$$

(5) di_(rec)M/dt - peak rate of change of current during t_b portion of t_{rr}



ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- E = single diode
- 2 5 = FRED generation 5
- Package:
 - T = TO-220 package
- W = warp hyperfast recovery
- Current rating (15 = 15 A)
- Voltage rating (06 = 600 V)
- FP = TO-220 FullPAK 2L
- Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

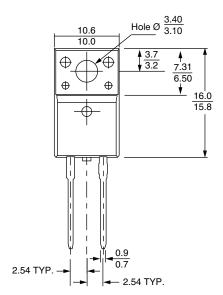
ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-E5TW1506FP-N3	50	1000	Antistatic plastic tube			

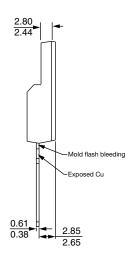
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?96157		
Part marking information	www.vishay.com/doc?95392		

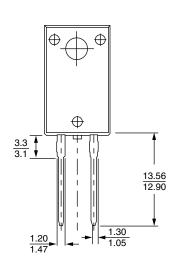


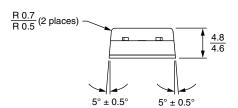
2L TO-220 FullPAK

DIMENSIONS in millimeters









Bottom view



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Vishay

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