



Solid State Devices, Inc.

14701 Firestone Blvd * La Mirada, Ca 90638

Phone: (562) 404-4474 * Fax: (562) 404-1773

ssdi@ssdi-power.com * www.ssdi-power.com

SSR4010CT/3

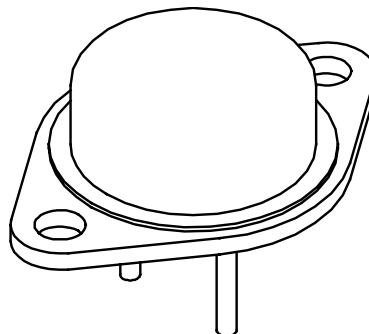
Designer's Data Sheet

FEATURES:

- PIV: 100 Volts
- Very Low Forward Voltage Drop
- Low Reverse Leakage
- Hermetically Sealed Package
- Guard Ring for Overvoltage Protection
- Available in Isolated and Non-isolated versions
- Gold Eutectic Die Attach
- 175°C Operating Junction Temperature
- Also Available in the following Configurations:
 - Common Anode- SSR4010CA/3
 - Doubler- SSR4010D/3
- TX, TXV, and Space Level Screening Available

**40 AMPS
100 VOLTS
POSITIVE CENTERTAP
SCHOTTKY
RECTIFIER**

TO-3



MAXIMUM RATINGS		Symbol	Value	Units
Peak Repetitive Reverse Voltage and DC Blocking Voltage	SSR4010CT/3	V_{RRM} V_{RWM} V_R	100	Volts
Average Rectified Forward Current ^{1/} (Resistive Load, 60 Hz, Sine Wave, $T_A=25^{\circ}\text{C}$)		I_O	40	Amps
Peak Surge Current ^{1/} (8.3 ms Pulse, Half Sine Wave Superimposed on I_O , allow junction to reach equilibrium between pulses, $T_A=25^{\circ}\text{C}$)		I_{FSM}	400	Amps
Operating and Storage Temperature		T_{OP} & T_{stg}	-65 to +175	$^{\circ}\text{C}$
Maximum Thermal Resistance ^{1/} Junction to Case		$R_{\theta JC}$	0.6	$^{\circ}\text{C/W}$

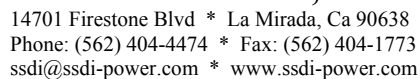
Notes:

^{1/} Both Legs Tied Together. (Doubler Per Leg: $I_O = 20\text{A}$, $I_{FSM} = 300\text{A}$, $R_{\theta JC} = 1.2^{\circ}\text{C/W}$)

NOTE: All specifications are subject to change without notification.
SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: RS0097D

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