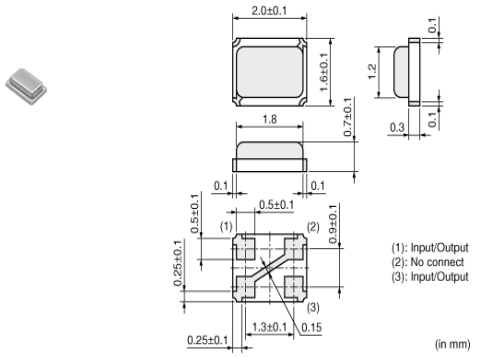
CSACM10M0G530000R0

In Production RoHS REACH

< List of part numbers with package codes >
CSACM10M0G530000R0

Appearance & Shape



Features

MURATA's package technology expertise has enabled the development of the Chip CERALOCK(R).

Features
1. Extremely small and have a low profile.
2. No adjustment is necessary for oscillation circuits.

Applications

Other Usage	Consumer/Industrial

Packaging Information

Packaging	Specifications	Minimum Order Quantity
-R0	180mm Embossed Tape	3000

Attention
1.This datasheet is downloaded from the website of Murata Manufacturing Co., Ltd. Therefore, it's specifications are subject to change or our products in it may be discontinued without advance notice. Please check with our sales representatives or product engineers before ordering.
2.This datasheet has only typical specifications because there is no space for detailed specifications.
Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering.

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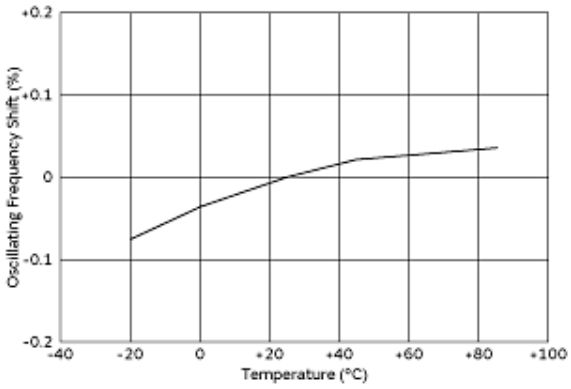
Specifications

Shape	SMD
Frequency	10.000MHz
Frequency Tolerance	±0.5%
Operating Temperature Range	-20℃ to 85℃
Frequency Shift by Temperature	-0.25/ +0.2%
Frequency Aging	-0.05/ +0.1%
Wash	Not avairable
Resonant Impedance	180Ω

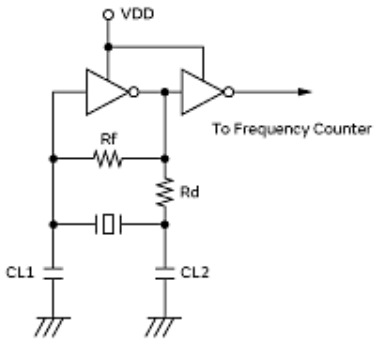
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Product Data



Temperature Characteristics



Measurement Circuit

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