

**Vishay Semiconductors** 

RoHS

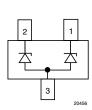
COMPLIANT

GREEN

(5-2008)<sup>4</sup> Available

### **Two-Line ESD-Protection in SOT-23**

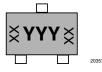




#### FEATURES

- Two-line ESD-protection device
- ESD-protection acc. IEC 61000-4-2 ± 30 kV contact discharge
  - ± 30 kV air discharge
- Space saving SOT-23 package
- AEC-Q101 qualified
- e3 Sn
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

**MARKING** (example only)



YYY = type code (see table below) XX = date code

ORDERING INFORMATION							
DEVICE NAME	ENVIRONMENTAL STATUS	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY			
GSOT03C	Standard	GSOT03C-GS08	3000	15 000			
6301030	Green	GSOT03C-V-G-08	3000	15 000			
GSOT04C	Standard	GSOT04C-GS08	3000	15 000			
6301040	Green	GSOT04C-V-G-08	3000	15 000			
GSOT05C	Standard	GSOT05C-GS08	3000	15 000			
6501050	Green	GSOT05C-V-G-08	3000	15 000			
GSOT08C	Standard	GSOT08C-GS08	3000	15 000			
6501080	Green	GSOT08C-V-G-08	3000	15 000			
GSOT12C	Standard	GSOT12C-GS08	3000	15 000			
6501120	Green	GSOT12C-V-G-08	3000	15 000			
GSOT15C	Standard	GSOT15C-GS08	3000	15 000			
6501150	Green	GSOT15C-V-G-08	3000	15 000			
CSOT24C	Standard	GSOT24C-GS08	3000	15 000			
GSOT24C	Green	GSOT24C-V-G-08	3000	10 000			
0007260	Standard	GSOT36C-GS08	3000	15 000			
GSOT36C	Green	GSOT36C-V-G-08		15 000			

\*\* Please see document "Vishay Material Category Policy": <u>www.vishay.com/doc?99902</u>



1

# Vishay Semiconductors Two-Line ESD-Protection in SOT-23

PACKA	GE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	ENVIRONMENTAL STATUS	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
GSOT03C	SOT-23	03C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
0001000	001 20	C1G	Green	8.1 mg	02 34 V 0	(according J-STD-020)			
GSOT04C	SOT-23	04C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
0501040	301-23	C8G	Green	8.1 mg	01 94 0-0	(according J-STD-020)			
GSOT05C	SOT-23	05C	Standard	8.8 mg		UL 94 V-0		MSL level 1	260 °C/10 s at terminals
0301030	301-23	C2C	Green	8.1 mg	01 94 0-0	(according J-STD-020)			
GSOT08C	SOT-23	08C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
0301000	301-23	C3G	Green	8.1 mg	0L 94 V-0	(according J-STD-020)			
GSOT12C	SOT-23	12C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
0301120	301-23	C4G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)			
GSOT15C	SOT-23	15C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
0301130	301-23	C5G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)			
GSOT24C	SOT-23	24C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
0301240	301-23	C6G	Green	8.1 mg	01 34 0-0	(according J-STD-020)			
GSOT36C	SOT-23	36C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals		
0301300	301-23	C7G	Green	8.1 mg	02 34 0-0	(according J-STD-020)			

ABSOLUTE MAXIMUM RATINGS GSOT03C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Dock pulse ourrent	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	1	30	А	
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	IPPM	30	А	
Peek pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	P	369	W	
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	P <sub>PP</sub>	504	W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

ABSOLUTE MAXIMUM RATINGS GSOT04C						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	<b>I</b>	30	А		
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu$ s; single shot	IPPM	30	А		
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	<b>_</b>	429	W		
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu$ s; single shot	P <sub>PP</sub>	564	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V ESD	± 30	kV		
Operating temperature	Junction temperature	ТJ	- 40 to + 125	°C		
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C		





# Two-Line ESD-Protection in SOT-23 Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS GSOT05C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	<b>I</b>	30	А	
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	IPPM	30	А	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	P <sub>PP</sub>	480	W	
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	г рр	612	W	
	Contact discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	▼ ESD	± 30	kV	
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

ABSOLUTE MAXIMUM RATINGS GSOT08C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	<b>1</b>	18	А	
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	IPPM	18	А	
Posk pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	P <sub>PP</sub>	345	W	
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	Г.ЬЬ	400	W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	VESD	± 30	kV	
Operating temperature	Junction temperature	ТJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

ABSOLUTE MAXIMUM RATINGS GSOT12C						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	<b>1</b>	12	А		
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	I <sub>PPM</sub>	12	А		
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	P <sub>PP</sub>	312	W		
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	Грр	337	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	± 30	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C		
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C		

### Vishay Semiconductors Two-Line ESD-Protection in SOT-23



ABSOLUTE MAXIMUM RATINGS GSOT15C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	I	8	А	
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu$ s; single shot	IPPM	8	А	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	Р	345	W	
	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	P <sub>PP</sub>	400	W	
	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

ABSOLUTE MAXIMU	I RATINGS GSOT24C			
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Poak pulso current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	<b>I</b>	5	А
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu$ s; single shot	IPPM	5	А
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu$ s; single shot	P <sub>PP</sub>	235	W
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu$ s; single shot	ГРР	240	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV
ESD inmunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C
Storage temperature		T <sub>STG</sub>	- 55 to + 150	٥°

ABSOLUTE MAXIMU	M RATINGS GSOT36C			
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu$ s; single shot		3.5	А
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu$ s; single shot	IPPM	3.5	А
Poak pulso power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \ \mu s$ ; single shot	P <sub>PP</sub>	248	W
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \ \mu$ s; single shot	ГРР	252	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV
ESD minuting	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C

BIAs-MODE (2-line bidirectional asymmetrical protection mode)

With the GSOTxx one signal- or data-lines (L1) can be protected against voltage transients. With pin 1 connected to ground and pin 3 connected to a signal- or data-line which has to be protected. As long as the voltage level on the data- or signal-line is between 0 V (ground level) and the specified maximum reverse working voltage ( $V_{RWM}$ ) the protection diode between pin 1 and pin 3 offer a high isolation to the ground line. The protection device behaves like an open switch.

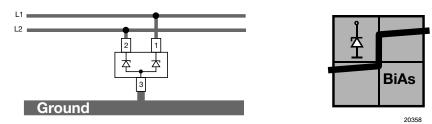
As soon as any positive transient voltage signal exceeds the break through voltage level of the protection diode, the diode becomes conductive and shorts the transient current to ground. Now the protection device behaves like a closed switch. The clamping voltage ( $V_C$ ) is defined by the breakthrough voltage ( $V_{BR}$ ) level plus the voltage drop at the series impedance (resistance and inductance) of the protection device.

Any negative transient signal will be clamped accordingly. The negative transient current is flowing in the forward direction of the protection diode. The low forward voltage (V<sub>F</sub>) clamps the negative transient close to the ground level.

Due to the different clamping levels in forward and reverse direction the GSOTxx clamping behaviour is bidirectional and asymmetrical (BiAs).



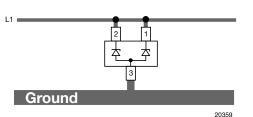
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If a higher surge current or peak pulse current ( $I_{PP}$ ) is needed, both protection diodes in the GSOTxxC can also be used in parallel in order to "double" the performance.

This offers:

- double surge power = double peak pulse current (2 x I<sub>PPM</sub>)
- half of the line inductance = reduced clamping voltage
- half of the line resistance = reduced clamping voltage
- double line capacitance (2 x C<sub>D</sub>)
- double reverse leakage current (2 x I<sub>R</sub>)



ELECTRICAL CHARACTERISTICS GSOT03C							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse working voltage	at I <sub>R</sub> = 100 μA	V <sub>RWM</sub>	3.3	-	-	V	
Reverse current	at V <sub>R</sub> = 3.3 V	I <sub>R</sub>	-	-	100	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	4	4.6	-	V	
Boveras elemping veltage	at I <sub>PP</sub> = 1 A	N	-	5.7	7.5	V	
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	10	12.3	V	
Forward elemping valtage	at I <sub>PP</sub> = 1 A	VF	-	1	1.2	V	
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	VF	-	4.5	-	V	
<u>Openneitenen</u>	at $V_R = 0$ V; f = 1 MHz	6	-	420	600	pF	
Capacitance	at V <sub>R</sub> = 1.6 V; f = 1 MHz	C <sub>D</sub>	-	260	-	pF	

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARACTERISTICS GSOT04C							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse working voltage	at I <sub>R</sub> = 20 μA	V <sub>RWM</sub>	4	-	-	V	
Reverse current	at V <sub>R</sub> = 4 V	I <sub>R</sub>	-	-	20	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	5	6.1	-	V	
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	7.5	9	V	
neverse clamping voltage	at $I_{PP} = I_{PPM} = 30 \text{ A}$	vс	-	11.2	14.3	V	
Forward clamping voltage	at I <sub>PP</sub> = 1 A	VF	-	1	1.2	V	
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	٧F	-	4.5	-	V	
<u>Connections</u>	at $V_R = 0 V$ ; f = 1 MHz	<u> </u>	-	310	450	pF	
Capacitance	at $V_R = 2 V$ ; f = 1 MHz	C <sub>D</sub>	-	200	-	pF	

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

# Vishay Semiconductors Two-Line ESD-Protection in SOT-23



ELECTRICAL CHARAC	TERISTICS GSOT05C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 10 μA	V <sub>RWM</sub>	5	-	-	V
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6	6.8	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V	-	7	8.7	V
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	12	16	V
Forward elemping voltage	at I <sub>PP</sub> = 1 A	V <sub>E</sub>	-	1	1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	٧F	-	4.5	-	V
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	6	-	260	350	pF
	at V <sub>R</sub> = 2.5 V; f = 1 MHz	C <sub>D</sub>	-	150	-	pF

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARACTERISTICS GSOT08C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse working voltage	at I <sub>R</sub> = 5 μA	V <sub>RWM</sub>	8	-	-	V		
Reverse current	at V <sub>R</sub> = 8 V	I <sub>R</sub>	-	-	5	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	9	10	-	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V	-	10.7	13	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	V <sub>C</sub>	-	15.2	19.2	V		
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	٧F	-	3	-	V		
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	- C <sub>D</sub>	-	160	250	pF		
	at $V_R = 4 V$ ; f = 1 MHz		-	80	-	pF		

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	12	-	-	V
Reverse current	at V <sub>R</sub> = 12 V	I <sub>R</sub>	-	-	1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	13.5	15	-	V
Poveres elemping veltage	at I <sub>PP</sub> = 1 A	V	-	15.4	18.7	V
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	V <sub>C</sub>	-	21.2	26	V
Forward elemping voltage	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	V <sub>F</sub>	-	2.2	-	V
Capacitanaa	at $V_R = 0$ V; f = 1 MHz	CD	-	115	150	pF
Capacitance	at $V_R = 6 V$ ; f = 1 MHz		-	50	-	pF

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)



## Two-Line ESD-Protection in SOT-23 Vishay Semiconductors

ELECTRICAL CHARAC	TERISTICS GSOT15C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	15	-	-	V
Reverse current	at V <sub>R</sub> = 15 V	I <sub>R</sub>	-	-	1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	16.5	18	-	V
Poweres elemping veltage	at I <sub>PP</sub> = 1 A	V	-	19.4	23.5	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 8 A$	V <sub>C</sub>	-	24.8	28.8	V
Forward elemping voltage	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 8 A	V <sub>F</sub>	-	1.8	-	V
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	-	90	120	pF
	at V <sub>R</sub> = 7.5 V; f = 1 MHz		-	35	-	pF

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARACTERISTICS GSOT24C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse working voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	24	-	-	V		
Reverse current	at V <sub>R</sub> = 24 V	I <sub>R</sub>	-	-	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	27	30	-	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	N/	-	34	41	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>C</sub>	-	41	47	V		
Forward elemping voltage	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>F</sub>	-	1.4	-	V		
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	-	65	80	pF		
	at V <sub>R</sub> = 12 V; f = 1 MHz		-	20	-	pF		

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

ELECTRICAL CHARAC	TERISTICS GSOT36C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines
Reverse working voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	36	-	-	V
Reverse current	at V <sub>R</sub> = 36 V	I <sub>R</sub>	-	-	1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	39	43	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V	-	49	60	V
neverse clamping voltage	at $I_{PP} = I_{PPM} = 3.5 \text{ A}$	V <sub>C</sub>	-	59	71	V
Forward clamping voltage	at I <sub>PP</sub> = 1 A	- V <sub>F</sub>	-	1	1.2	V
Forward clamping voltage	at $I_{PP} = I_{PPM} = 3.5 \text{ A}$	٧F	-	1.3	-	V
Capacitance -	at $V_R = 0 V$ ; f = 1 MHz	C <sub>D</sub>	-	52	65	pF
	at V <sub>R</sub> = 18 V; f = 1 MHz		-	12	-	pF

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 3 or pin 2 to 3)

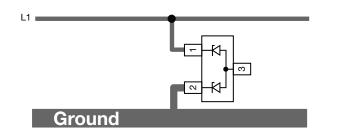
#### **BiSy-MODE** (1-line bidirectional symmetrical protection mode)

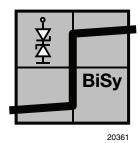
If a bipolar symmetrical protection device is needed the GSOTxxC can also be used as a single line protection device. Therefore pin 1 has to be connected to the signal- or data-line (L1) and pin 2 to ground (or vice versa). Pin 3 must not be connected. Positive and negative voltage transients will be clamped in the same way. The clamping current through the GSOTxxC passes one diode in forward direction and the other one in reverse direction. The clamping voltage ( $V_c$ ) is defined by the breakthrough voltage ( $V_{BR}$ ) level of one diode plus the forward voltage of the other diode plus the voltage drop at the series impedances (resistances and inductances) of the protection device.

Due to the same clamping levels in positive and negative direction the GSOTxxC voltage clamping behaviour is bidirectional and symmetrical (BiSy).

### Vishay Semiconductors Two-Line ESD-Protection in SOT-23







ELECTRICAL CHARAC	TERISTICS GSOT03C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse working voltage	at I <sub>R</sub> = 100 μA	V <sub>RWM</sub>	3.8	-	-	V
Reverse current	at V <sub>R</sub> = 3.8 V	I <sub>R</sub>	-	-	100	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	4.5	5.3	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V	-	7	8.4	V
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	14	16.8	V
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	C <sub>D</sub>	-	210	300	pF
	at V <sub>R</sub> = 1.6 V; f = 1 MHz		-	190	-	pF

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARACTERISTICS GSOT04C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 20 μA	V <sub>RWM</sub>	4.5	-	-	V		
Reverse current	at V <sub>R</sub> = 4:5 V	I <sub>R</sub>	-	-	20	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	5.5	6.8	-	V		
Bayaraa alamping valtaga	at I <sub>PP</sub> = 1 A	V	-	7.5	9	V		
Reverse clamping voltage	everse clamping voltage $V_C$ at $I_{PP} = I_{PPM} = 30 \text{ A}$	vc	-	15.7	18.8	V		
	at $V_R = 0 V$ ; f = 1 MHz	<u> </u>	-	155	225	pF		
Capacitance	acitance $V_R = 2 V; f = 1 MHz$ $C_D$	UD	-	135	-	pF		

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARACTERISTICS GSOT05C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 10 μA	V <sub>RWM</sub>	5.5	-	-	V		
Reverse current	at V <sub>R</sub> = 5.5 V	I <sub>R</sub>	-	-	10	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6.5	7.5	-	V		
Deverse elements veltage	at I <sub>PP</sub> = 1 A	V	-	8.1	9.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	V <sub>C</sub>	-	17	20.4	V		
Capacitance	at $V_R = 0$ V; f = 1 MHz	CD	-	130	175	pF		
	at $V_R = 4 V$ ; f = 1 MHz		-	100	-	pF		

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

# Two-Line ESD-Protection in SOT-23 Vishay Semiconductors

ELECTRICAL CHARACTERISTICS GSOT08C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 5 μA	V <sub>RWM</sub>	8.5	-	-	V		
Reverse current	at V <sub>R</sub> = 8.5 V	I <sub>R</sub>	-	-	5	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	9.5	10.7	-	V		
Poweres elemping veltage	at I <sub>PP</sub> = 1 A	Vc	-	11.7	14	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	vc	-	18.5	22.2	V		
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	-	80	125	pF		
	at $V_R = 4 V$ ; f = 1 MHz		-	60	-	pF		

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARACTERISTICS GSOT12C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	12.5	-	-	V		
Reverse current	at V <sub>R</sub> = 12.5 V	I <sub>R</sub>	-	-	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	13.5	15.7	-	V		
Boverse elemping veltage	at I <sub>PP</sub> = 1 A	V	-	16.4	19.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	V <sub>C</sub>	-	23.4	28.1	V		
Capacitance	at $V_R = 0$ V; f = 1 MHz	CD	-	58	75	pF		
	at V <sub>R</sub> = 7.5 V; f = 1 MHz		-	36	-	pF		

#### Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARAC	TERISTICS GSOT15C					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse working voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	15.5	-	-	V
Reverse current	at V <sub>R</sub> = 15.5 V	I <sub>R</sub>	-	-	1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	17	18.7	-	V
Deverse elemeine veltage	at I <sub>PP</sub> = 1 A	V	-	20.4	24.5	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 8 A$	V <sub>C</sub>	-	26.6	30.6	V
	at $V_R = 0 V$ ; f = 1 MHz	C <sub>D</sub>	-	45	60	pF
Capacitance	at V <sub>R</sub> = 7.5 V; f = 1 MHz		-	25	-	pF

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

ELECTRICAL CHARACTERISTICS GSOT24C								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse working voltage	at I <sub>R</sub> = 1 µA	V <sub>RWM</sub>	24.5	-	-	V		
Reverse current	at V <sub>R</sub> = 24.5 V	I <sub>R</sub>	-	-	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	27.5	30.7	-	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	- V <sub>C</sub>	-	34	41	V		
	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A		-	40	48	V		
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	-	33	40	pF		
	at V <sub>R</sub> = 12 V; f = 1 MHz		-	18	-	pF		

Note

• Ratings at 25 °C, ambient temperature unless otherwise specified. BiAs mode (between pin 1 to 2 or pin 2 to 1; pin 3 not connected)

## Vishay Semiconductors Two-Line ESD-Protection in SOT-23

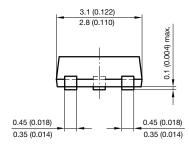


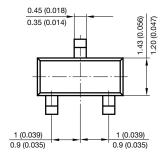
ELECTRICAL CHARACTERISTICS GSOT36C									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines			
Reverse working voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	36.5	-	-	V			
Reverse current	at V <sub>R</sub> = 36.5 V	I <sub>R</sub>	-	-	1	μA			
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	39.5	43.7	-	V			
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	50	60	V			
	at $I_{PP} = I_{PPM} = 3.5 \text{ A}$		-	60	72	V			
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	-	26	33	pF			
	at V <sub>R</sub> = 18 V; f = 1 MHz		-	10	-	pF			

#### Note

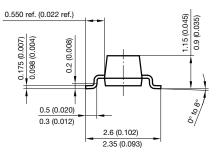
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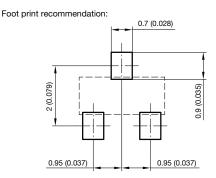
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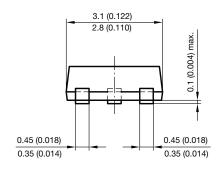


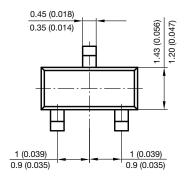
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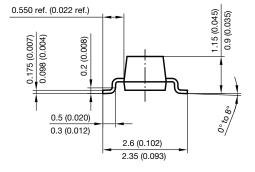
#### SOT-23



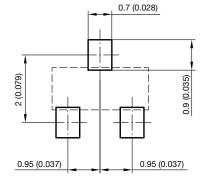
#### PACKAGE DIMENSIONS in millimeters (inches)







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