SM7302ESKP

Dual N-Channel Enhancement Mode MOSFET

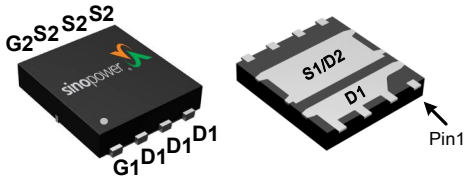
Features

- **Channel 1**
  30V/48A,
  $R_{DS(ON)}$ = 7mΩ (max.) @ V_{GS} = 10V
  $R_{DS(ON)}$ = 10mΩ (max.) @ V_{GS} = 4.5V
- **Channel 2**
  30V/50A,
  $R_{DS(ON)}$ = 4mΩ (max.) @ V_{GS} =10V
  $R_{DS(ON)}$ = 5.9mΩ (max.) @ V_{GS} =4.5V
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free Available (RoHS Compliant)

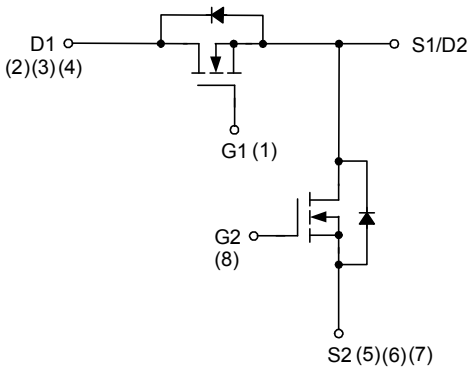
Applications

- Power Management in Desktop Computer or DC/DC Converters.

Pin Description



DFN5x6B-8_EP2



N-Channel MOSFET

Ordering and Marking Information

SM7302ES □□□-□□ □ Assembly Material Handling Code Temperature Range Package Code	Package Code KP : DFN5x6B-8_EP2 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM7302ES KP : SM7302 XXXXX	XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings (T_A = 25°C unless otherwise noted)

Symbol	Parameter		Channel 1	Channel 2	Unit
Common Ratings					
V_{DSS}	Drain-Source Voltage		30		V
V_{GSS}	Gate-Source Voltage		±20		
T_J	Maximum Junction Temperature		150		°C
T_{STG}	Storage Temperature Range		-55 to 150		
I_S	Diode Continuous Forward Current		5	20	A
I_{DM} [b]	Pulse Drain Current Tested	T_C=25°C	120	150	
I_D [a]	Continuous Drain Current	T_C=25°C	48	50	
		T_C=100°C	30	31.8	
P_D	Maximum Power Dissipation	T_C=25°C	25	41	W
		T_C=100°C	10	16	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	5	3	°C/W
I_{DM}	Pulse Drain Current Tested	T_A=25°C	48	80	A
I_D	Continuous Drain Current	T_A=25°C	12	20	
		T_A=70°C	9.5	16	
P_D	Maximum Power Dissipation	T_A=25°C	1.6	2.7	W
		T_A=70°C	1	1.7	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	t ≤ 10s	40	20	°C/W
		Steady State	80	45	
I_{AS} [c]	Avalanche Current, Single pulse	L=0.5mH	14	16.8	A
E_{AS} [c]	Avalanche Energy, Single pulse	L=0.5mH	50	70.56	mJ

Note a：Package is limited to 50A.
Note b：Pulse width limited by max. junction temperature.
Note c：UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature T_j=25°C).

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Channel 1 Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Channel 1			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250μA	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=24V, V_{GS}=0V	-	-	1	μA
		T_J=85°C	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	1.5	1.8	2.5	V
I_{GSS}	Gate Leakage Current	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$ [d]	Drain-Source On-state Resistance	V_{GS}=10V, I_{DS}=15A	-	5.8	7	mΩ
		T_J=125°C	-	8.4	-	
		V_{GS}=4.5V, I_{DS}=10A	-	7.7	10	
Gfs	Forward Transconductance	V_{DS}=5V, I_{DS}=15A	-	29	-	S
Diode Characteristics						
V_{SD} [d]	Diode Forward Voltage	I_{SD}=2A, V_{GS}=0V	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	I_{DS}=15A, dI_{SD}/dt=100A/μs	-	12	-	ns
t_a	Charge Time		-	6	-	
t_b	Discharge Time		-	6	-	
Q_{rr}	Reverse Recovery Charge		-	2.9	-	nC
Dynamic Characteristics [e]						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	0.7	1.5	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=15V, Frequency=1.0MHz	-	1180	1534	pF
C_{oss}	Output Capacitance		-	180	-	
C_{rss}	Reverse Transfer Capacitance		-	110	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=15V, R_L=15Ω, I_{DS}=1A, V_{GEN}=10V, R_G=6Ω	-	9	14	ns
t_r	Turn-on Rise Time		-	8	13	
$t_{d(OFF)}$	Turn-off Delay Time		-	25	40	
t_f	Turn-off Fall Time		-	8	14	
Gate Charge Characteristics [e]						
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=4.5V, I_{DS}=15A	-	10	14	nC
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=10V, I_{DS}=15A	-	20	-	
Q_{gth}	Threshold Gate Charge		-	2.2	-	
Q_{gs}	Gate-Source Charge		-	4	-	
Q_{gd}	Gate-Drain Charge		-	3.8	-	

Note d : Pulse test ; pulse width≤300μs, duty cycle≤2%.
Note e : Guaranteed by design, not subject to production testing.

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Channel 2 Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Channel 2			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250μA	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=24V, V_{GS}=0V	-	-	1	μA
		T_J=85°C	-	-	30	μA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	1.4	1.8	2.5	V
I_{GSS}	Gate Leakage Current	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$ [d]	Drain-Source On-state Resistance	V_{GS}=10V, I_{DS}=30A	-	3.3	4	mΩ
		T_J=125°C	-	4.9	-	
		V_{GS}=4.5V, I_{DS}=15A	-	4.5	5.9	
Gfs	Forward Transconductance	V_{DS}=5V, I_{DS}=10A	-	23	-	S
Diode Characteristics						
V_{SD} [d]	Diode Forward Voltage	I_{SD}=1A, V_{GS}=0V	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	I_{DS}=30A, dI_{SD}/dt=100A/μs	-	37	-	ns
t_a	Charge Time		-	21	-	
t_b	Discharge Time		-	16	-	
Q_{rr}	Reverse Recovery Charge		-	23	-	nC
Dynamic Characteristics [e]						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	1.3	2.6	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=15V, Frequency=1.0MHz	-	1280	1664	pF
C_{oss}	Output Capacitance		-	800	-	
C_{rss}	Reverse Transfer Capacitance		-	64	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=15V, R_L=15Ω, I_{DS}=1A, V_{GEN}=10V, R_G=6Ω	-	14.3	26	ns
t_r	Turn-on Rise Time		-	10	18	
$t_{d(OFF)}$	Turn-off Delay Time		-	30	54	
t_f	Turn-off Fall Time		-	32.6	59	
Gate Charge Characteristics [e]						
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=4.5V, I_{DS}=30A	-	9.1	-	nC
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=10V, I_{DS}=30A	-	19.6	27.5	
Q_{gth}	Threshold Gate Charge		-	2.2	-	
Q_{gs}	Gate-Source Charge		-	3.5	-	
Q_{gd}	Gate-Drain Charge		-	2.4	-	

Note d : Pulse test; pulse width≤300μs, duty cycle≤2%.

Note e : Guaranteed by design, not subject to production testing.

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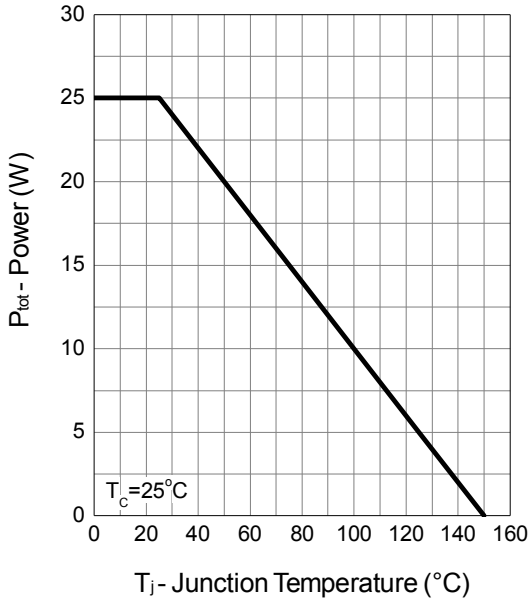
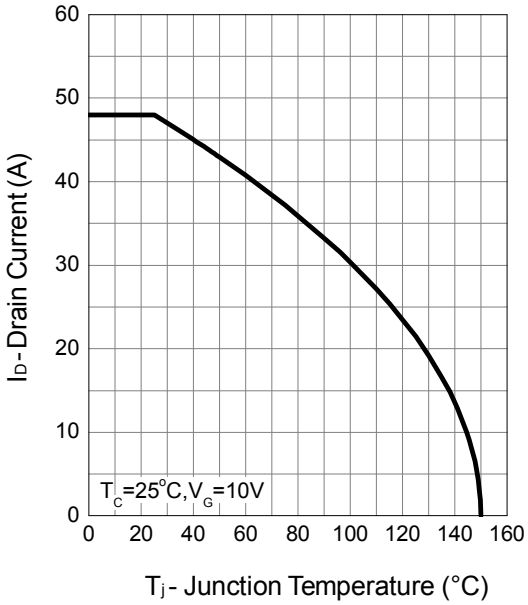
Channel 1 Typical Operating Characteristics

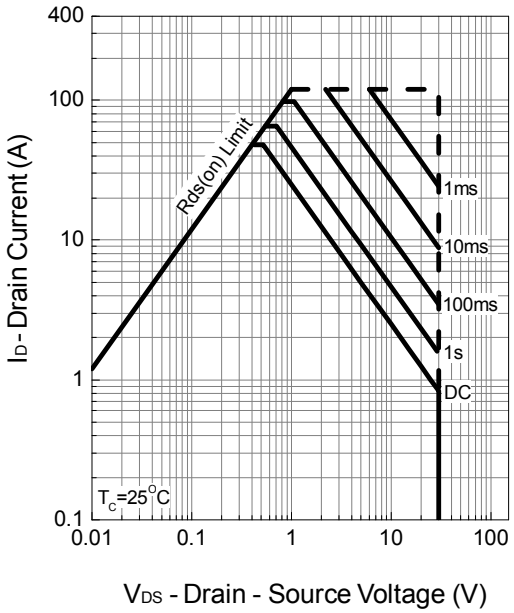
Power Dissipation



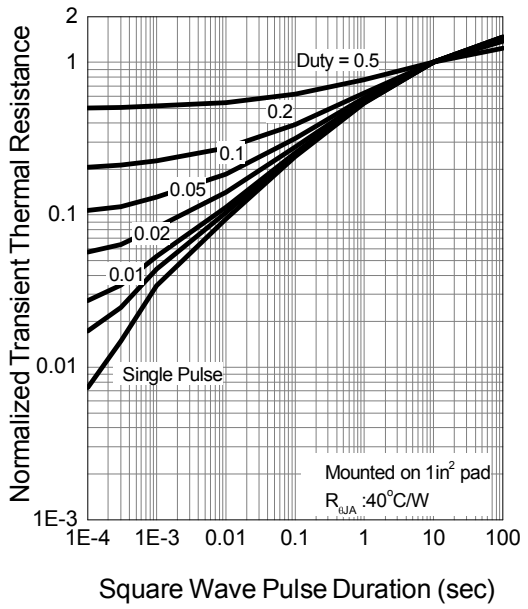
Drain Current



Safe Operation Area



Thermal Transient Impedance



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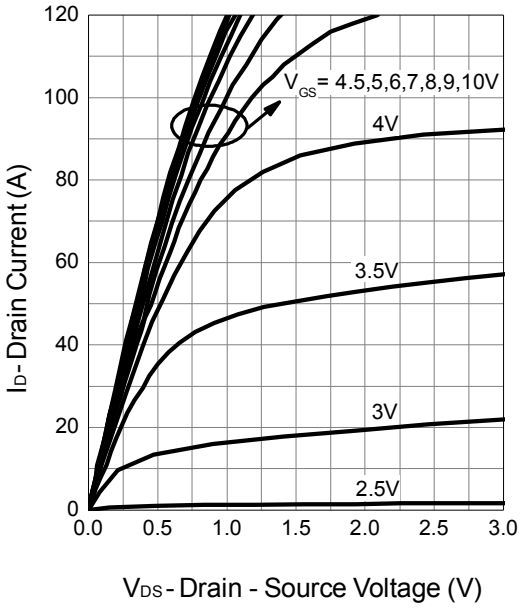
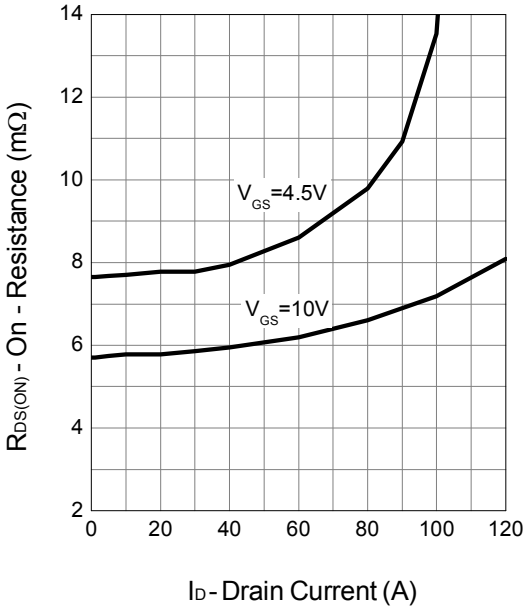
Channel 1 Typical Operating Characteristics (Cont.)

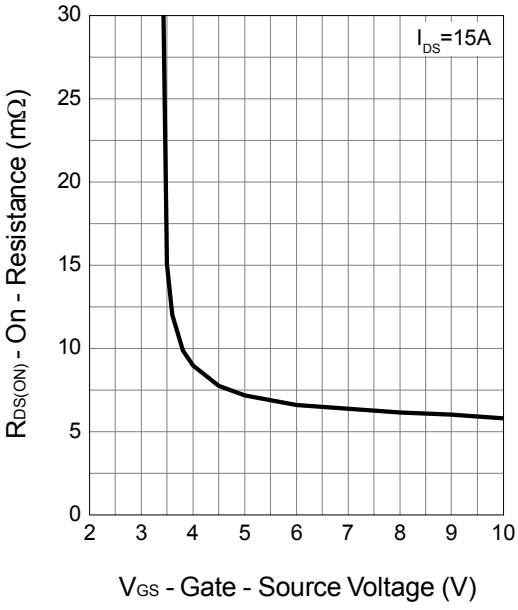
Output Characteristics



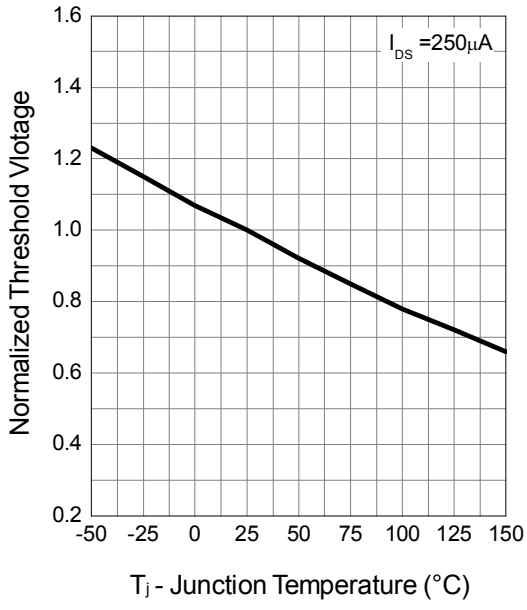
Drain-Source On Resistance



Gate-Source On Resistance



Gate Threshold Voltage



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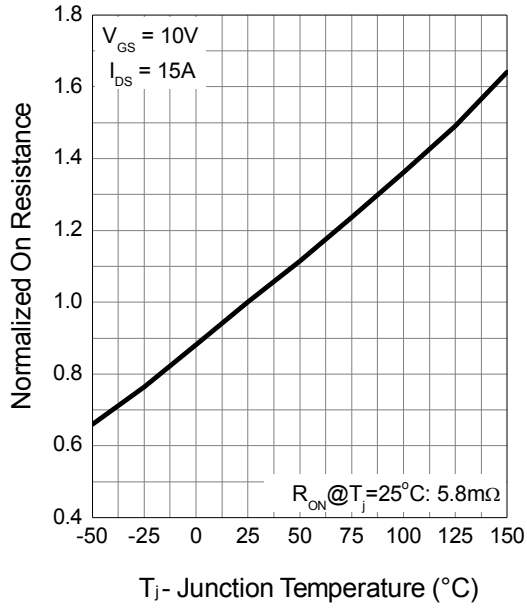
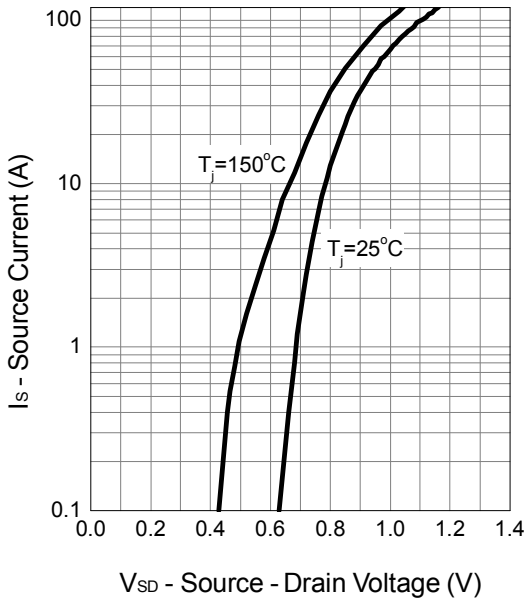
Channel 1 Typical Operating Characteristics (Cont.)

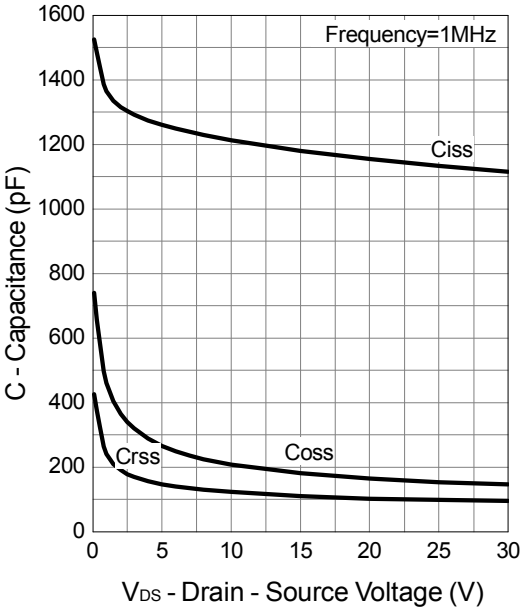
Drain-Source On Resistance



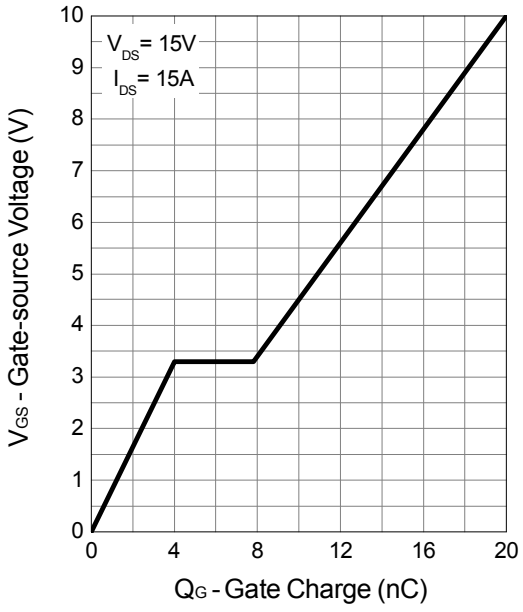
Source-Drain Diode Forward



Capacitance



Gate Charge



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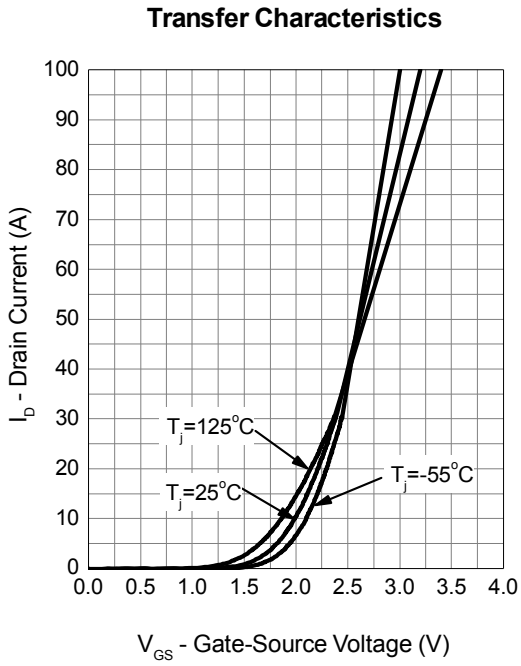
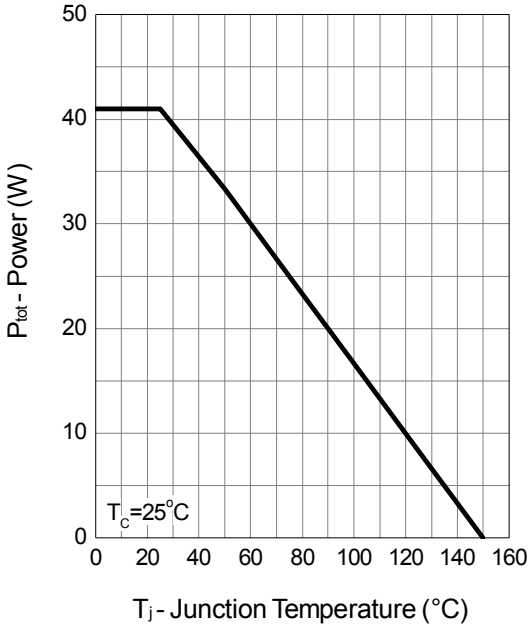
Channel 1 Typical Operating Characteristics (Cont.)

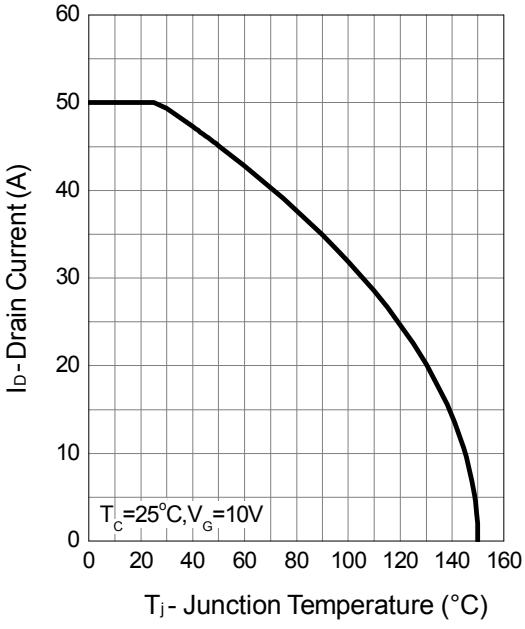


Channel 2 Typical Operating Characteristics

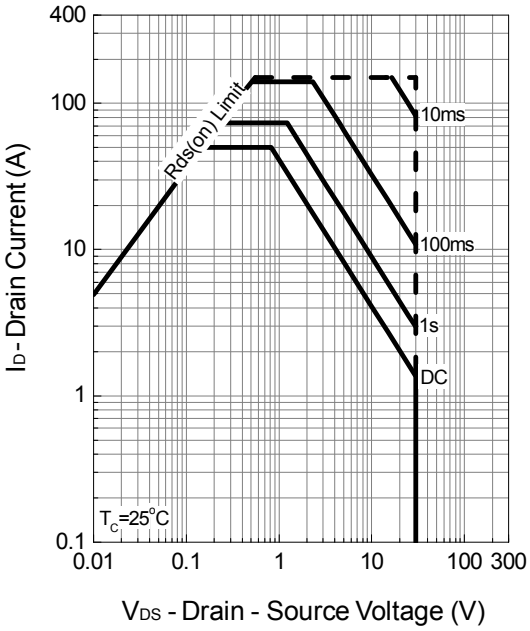
Power Dissipation



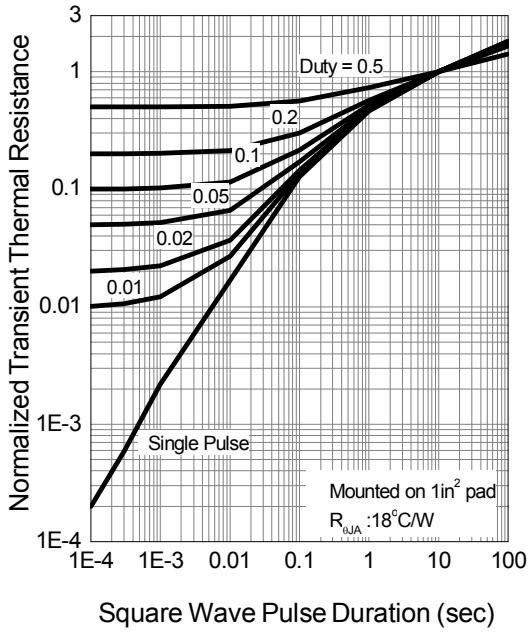
Drain Current



Safe Operation Area



Thermal Transient Impedance



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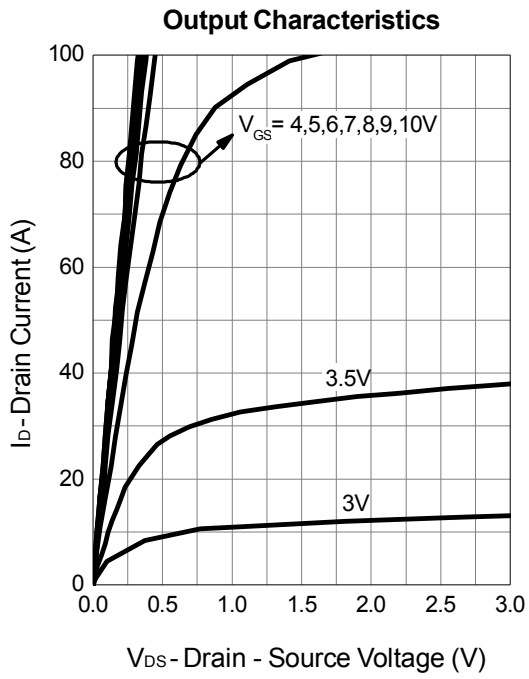
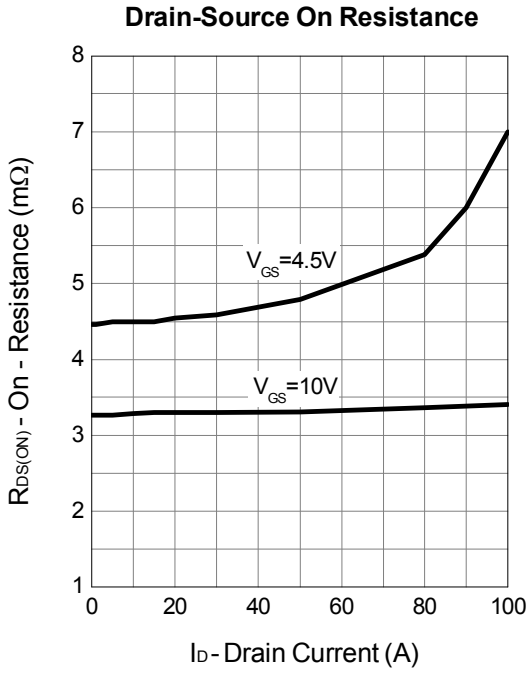
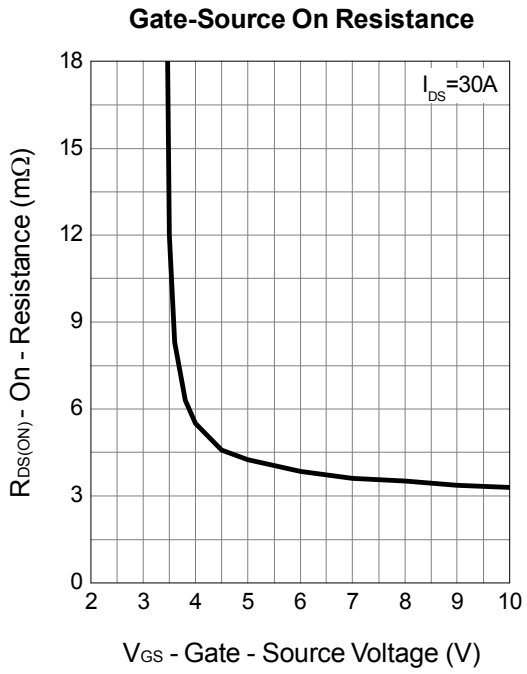
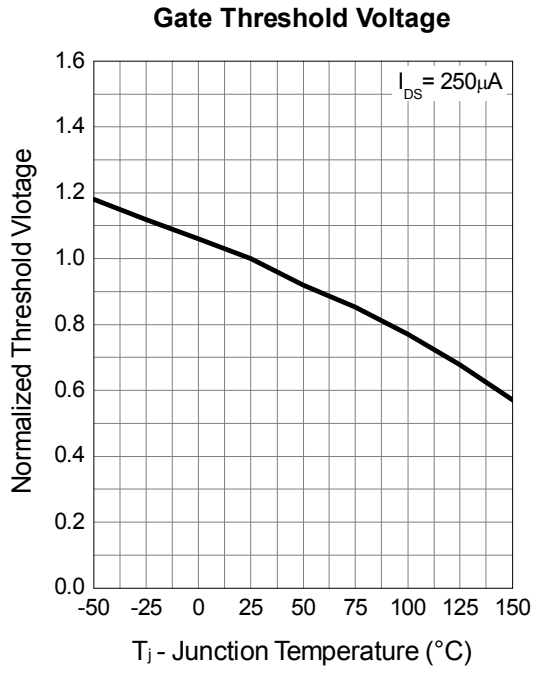
Channel 2 Typical Operating Characteristics (Cont.)









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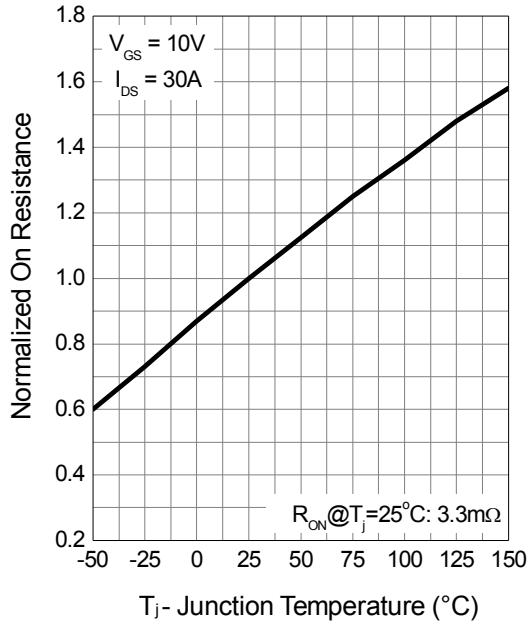
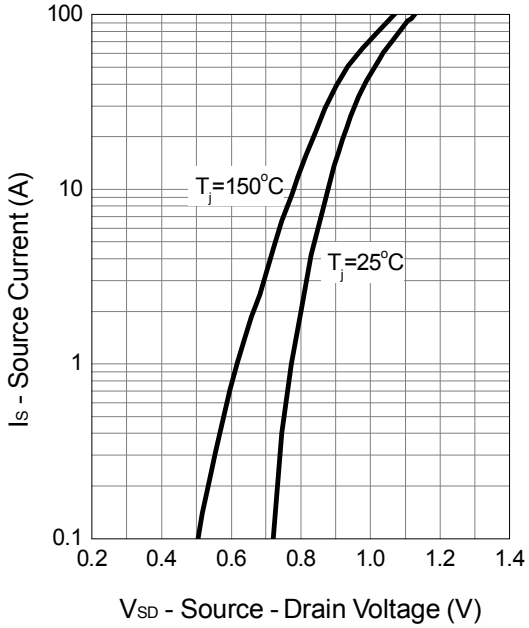
Channel 2 Typical Operating Characteristics (Cont.)

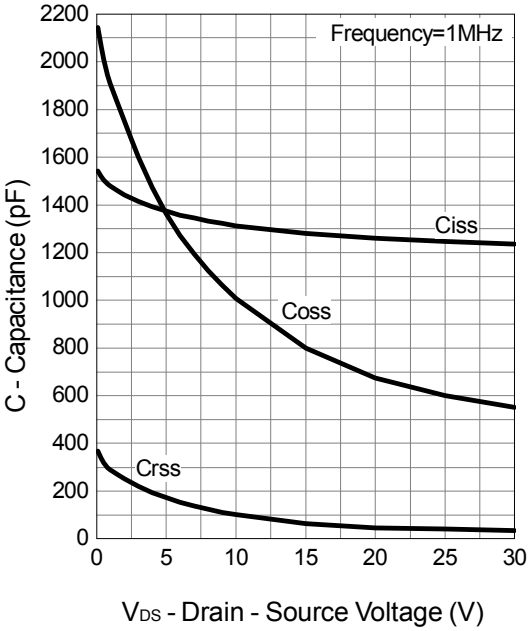
Drain-Source On Resistance



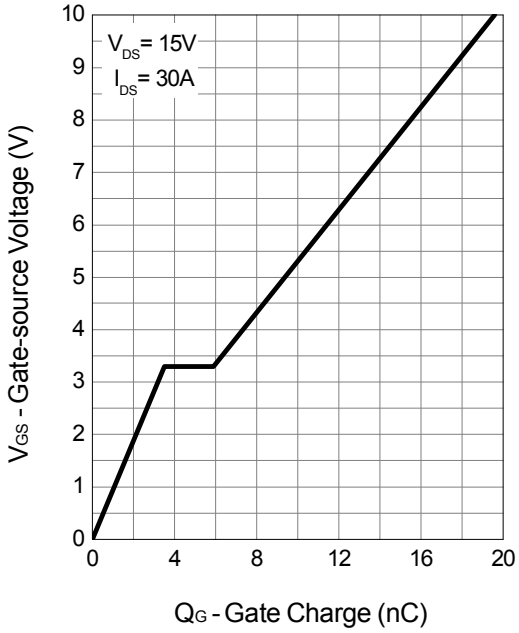
Source-Drain Diode Forward



Capacitance



Gate Charge



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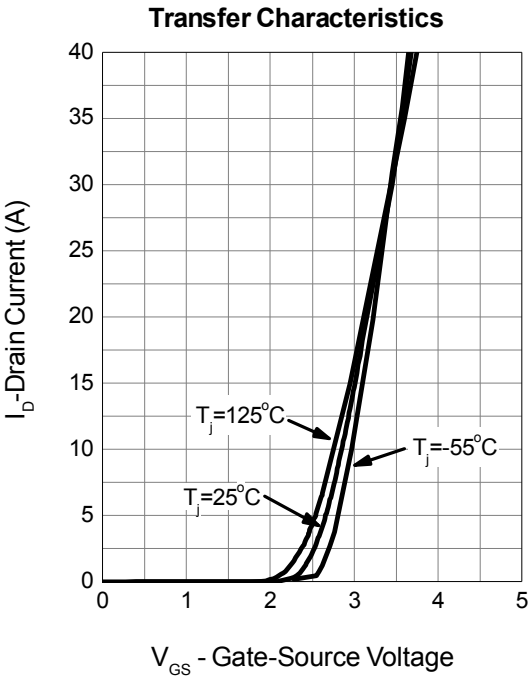
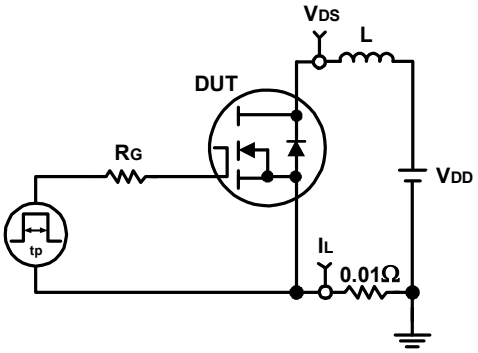
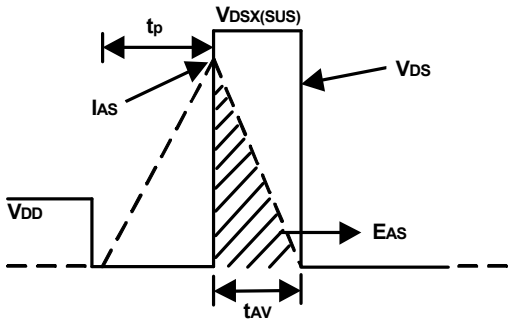
Channel 2 Typical Operating Characteristics (Cont.)

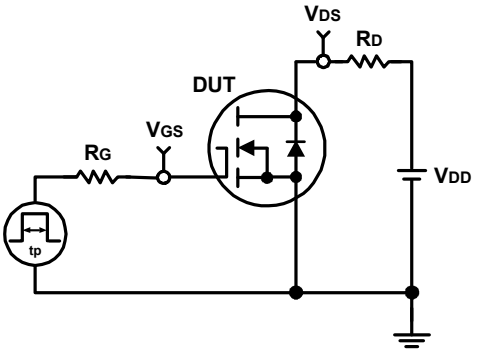


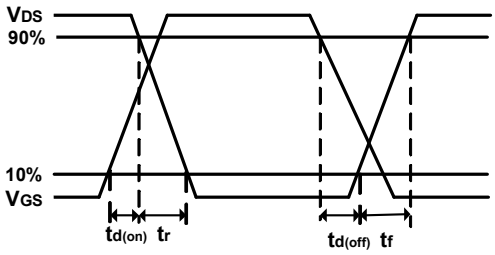
Avalanche Test Circuit and Waveforms





Switching Time Test Circuit and Waveforms





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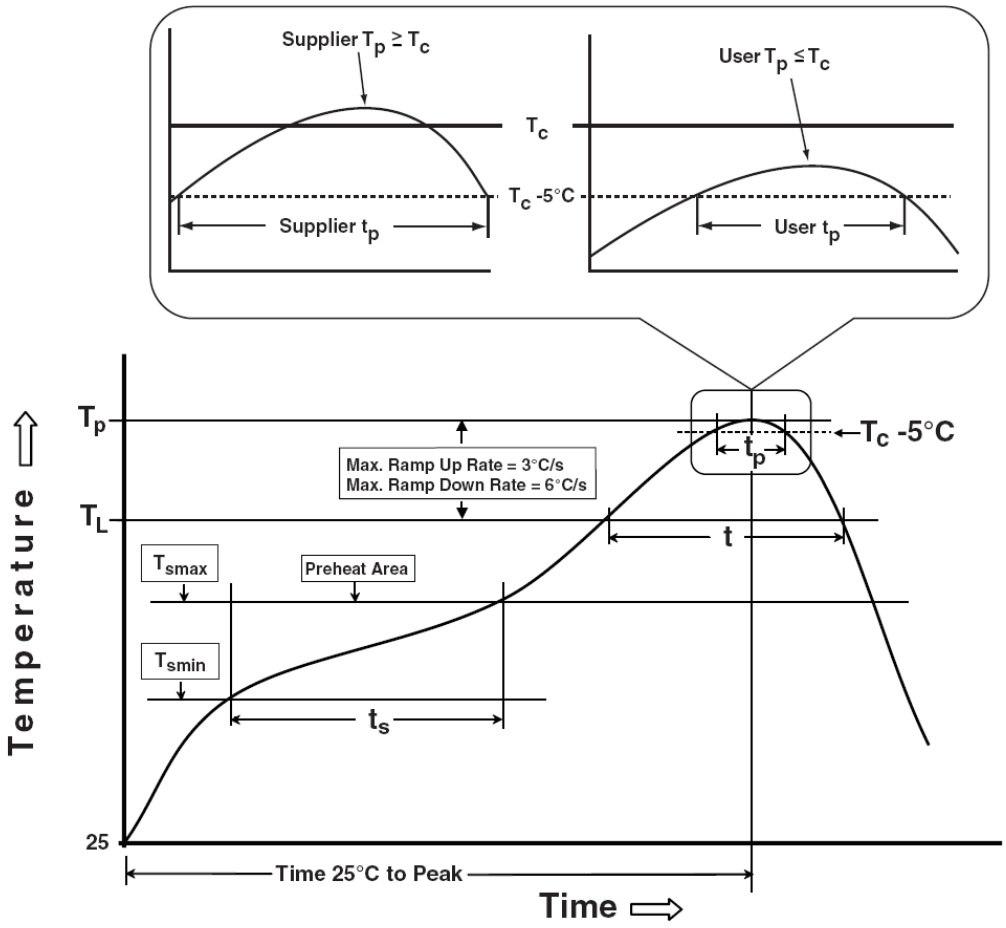
Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_P)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm^3 <350	Volume mm^3 ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm^3 <350	Volume mm^3 350-2000	Volume mm^3 >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ Tjmax
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ Tjmax
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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