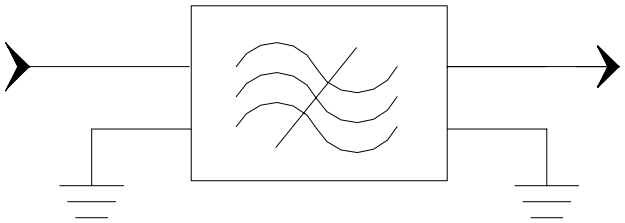
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	69.9	70	70.1
Insertion Loss	dB		25	26
3 dB Bandwidth	MHz	9.7	9.75	9.8
35 dB Bandwidth	MHz		10.48	10.55
45 dB Bandwidth	MHz		10.56	10.95
50 dB Bandwidth	MHz		10.62	11.75
Passband Variation	dB		0.8	1.2
Ultimate Rejection(f0±15MHz)	dB	55	58	
Absolute Delay	usec		3.6	
Material Temperature coefficient	KHz/°C	-6.58		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.0x12.8x4.7mm^3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

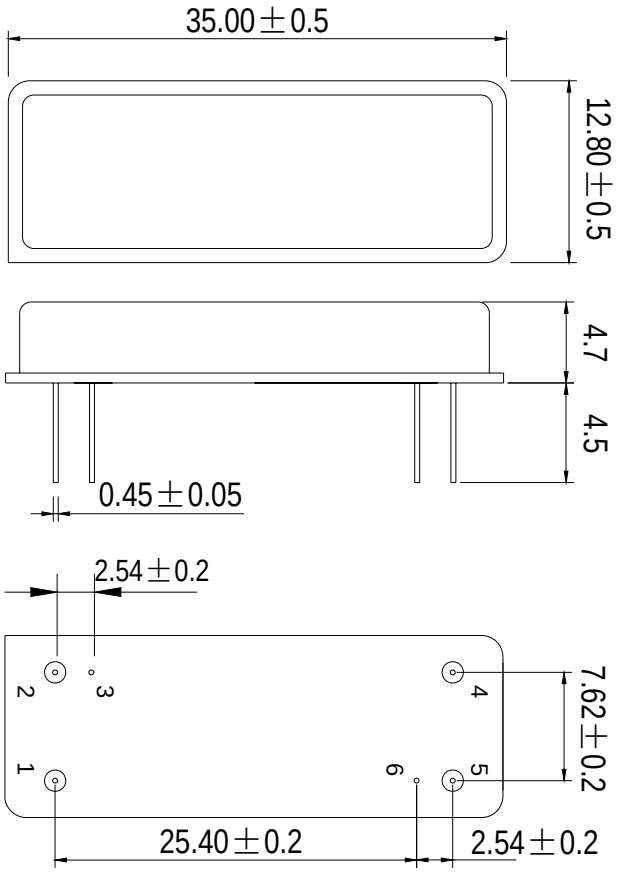
Matching Configuration



Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

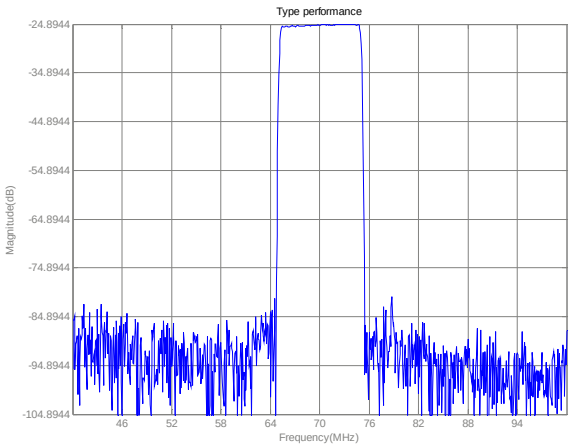
Package Dimension



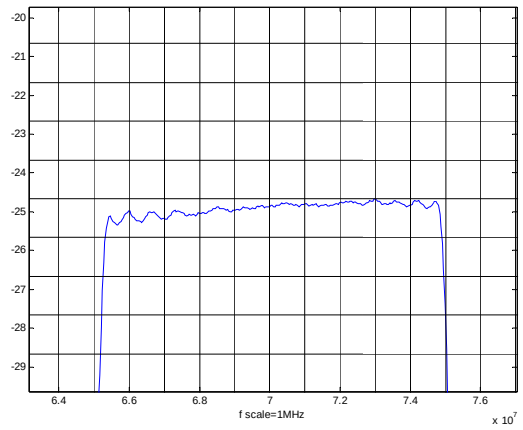
Input:1
Output:5

Typical Performance

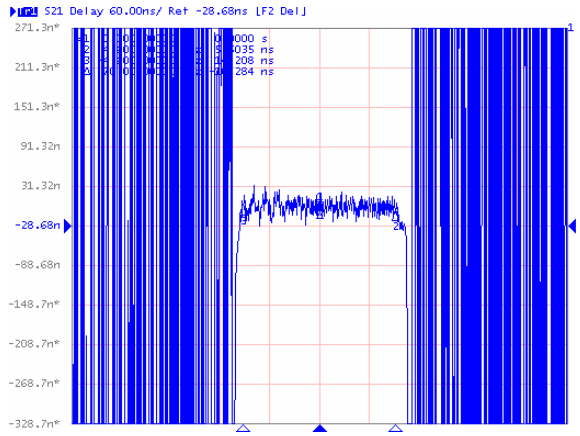
Frequency Respond



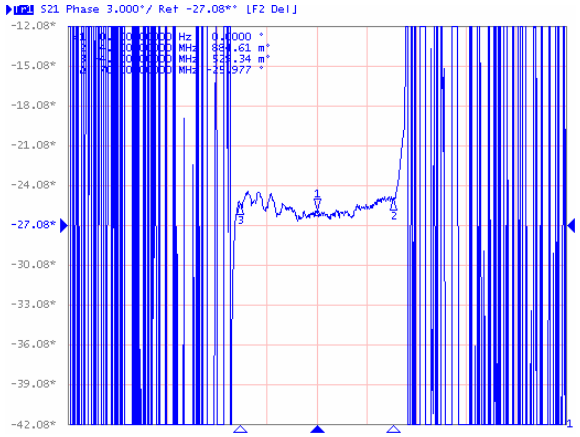
Passband Respond



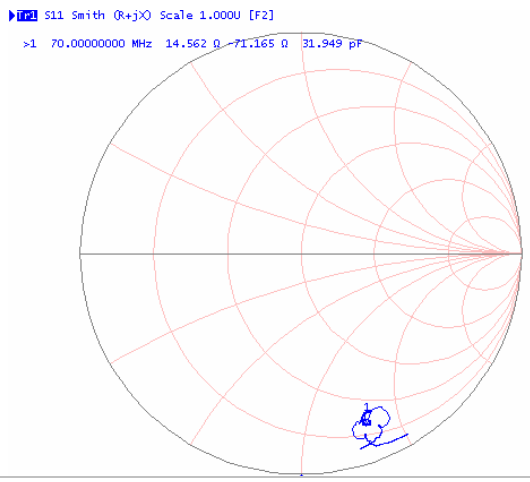
Group delay Variation（F_0±4.6MHz）



Phase Linearity（F_0±4.6 MHz）



Simth Chart S11



Simth Chart S22

