
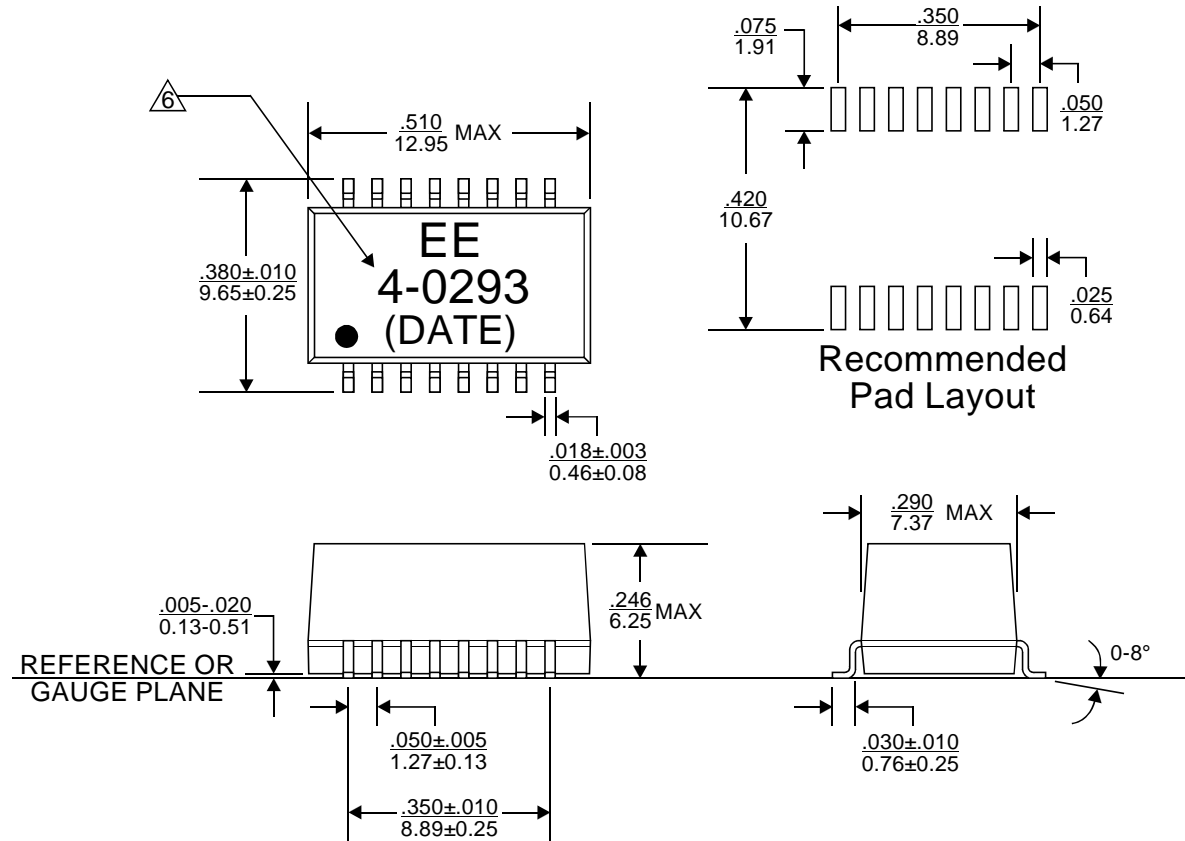


REVISIONS			
REV.	DESCRIPTION	ECN NO.	DATE
01	FIRST RELEASE FOR RFQ#D2005-5730	N/A	02/14/05
02	UPDATE ES DRAWING	EE8542	09/04/06
03	ADD INTERNAL PAGE	EE9952	12/12/07
04	UPDATE INTERNAL PAGE	EE10267	04/17/08
05	UPDATE INTERNAL PAGE	EE12597	02/10/10
06	UPDATE INTERNAL PAGE	EE12731	04/14/11

PART NUMBER		PART DESCRIPTION		<b>PAGE 4&amp;5 ARE FOR INTERNAL ONLY</b> TITLE <b>TRANSFORMER, 100BASE-T(PoE),          SINGLE, SOIC, 16 PIN, EXT. TEMP.</b>																												
824-00293		Standard version, with lead																														
824-00293F		RoHS compliant per EU Directive 2002/95/EC (without exemption of solder content)																														
<p align="center"><b><u>WARNING !</u></b></p> <p>ALL SHEETS OF THIS DOCUMENT ARE CONTROLLED DOCUMENTATION AND ARE NOT TO BE RELEASED OUTSIDE OF E&amp;E OR ITS SUB-CONTRACTORS WITHOUT AUTHORIZATION.</p>		UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCH/mm.		 <b>E &amp; E Magnetic Products Ltd.</b>																												
		<p align="center"><u>TOLERANCE ARE:</u></p> <table border="0"> <tr> <td>INCH</td> <td>mm</td> <td>ANGLE</td> </tr> <tr> <td>.XXX ± .005</td> <td>.XX ± .13</td> <td>X.X ± 0.3</td> </tr> <tr> <td>.XX ± .02</td> <td>.X ± .5</td> <td>X. ± 1</td> </tr> </table>		INCH	mm	ANGLE	.XXX ± .005	.XX ± .13	X.X ± 0.3	.XX ± .02	.X ± .5	X. ± 1	<table border="1"> <thead> <tr> <th>APPROVALS</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td>DRAWN BY Y.L YUNG</td> <td>04/14/11</td> </tr> <tr> <td>PROJ. ENG D. YUNG</td> <td>04/14/11</td> </tr> <tr> <td>APPROVED BY V. TSUI</td> <td>04/14/11</td> </tr> <tr> <td>Q.A. C.M. TSUI</td> <td>04/14/11</td> </tr> </tbody> </table>		APPROVALS	DATE	DRAWN BY Y.L YUNG	04/14/11	PROJ. ENG D. YUNG	04/14/11	APPROVED BY V. TSUI	04/14/11	Q.A. C.M. TSUI	04/14/11	<table border="1"> <tr> <td colspan="2">DRAWING NO./MODEL</td> <td>REV</td> </tr> <tr> <td align="center" colspan="2"><b>824-00293</b></td> <td align="center"><b>06</b></td> </tr> </table>		DRAWING NO./MODEL		REV	<b>824-00293</b>		<b>06</b>
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- Dimensions are specified in  $\frac{\text{inches}}{\text{mm}}$  with higher precedence in inches.
- Unless otherwise specified, all tolerances are  $\pm \frac{.010}{0.25}$ .
- Coplanarity:  $\frac{.004}{0.10}$  max.
- "(DATE)" includes at least the manufacturing date code (in YYWW format) plus two character country code.
- Dimensions are to be measured in accordance with JEDEC standard 95-1.

6. For RoHS compliant version, 824-00293F, the part will be marked with "4-0293F", instead of "4-0293".

## MECHANICAL OUTLINE



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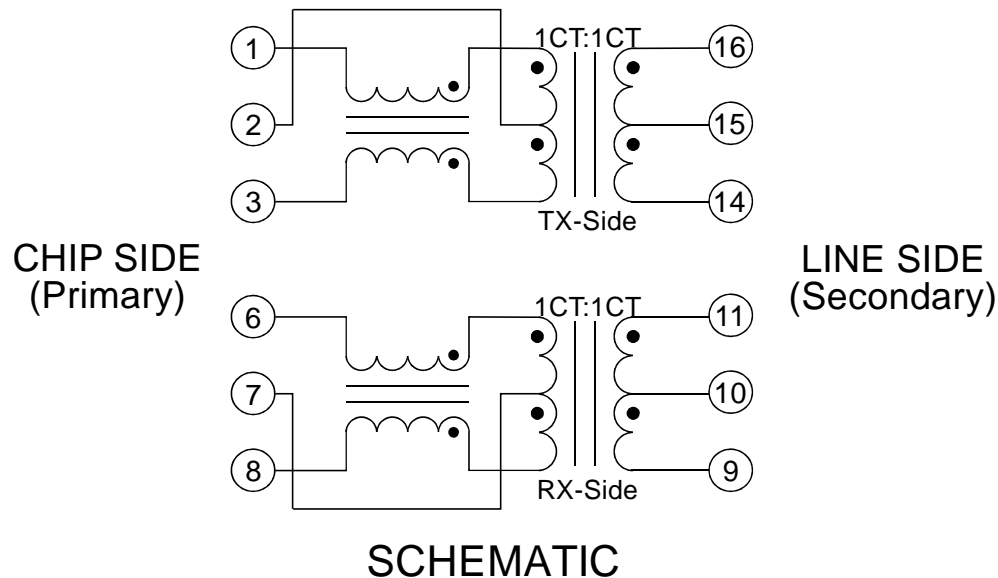
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**ELECTRICAL SPECIFICATION @25°C:**

PARAMETERS	UNIT	TX Side	RX Side
Turn Ratio (Primary):(Secondary)	-	1CT:1CT ± 2%	1CT:1CT ± 2%
Polarity (Primary): (Secondary)	-	Per Schematic	Per Schematic
Inductance, Ls(Primary) @100KHz, 0.1Vrms, 8mADC	uH	350 Min	350 Min
Insertion Loss @0.1-100MHz	dB	1.1 Max	1.1 Max
Return Loss @30MHz	dB	16.0 Min	16.0 Min
Return Loss @60MHz	dB	10.0 Min	10.0 Min
Return Loss @80MHz	dB	10.0 Min	10.0 Min
Crosstalk @30MHz (for refer. only)	dB	35 Min	
Crosstalk @60MHz (for refer. only)	dB	30 Min	
Crosstalk @100MHz (for refer. only)	dB	30 Min	
Hipot (Primary):(Secondary)	Vrms	1500 Min	1500 Min

7. Operating temperature: -40°C to +85°C.

8. Balanced DC line current capability (LINE SIDE): 350mADC maximum.



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