



Z84C15

IPC/EIPC ENHANCED INTELLIGENT PERIPHERAL CONTROLLER

GENERAL DESCRIPTION

The Z84C15 are a series of Intelligent Peripheral Controllers (IPC) targeted for a broad range of applications from error correction modems/faxes, to enhancement/cost reduction of existing Z80 hardware using Z80 based discrete devices. The products are software and hardware compatible with the discrete devices Z84C00, Z84C20, Z84C30 and Z84C40.

The enhanced version, Z84C15, is pin compatible with the Z84015 and both are housed in a 100-pin QFP package. Zilog also offers Z84C15 in a 100-pin VQFP package.

FEATURES

- Z84C15 Enhancements:
 - Power-On Reset
 - Two Additional Chip Select Pins
 - 32-Bit CRC for Channel A of SIO
 - WAIT State Generator
 - Simplified EV Mode
 - Schmitt Trigger I/O, No Tx/Rx Clock
 - Crystal Divide-by-One Mode
 - Z84C15 Features the Z84015 and Above Enhancements
 - Z84015 Features:
 - Z84C00 - CPU
 - Z84C40 - SIO - Two Serial Channels
 - Z84C30 - CTC - 4x8 bit C/T
 - Z84C20 - PIO
 - Watch-Dog Timer (WDT)
 - Clock Generator Controller (CGC)
 - Low Power Consumption
Three Operating Modes
 - 16 MHz Operation for Z84C15 Only.
 - Voltage Range 5V $\pm 10\%$
 - 100-Pin VQFP Package for Z84C15
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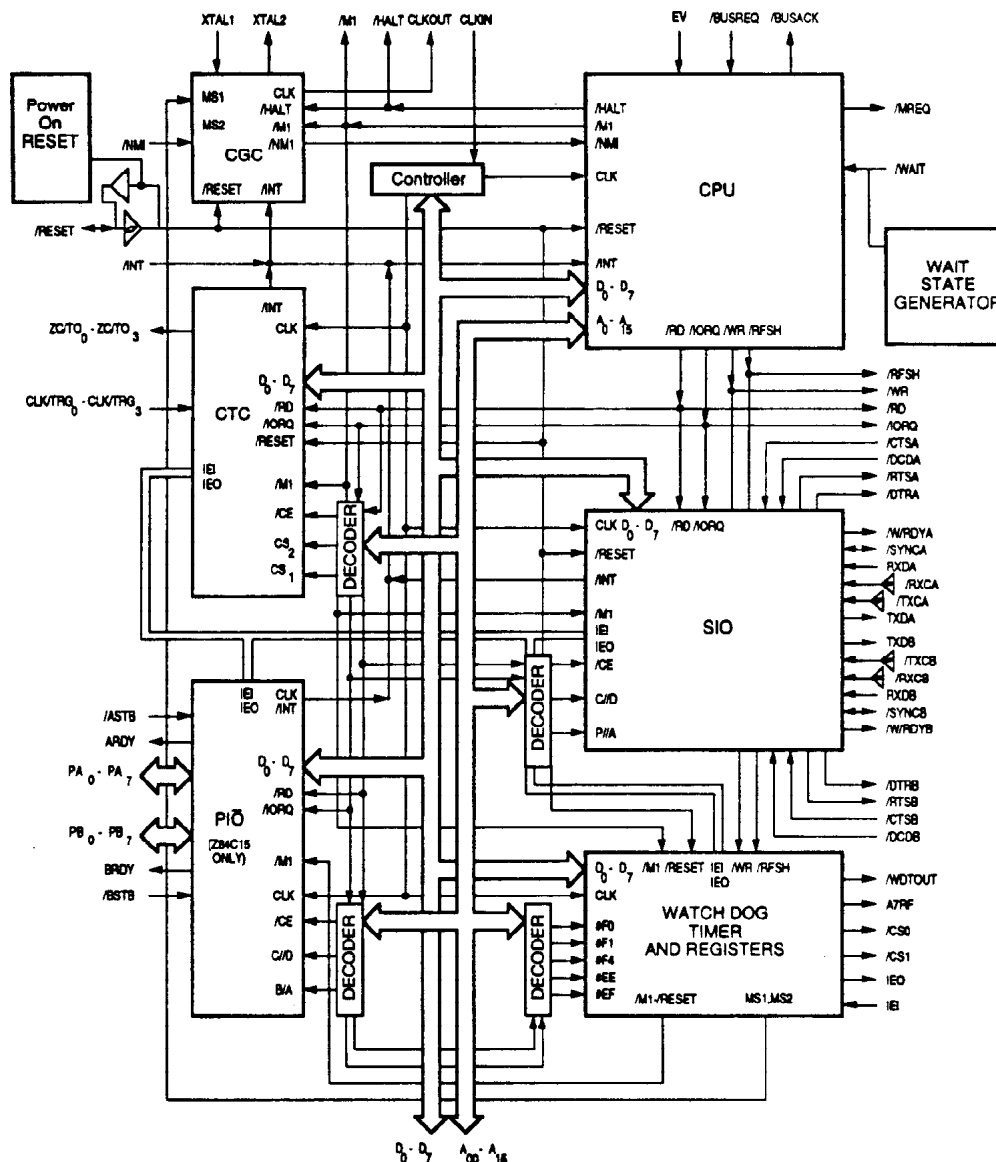


Figure 1. Z84C15 Functional Block Diagram

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