HM5083

3A Synchronous Buck 3 Cells Li-ion Charger

DESCRIPTION

The HM5083 is a 3A 3 Cells Li-Ion battery charger. It utilizes a 500 KHz synchronous buck converter topology to reduce power dissipation during charging. Low power dissipation, an internal MOSFET allow a physically small charger that can be embedded in a wide range of handheld applications. The HM5083 includes complete charge termination circuitry, automatic recharge and a ±1% 12.6V float voltage. Battery charge current, charge timeout and end-of-charge indication parameters are set with external components. Additional features include shorted cell detection; temperature qualified charging and overvoltage protection. The HM5083 is available in a ESOP8 and DFN10 package

FEATURES

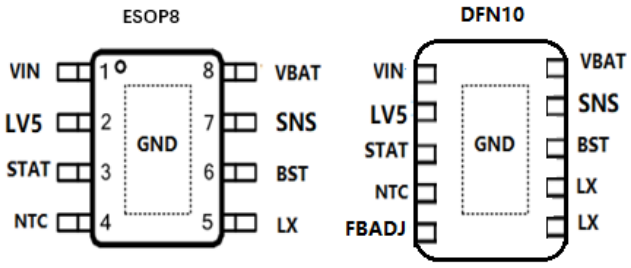
- Input voltage range 4V~22V
- Dynamic input current allocation for maximum charging rate
- 3A Maximum Charge Current
- No External MOSFETs and Blocking Diode Required
- Efficiency up to 95%
- Constant-Current/Constant-Voltage Operation with Thermal Regulation to Maximize Charge Rate Without Risk of Overheating
- Optional Battery Temperature Monitoring Before and During Charge Automatic Sleep Mode for Low-Power
- Over Current Protection
- Consumption Available in ESOP8 and DFN10
- RoHS Compliant and 100% Lead (Pb)-Free

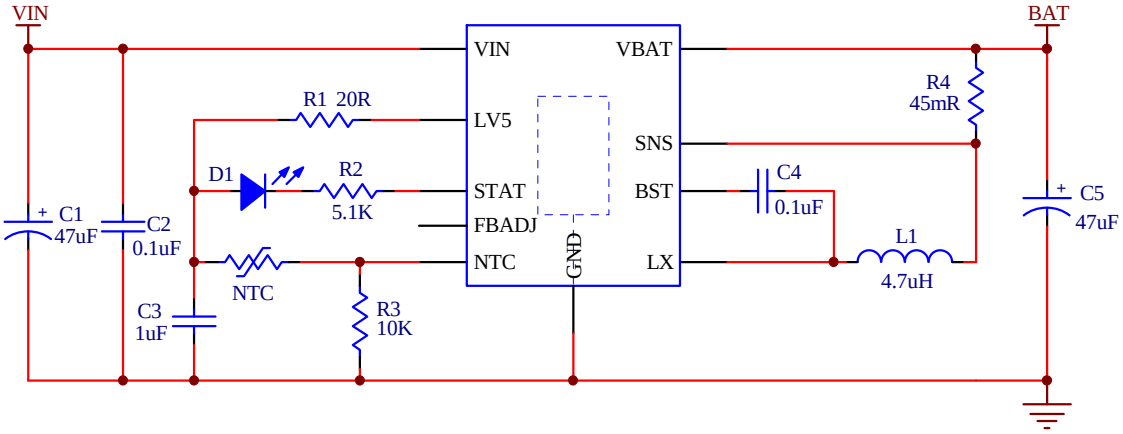
APPLICATIONS

Portable Media Players
Cellular and Smart mobile phone
PDA/DSC
Handheld Battery-Powered Devices
Handheld Computers
Charging Docks and Cradles

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PACKAGE AND APPLICATION

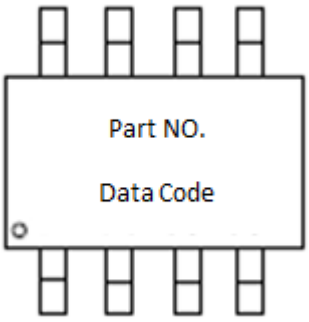




ORDING INFORMATION

Part Number	Package Type	Package Qty	Op Temp(°C)
HM5083E	ESOP8	4000	-40~85
HM5083D	DFN10	3000	-40~85

MARK INFORMATION



ABSOLUTE MAXIMUM RATINGS (1)

Over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		MIN	MAX	UNIT
VIN /VBAT/SNS/LX to GND	VIN, VBAT, SNS,LX	-0.3	25	V
BST to LX	BST,LX	-0.3	6.5	
ALL OTHER PINS to GND	VIN, BAT, SNS, LX, BST	-0.3	13	V
ESD rating, Human Body Model (HBM)	VIN		2	kV
	BAT, SNS, LX, GND ,BST		3	
ESD rating, Charging Device Model (CDM)			200	V
Operating Junction Temperature	T_J	-40	125	°C
Storage Temperature Range	T_{stg}	-65	150	

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

THERMAL CHARACTERISTICS

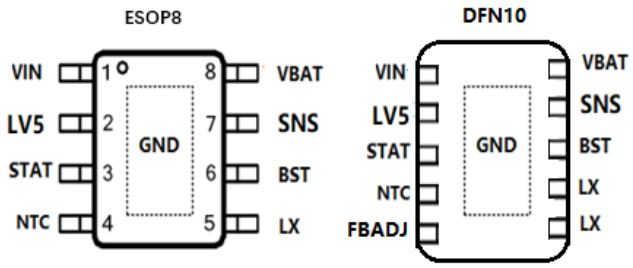
over operating free-air temperature range (unless otherwise noted)

THERMAL METRIC(θ_{JA})			UNIT
ESOP8	Package thermal impedance(1)	45	°C/W
DFN8	Package thermal impedance(1)	65	°C/W

(1) The package thermal impedance is calculated in accordance with JESD 51-7.

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PINOUT



PIN FUNCTIONS

Pin Name	PIN Number		
	ESOP8	ESSOP10	Description
LX	5	6，7	Switch pin. Connect to external inductor.
BST	6	8	Internal charge pump boost pin.
STAT	3	3	Open-Drain Charge Status Output. When the battery is charging, this pin is pulled low by an internal N-channel MOSFET. When the HM5083 detects an under voltage lockout condition, STAT is forced high impedance.
NTC	4	4	NTC (Negative Temperature Coefficient) Thermistor Input. This pin senses the Temperature of the battery pack and stops the charger when the temperature is out of range. Connect to GND for disabling
VBAT	8	10	Battery pin.
SNS	7	9	Detecting pin of charge current.
LV5	2	2	5V LDO output PIN. Connect a minimum 1uF capacitor between this PIN and GND.
VIN	1	1	Positive Supply Voltage Input. Decouple with a 10μF or larger surface mounted
GND	ePAD	ePAD	Ground.
FBADJ		5	CV adjust PIN. Connect resistor to GND increase CV voltage. Connect resistor to VBAT decrease CV voltage.

ELECTRICAL CHARACTERISTICS

(TA=25℃, VIN = 5V, unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Units
V_{IN}	Adapter/USB Voltage		4		22	V
I_{CC}	Input Supply Current	Standby Mode (Charge Terminated)		1		mA
V_{FLOAT}	Regulated Output (Float) Voltage	FBADJ floating		12.6		V
V_{RS}	Program Charging Current (For RS to BAT)	$V_{TRIKL}<V_{BAT}<V_{FLOAT}$		50		mV
I_{BAT}	BAT Pin Current	R_{SNS}=50mΩ,		1000		mA
		R_{SNS}=25mΩ,		2000		mA
		Standby Mode		4		μA
I_{TRIKL}	Trickle Charge Current	$V_{BAT}<V_{TRIKL}$,		100		mA
		V_{BAT}<2.3V		20		mA
V_{TRIKL}	Trickle Charge Threshold	R_{SNS}=50mΩ, V_{BAT}		8.4		V
V_{TRHYS}	Trickle Charge Hysteresis	R_{SNS}=50mΩ		100		mV
V_{STAT}	STAT Pin Output Low	I_{STAT}=5mA			0.5	V
I_{STAT}	STAT Pin Weak Pull-Down	V_{STAT}=5V			5	uA
ΔV_{REC}	Recharge Battery Threshold	V_{FLOAT}-V_{RECHRG}		150		mV
T_{LIM}	Junction Temperature in Constant			150		℃
I_{TERM}	C/10 Terminal Current	R_{SNS}=50mΩ		100		mA
VIN UVLO	Under Voltage Lockout of VIN	V_{IN} rising		4		V
		V_{IN} falling		3.8		V
VIN OVP	VIN over voltage protect	VIN rising		25		V
		hysteresis		1		
V_{NTC-H}	High Temperature Protection Threshold Voltage	Battery Temperature rising		30		$\%V_{LV5}$
V_{NTC-L}	Low Temperature Protection Threshold Voltage	Battery Temperature falling		70		$\%V_{LV5}$
F_{OSC}	Frequency			500		KHz

APPLICATION INFORMATION

HM5083 is a wide range VIN(4V-20V), 3A 3 cell asynchronous boost Li-Ion battery charger integrates 500KHz switching frequency and full protection functions. The charge current can be programmed up to Max 3A by using the external resistor for different portable applications and indicates the charger current information simultaneous. In constant current mode, the charge current is set by the external sense resistor RSNS and an internal 50mV reference;

IBAT=VRS/RSNS=50mV/RSNS

When the battery voltage approaches the programmed float voltage, the charge current will start to decrease. When the current drops to 10% of the full-scale charge current, an internal comparator turns off charging is terminated

Input Source Qualification

After REGN amplifier powers up, the HM5083 checks the current capability of the input source. The input source has to meet the VREG>1V to enable the chip

Battery Temperature Detection

The HM5083 continuously monitors temperature by measuring the voltage between the NTC and GND pins. A negative or a positive temperature coefficient thermistor (NTC, PTC) and an external voltage divider typically develop this voltage. The HM5083 compares this voltage against its internal VNTC-H and VNTC-L thresholds to determine if charging is allowed. The temperature sensing circuit is immune to any fluctuation in LV5, since both the external voltage divider and the internal thresholds (VNTC-H and VNTC-L) are referenced to LV5.

The resistor values of R1 and R2 are calculated by the following equations:

For NTC Thermistors:

K1 (VNTC-H) =30%,

K2 (VNTC-L) =70%.

Automatic Recharge

Once the charge cycle is terminated, the HM5083 continuously monitors the voltage on the BAT pin using a comparator with a 1.8ms filter time (tRECHARGE). A charge cycle restarts when the battery voltage falls below 12.47V (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations.

Where RTL is the low temperature resistance and RTH is the high temperature resistance of thermistor, as specified by the thermistor manufacturer. R1 or R2 can be omitted if only one temperature (low or high) setting is required. Applying a voltage between the VNTC-H and VNTC-L thresholds to pin NTC disables the temperature-sensing feature.

CV adjustment

Battery termination voltage default set to 12.6V. If other CV voltage wanted, the follow equations change the CV voltage.

If increase CV voltage, connect a resistor from FBADJ to GND. the increased voltage determined by:

$$\Delta V = \frac{2.1}{R_{trim}} * R_{divup}$$

If decrease CV voltage, connect a resistor from battery to FBADJ, the decreased voltage determined by

$$\Delta V = \frac{V_{CV} - 2.1}{R_{trim}} * R_{divdown}$$

Rdivup=156K

Rdivdown=156K/5 for 3cell

Shutdown

The HM5083 can be shut down by pulling the NTC pin to VIN. When the NTC pin is released, the internal timer is reset and a new charge cycle starts.

Inductor Selection

A operating frequency was chosen for the buck switcher in order to minimize the size of the inductor. However, take care to use inductors with low core loss at this frequency. To calculate the inductor ripple current:

Charge Status Indicator (STAT)

The charge status output has two different states: strong pull-down (~5mA) and high impedance. The strong pull-down state indicates that the HM5083 is in a charge cycle. Once the charge cycle has terminated, the pin state is determined by under voltage lockout conditions. High impedance indicates that the charge cycle complete.

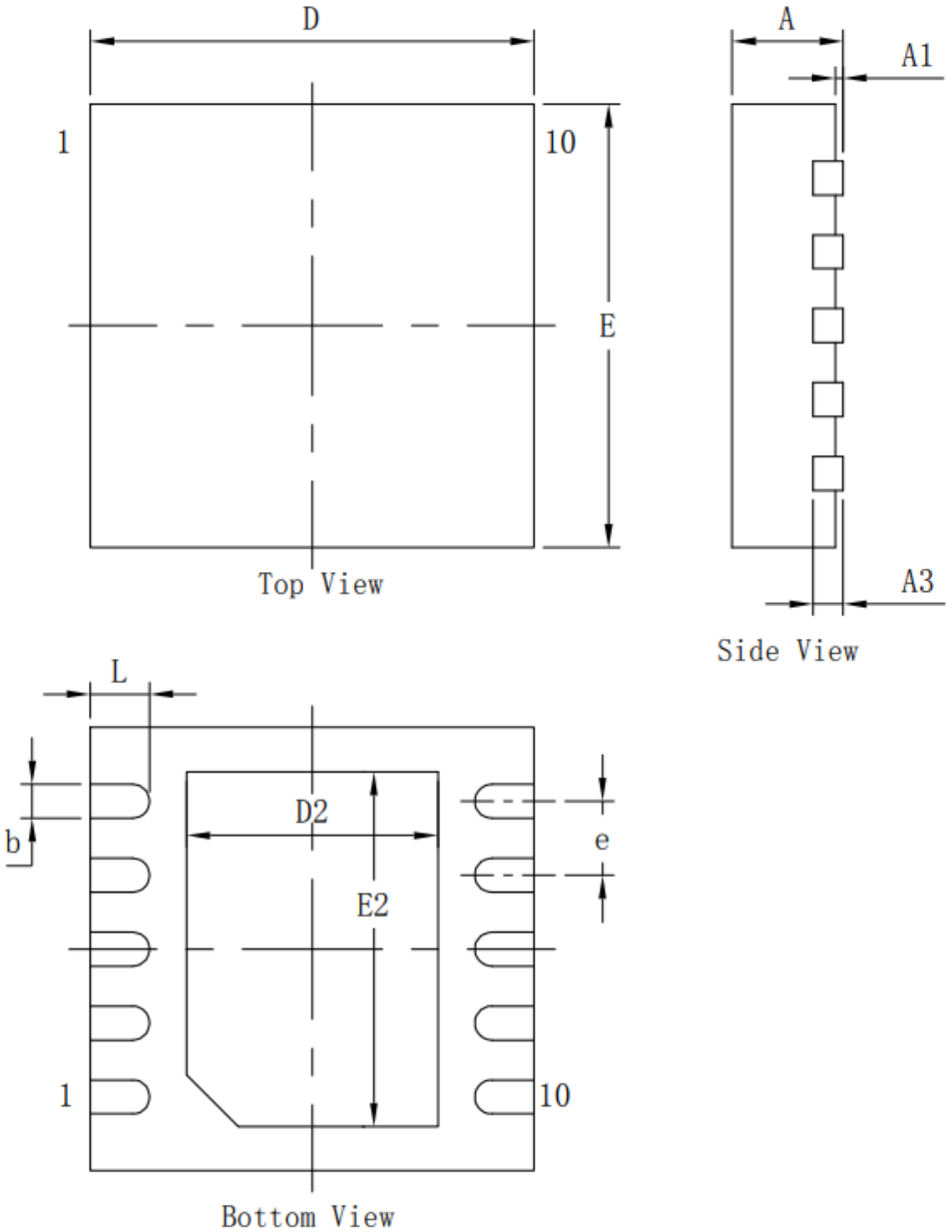
Layout Considerations

Switch rise and fall times are kept under 5ns for maximum efficiency. To minimize radiation, the SW pin and input bypass capacitor leads (between VIN and GND) should be kept as short as possible. A ground plane should be used under the switching circuitry to prevent inter plane coupling. The Exposed Pad must be connected to the ground plane for proper power dissipation. The other paths contain only DC and/or 500KHz tri-wave ripple current and are less critical. With the exception of the input and output filter capacitors (which should be connected to GND) all other components that return to ground.

PACKAGE INFORMATION

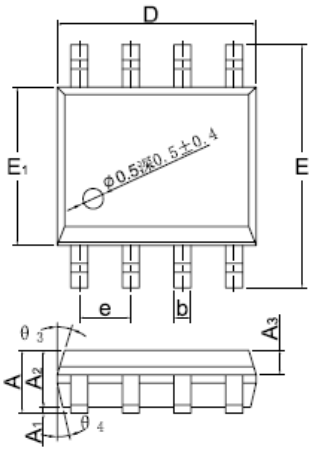
DFN10

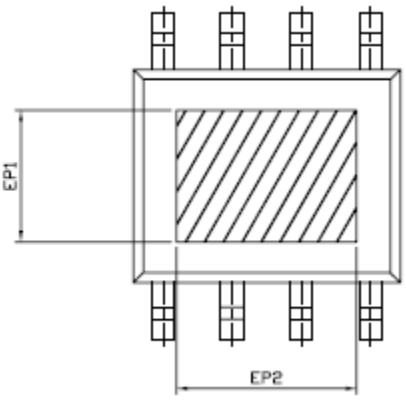
尺寸 / 标注	最小(mm)	标准(mm)	最大(mm)	尺寸 / 标注	最小(mm)	标准(mm)	最大(mm)
A	0.70	0.75	0.80	E	2.90	3.00	3.10
A1	-	-	0.05	D2	1.60	1.70	1.80
A3	0.203 REF			E2	2.30	2.40	2.50
b	0.18	0.23	0.28	e	0.50 TYP		
D	2.90	3.00	3.10	L	0.35	0.40	0.45

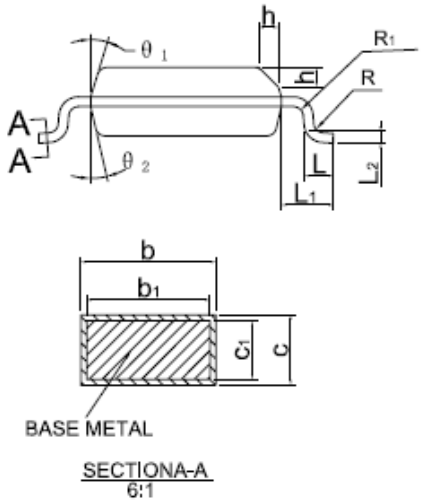


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ESOP8







SECTIONA-A
6:1

DIMENSIONS IN MUILLIMETERS

SYMBOL	MIN	NOM	MAX
A	1.35	1.55	1.75
A1	0.00	——	0.10
A2	1.25	1.40	1.65
A3	0.50	0.60	0.70
b	0.39	——	0.49
b1	0.28	——	0.48
c	0.10	——	0.25
c1	0.10	——	0.23
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27BSC		
L	0.45	——	1.00
L1	1.04REF		
L2	0.25BSC		
R	0.07	—	—
R1	0.07	—	—
h	0.3	0.4	0.5
	0°	—	8°
θ1	11°	17°	19°
θ2	11°	13°	15°
θ3	15°	17°	19°
θ4	11°	13°	15°
EP1	2.40	—	—
EP2	3.30	—	—