

**FILTERLESS CLASS-D MONO AUDIO AMPLIFIER**

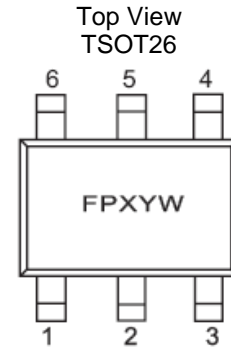
**Description**

The PAM8301 is a 1.5W Class-D mono audio amplifier. Its low THD+N feature offers high quality sound reproduction. The new filterless architecture allows the device to drive speaker directly instead of using low-pass output filters, therefore saving system cost and PCB area.

With the same number of external components, the efficiency of the PAM8301 is much better than that of Class-AB cousins. It can optimize battery life thus is ideal for portable applications.

The PAM8301 is available in TSOT26 package.

**Pin Assignments**



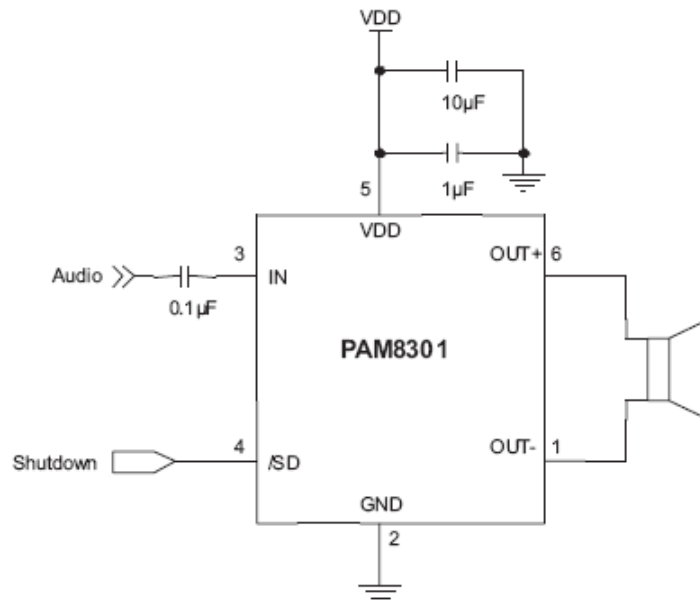
**Features**

- 1.5W Output at 10% THD with a 8Ω Load and 5V Power Supply
- Filterless, Low Quiescent Current and Low EMI
- High Efficiency up to 88%
- Superior Low Noise
- Short Circuit Protection
- Thermal Shutdown
- Few External Components to Save Space and Cost
- Tiny TSOT26 Package
- Pb-Free Package

**Applications**

- PMP/MP4
- GPS
- Portable Speakers
- Walkie Talkie
- Handsfree Phones/Speaker Phones
- Cellular Phones

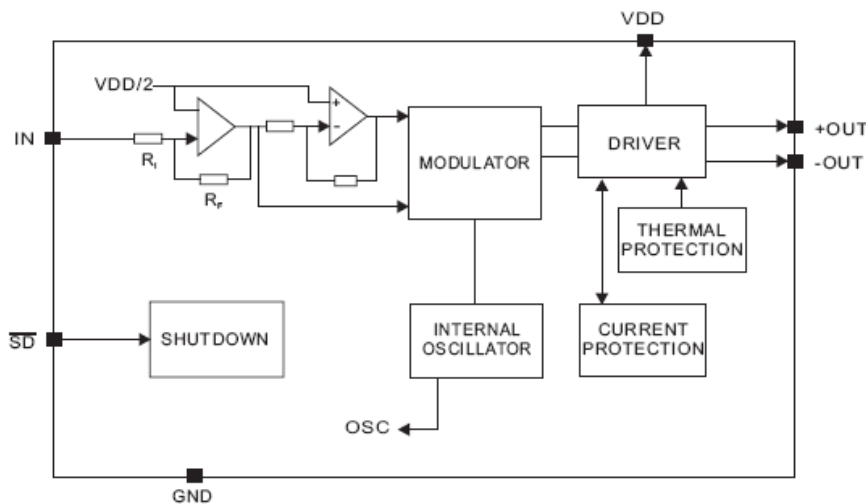
**Typical Applications Circuit**



## Pin Descriptions

Pin Number	Pin Name	Function
1	OUT-	Negative Output
2	GND	Ground
3	IN	Input
4	$\overline{SD}$	Shutdown, Active Low
5	VDD	Power Supply
6	OUT+	Positive Output

## Functional Block Diagram



## Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

These are stress ratings only and functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability. All voltages are with respect to ground.

Parameter	Rating	Unit
Supply Voltage at No Input Signal	6.0	V
Input Voltage Range	-0.3 to V <sub>DD</sub> + 0.3	
Maximum Junction Temperature	150	°C
Storage Temperature	-65 to +150	
Soldering Temperature	300, 5sec	

## Recommended Operating Conditions (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Parameter	Rating	Unit
Supply Voltage Range	2.5 to 5.5	V
Operation Temperature Range	-40 to +85	°C
Junction Temperature Range	-40 to +125	°C

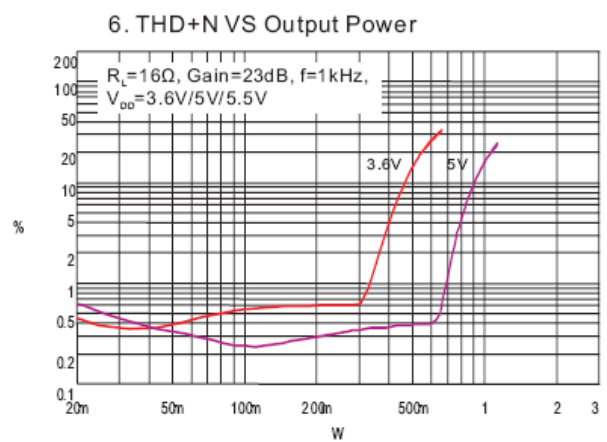
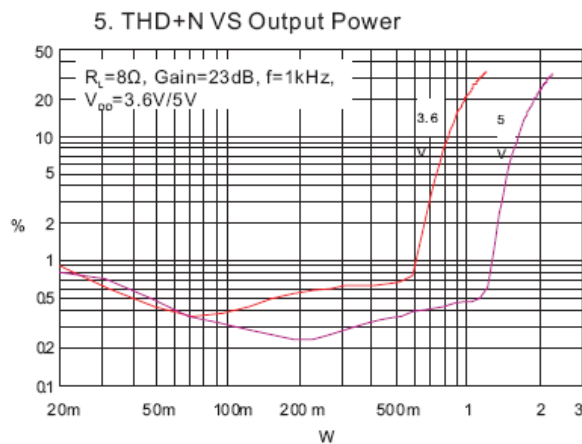
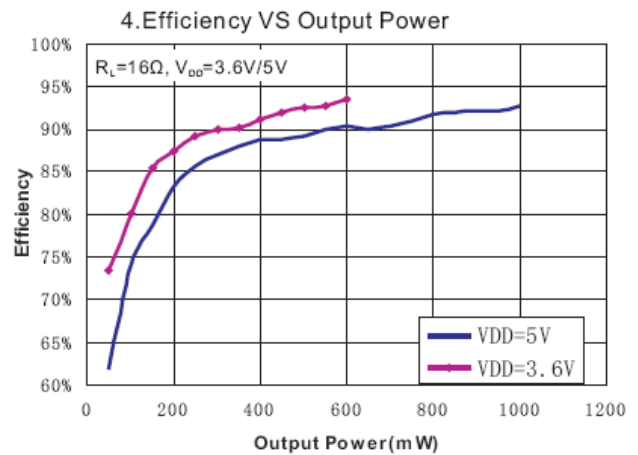
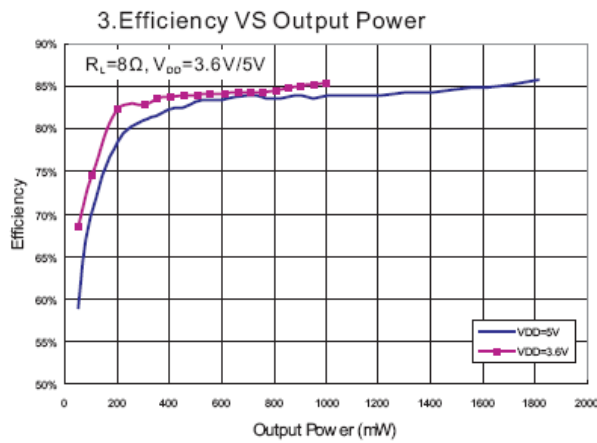
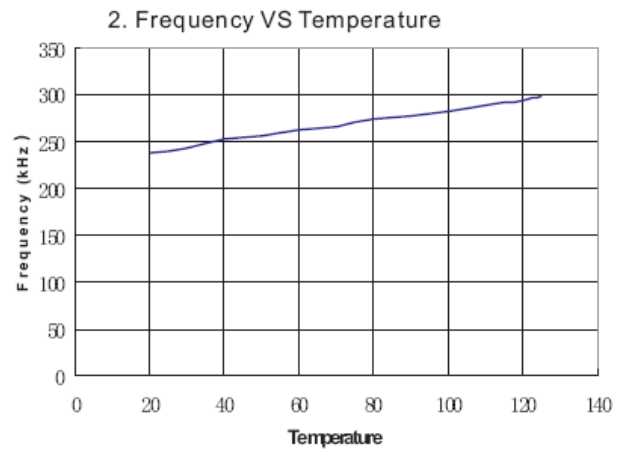
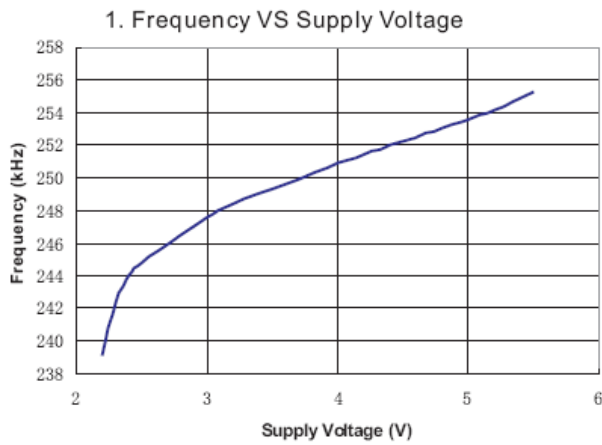
## Thermal Information

Parameter	Package	Symbol	Max	Unit
Thermal Resistance (Junction to Ambient)	TSOT26	$\theta_{JA}$	250	°C/W
Thermal Resistance (Junction to Case)	TSOT26	$\theta_{JC}$	130	

## Electrical Characteristics (@ $T_A = +25^\circ\text{C}$ , $V_{DD} = 5\text{V}$ , Gain = 24dB, $R_L = 8\Omega$ , unless otherwise specified.)

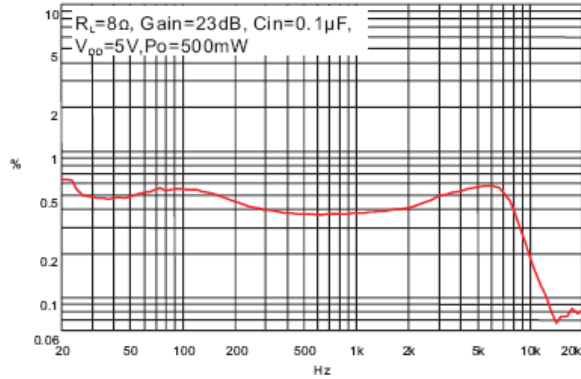
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$V_{DD}$	Supply Voltage Range		2.5		5.5	V
$I_Q$	Quiescent Current	No Load		4	8	mA
$I_{SHDN}$	Shutdown Current	$V_{SHDN} = 0\text{V}$			1	$\mu\text{A}$
$V_{SH}$	SHDN Input High		1.2			V
$V_{SL}$	SHDN Input Low				0.4	
$R_{DS(ON)}$	Drain-Source On-State Resistance	$I_{DS} = 100\text{mA}$		0.45		$\Omega$
				0.20		
$P_O$	Output Power	$f = 1\text{kHz}$		1.2		W
				1.5		
THD+N	Total Harmonic Distortion Plus Noise	$R_L = 8\Omega$ , $P_O = 200\text{mW}$		0.2		%
		$R_L = 8\Omega$ , $P_O = 0.5\text{W}$		0.3		
PSRR	AC Power Supply Ripple Rejection	No Inputs, $f = 1\text{kHz}$ , $V_{PP} = 200\text{mV}$	45	50		dB
$G_V$	Gain			24		dB
$V_N$	Output Noise	No A-Weighting		180		$\mu\text{V}$
		A-Weighting		120		
$f_{OSC}$	Oscillator Frequency		200	250	300	kHz
$\eta$	Peak Efficiency	$f = 1\text{kHz}$	85	88		%
SNR	Signal to Noise Ratio	$f = 20$ to $20\text{kHz}$		78		dB
OTP	Over Temperature Protection			135		°C
OTH	Over Temperature Hysterisis			30		°C

**Typical Performance Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

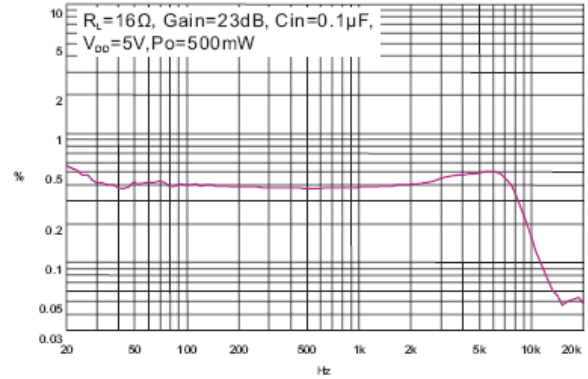


**Typical Performance Characteristics** (cont.) (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

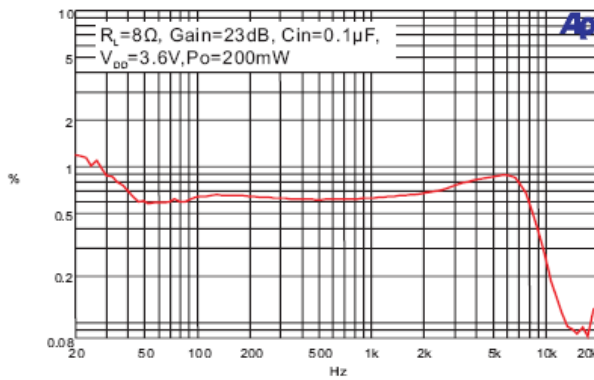
7. THD+N VS Frequency



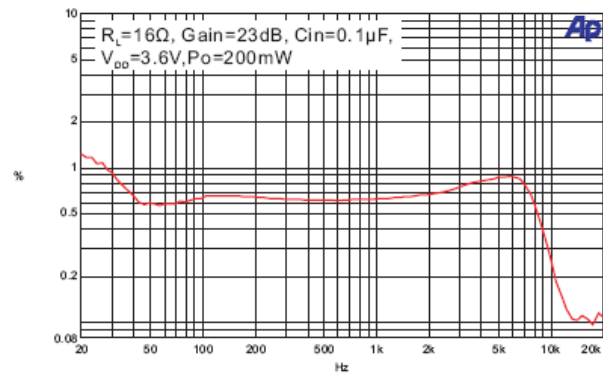
8. THD+N VS Frequency



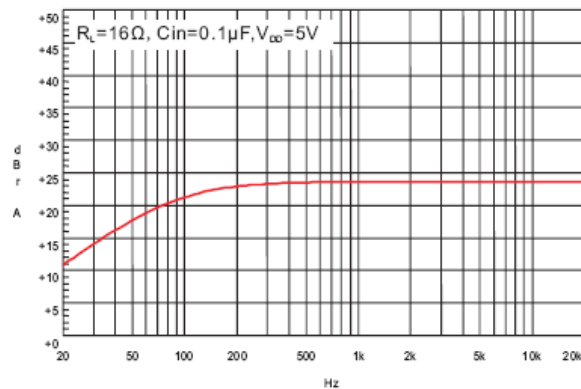
9. THD+N VS Frequency



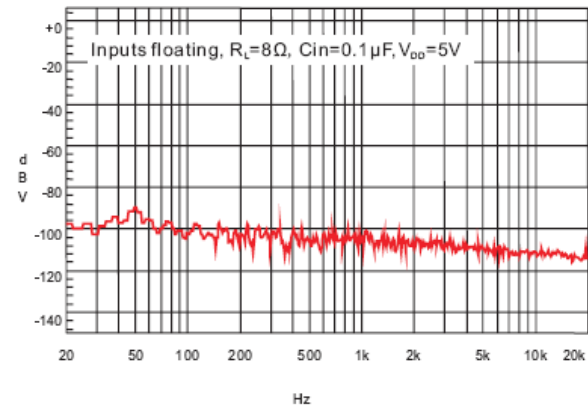
10. THD+N VS Frequency



11. Frequency Response

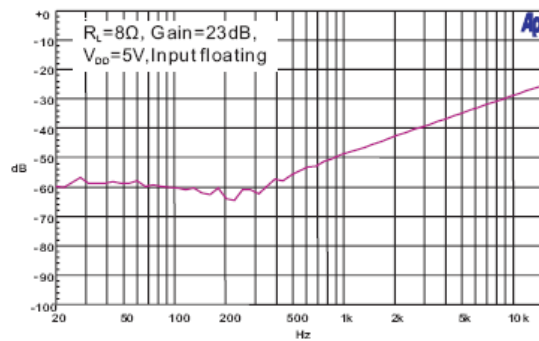


12. Noise Floor

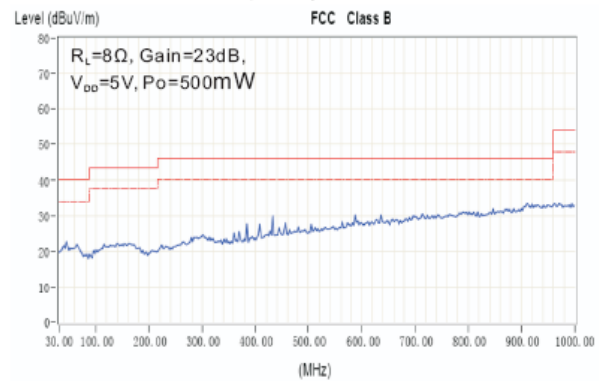


**Typical Performance Characteristics** (cont.) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

13. PSRR

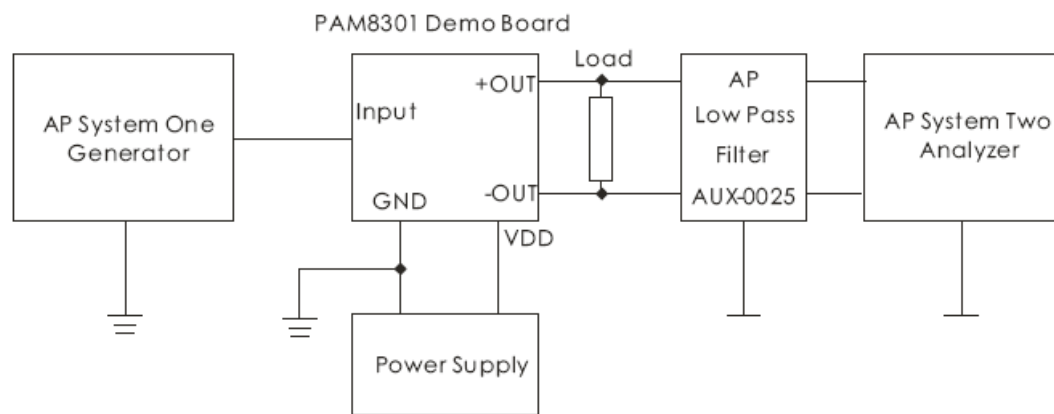


14. EMI vs Frequency



**Application Information**

**Test Setup for Performance Testing**



- Notes:
1. The AP AUX-0025 low pass filter is necessary for every class-D amplifier measurement with AP analyzer.
  2. Two 22μH inductors are used in series with load resistor to emulate the small speaker for efficiency measurement.

## Application Information (cont.)

### Maximum Gain

As shown in block diagram (Page 2), the PAM8301 has two internal amplifier stages. The first stage's gain is externally configurable, while the second stage's is internally fixed. The closed-loop gain of the first stage is set by selecting the ratio of  $R_F$  to  $R_I$  while the second stage's gain is fixed at  $2x$ . The output of amplifier one serves as the input to amplifier two, thus the two amplifiers produce signals identical in magnitude, but different in phase by  $+180^\circ$ . Consequently, the differential gain for the IC is

$$A_{VD} = 20 \cdot \log [2 \cdot (R_F/R_I)]$$

The PAM8301 sets maximum  $R_F = 80k\Omega$ , minimum  $R_I = 10k\Omega$ , so the maximum closed-gain is 24dB.

### Input Capacitors ( $C_I$ )

In typical application, an input capacitor,  $C_I$ , is required to allow the amplifier to bias input signals to a proper DC level for optimum operation. In this case,  $C_I$  and the minimum input impedance  $R_I$  (10k internal) form a high pass filter with a corner frequency determined by the following equation:

$$f_C = \frac{1}{2\pi R_I C_I}$$

It is important to choose the value of  $C_I$  as it directly affects low frequency performance of the circuit, for example, when an application requires a flat bass response as low as 100Hz. Equation is reconfigured as follows:

$$C_I = \frac{1}{2\pi R_I f_l}$$

As the input resistance is variable, for the  $C_I$  value of  $0.16\mu F$ , one should actually choose the  $C_I$  within the range of  $0.1\mu F$  to  $0.22\mu F$ . A further consideration for this capacitor is the leakage path from the input source through the input network ( $R_I$ ,  $R_F$ ,  $C_I$ ) to the load. This leakage current creates a DC offset voltage at the input to the amplifier that reduces useful headroom, especially in high gain application. For this reason, a low leakage tantalum or ceramic capacitor is the best choice. When a polarized capacitor is used, the positive side of the capacitor should face the amplifier input in most applications as the DC level is held at  $V_{DD}/2$ , which is likely higher than the source DC level. Please note that it is important to confirm the capacitor polarity in the application.

### Power Supply Decoupling ( $C_S$ )

The PAM8301 is a high-performance CMOS audio amplifier that requires adequate power supply decoupling to ensure the output THD and PSRR as low as possible. Power supply decoupling affects low frequency response. Optimum decoupling is achieved by using two capacitors of different types that target different types of noise on the power supply leads. For higher frequency transients, spikes, or digital hash on the line, a good low equivalent-series-resistance (ESR) ceramic capacitor, typically  $1.0\mu F$  is good, placing it as close as possible to the device  $V_{DD}$  terminal. For filtering lower-frequency noise signals, a capacitor of  $10\mu F$  or larger, closely located to near the audio power amplifier is recommended.

### Shutdown Operation

In order to reduce shutdown power consumption, the PAM8301 contains shutdown circuitry for turn off the amplifier. This shutdown feature turns the amplifier off when a logic low is applied on the SHDOWN pin. By switching the shutdown pin over to GND, the PAM8301 supply current draw will be minimized in idle mode.

**For the best power on/off pop performance, the amplifier should be set in the shutdown mode prior to power on/off operation.**

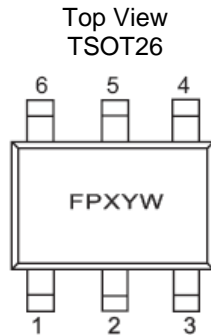
### Under Voltage Lock-Out (UVLO)

The PAM8301 incorporates circuitry to detect low on or off voltage. When the supply voltage drops to 2.1V or below, the PAM8301 goes into a state of shutdown, and the device comes out of its shutdown state and starts to normal operation by reset the power supply or  $\overline{SD}$  pin.





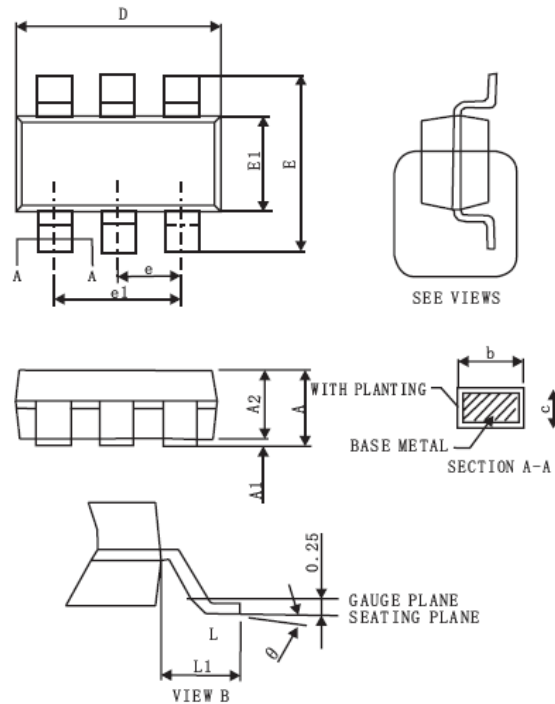
**Marking Information**



FP: Product Code of PAM8301  
X: Internal Code  
Y: Year  
W: Week

**Package Outline Dimensions** (All dimensions in mm.)

TSOT26



Symbol	A	A1	A2	b	c	D	E
Spec	1.20±0.25	0.10±0.05	1.10±0.2	0.40±0.1	0.15±0.07	2.90±0.1	2.80±0.2
Symbol	E1	e	e1	L	L1	θ	
Spec	1.60±0.1	0.95BSC	1.90BSC	0.55±0.25	0.60REF	4°±4°	

Unit: Millimeter

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