

COMPLIANT

HALOGEN

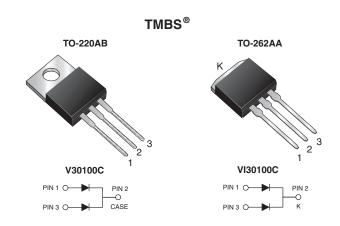
FREE



Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.455 \text{ V}$ at $I_F = 5 \text{ A}$



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 15 A				
V _{RRM}	100 V				
I _{FSM}	160 A				
V _F at I _F = 15 A	0.63 V				
T _J max.	150 °C				

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses

• High efficiency operation

Low thermal resistance

• Solder dip 275 °C max. 10 s, per JESD 22-B106

- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and

commercial grade

Base P/NHM3 - halogen-free, RoHS compliant, and

AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	V30100C	VI30100C	UNIT	
Maximum repetitive peak reverse voltage		V_{RRM}	100		V	
Maximum average forward rectified current (fig. 1)	per device		30 15		А	
	per diode	I _{F(AV)}				
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	160		А	
Voltage rate of change (rated V _R)		dV/dt	10	000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	- 40 to	+ 150	°C	

V30100C, VI30100C

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	$T_A = 25 ^{\circ}\text{C}$ $V_F^{(1)}$ $T_A = 125 ^{\circ}\text{C}$	V _F ⁽¹⁾	0.516	-	V	
	I _F = 7.5 A			0.576	-		
	I _F = 15 A			0.734	0.80		
	I _F = 5 A			0.455	-		
	I _F = 7.5 A			0.522	-		
	I _F = 15 A		0.627	0.68			
Reverse current per diode	V _R = 70 V	T _A = 25 °C	I _R ⁽²⁾	7.2	-	μΑ	
		T _A = 125 °C		8.0	-	mA	
	V _R = 100 V	T _A = 25 °C		65	500	μΑ	
		T _A = 125 °C		20	35	mA	

Notes

 $^{(1)}$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	V30100C	VI30100C	UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	2.5		°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	V30100C-M3/4W	1.88	4W	50/tube	Tube	
TO-262AA	VI30100C-M3/4W	1.45	4W	50/tube	Tube	
TO-220AB	V30100CHM3/4W (1)	1.88	4W	50/tube	Tube	
TO-262AA	VI30100CHM3/4W (1)	1.45	4W	50/tube	Tube	

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

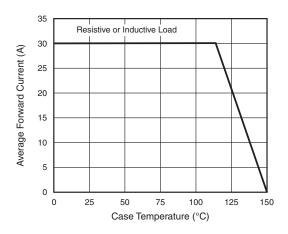


Fig. 1 - Forward Current Derating Curve

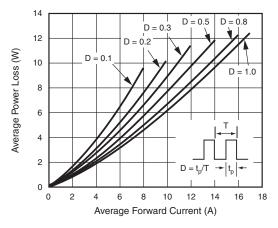


Fig. 2 - Forward Power Loss Characteristics Per Diode

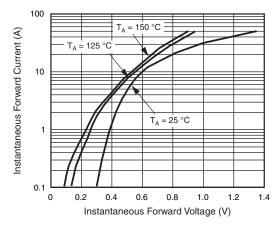


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

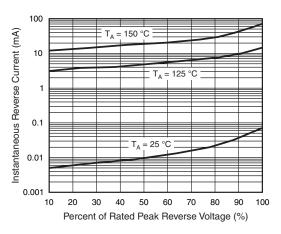


Fig. 4 - Typical Reverse Characteristics Per Diode

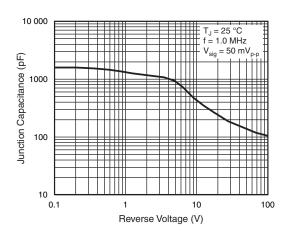


Fig. 5 - Typical Junction Capacitance

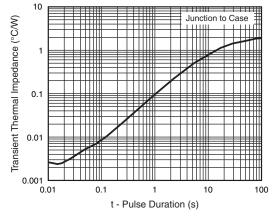


Fig. 6 - Typical Transient Thermal Impedance Per Diode

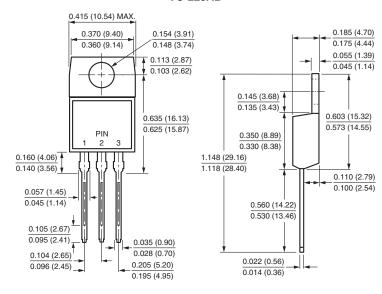
V30100C, VI30100C

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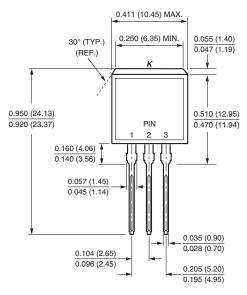


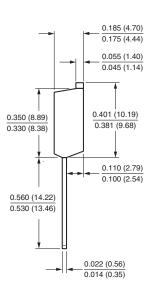
PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-220AB



TO-262AA









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