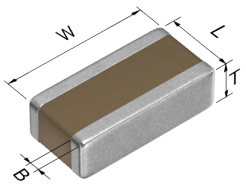
Creation Date : March 22, 2019 (GMT)

Multilayer Ceramic Chip Capacitors

C0510X5R1A474M030BC

RoHS | REACH SVHC-Free | Halogen Free | Lead Free

TDK item description	C0510X5R1A474MT****
Applications	Commercial Grade
Feature	Low ESL Reverse Geometry
Series	C0510 [EIA 0204]
Status	Production



Size	
Length(L)	0.52mm ±0.05mm
Width(W)	1.00mm ±0.05mm
Thickness(T)	0.30mm ±0.05mm
Terminal Width(B)	0.10mm Min.
Recommended Land Pattern (PA)	0.20mm Nom.
Recommended Land Pattern (PB)	0.20mm Nom.
Recommended Land Pattern (PC)	1.00mm Nom.

Electrical Characteristics	
Capacitance	470nF ±20%
Rated Voltage	10VDC
Temperature Characteristic	X5R(±15%)
Dissipation Factor (Max.)	10%
Insulation Resistance (Min.)	212MΩ

Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	15000pcs

! Images are for reference only and show exemplary products.
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! All specifications are subject to change without notice.

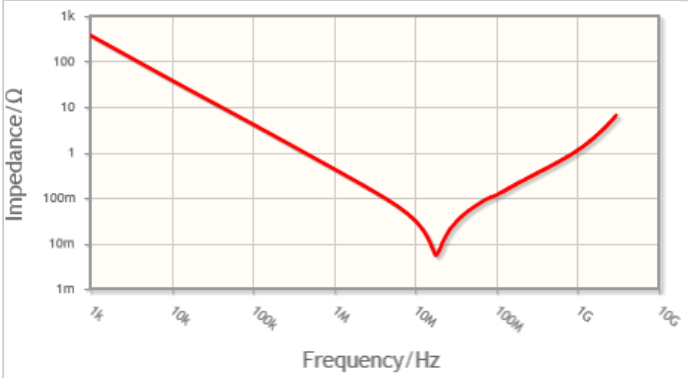
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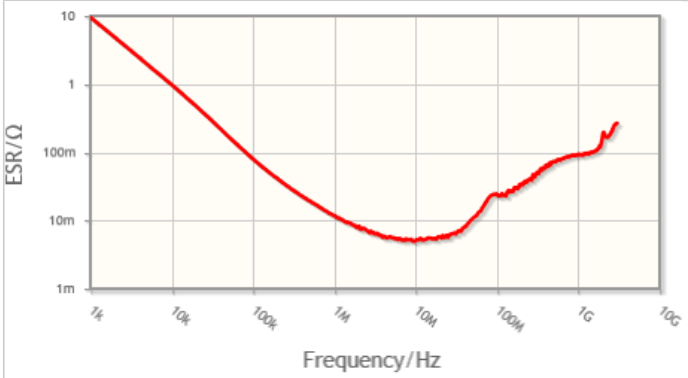
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



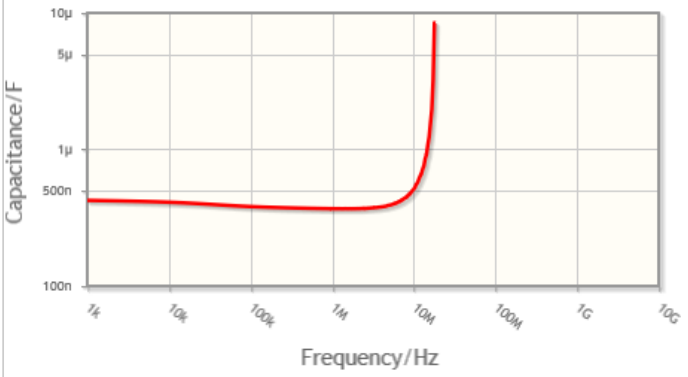
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ESR



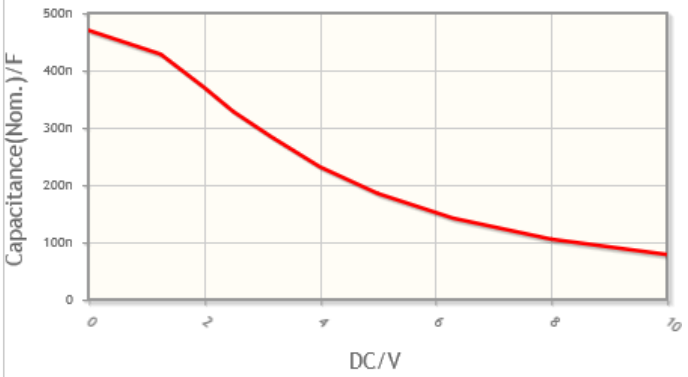
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Capacitance



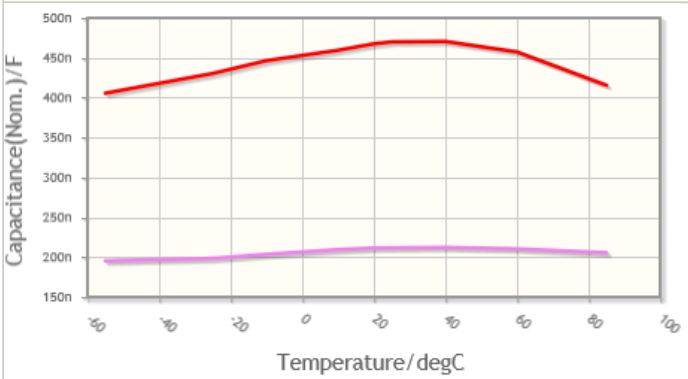
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DC Bias Characteristic



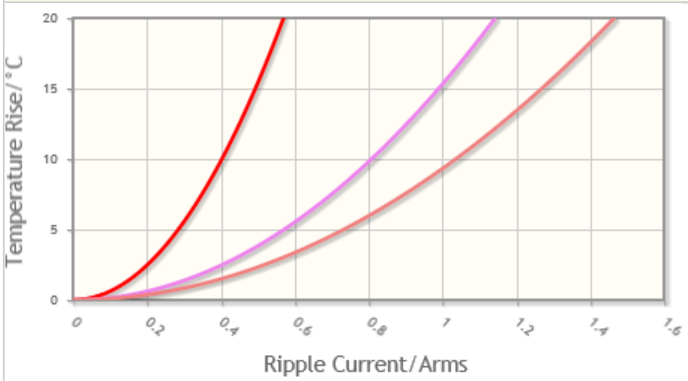
C0510X5R1A474M030BC

Temperature Characteristic



C0510X5R1A474M030BC(No Bias) C0510X5R1A474M030BC(DC Bias = 5)

Ripple Temperature Rising



C0510X5R1A474M030BC(100kHz) C0510X5R1A474M030BC(500kHz) C0510X5R1A474M030BC(1MHz)

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Associated Images

Land Pattern (Terminal Connection)



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