

## Evaluation Module - M28500/29503/ 26910

### Errata

**Product Affected:** M28500/29503/26910 Sonet Chipset Evaluation Module

The purpose of this document is to describe the following:

- SONET/SDH Functionality Change
- Requirement Changes
- DS3/E3 Framer on Board
- Part Revision Changes

### ***SONET/SDH Functionality Change***

Temporarily, SONET SDH functionality has been removed from the SONET Chipset Evaluation Module. Only electrical DS3 and E3 modes are operational (i.e. operating modes 1-6). The User Guide refers to the CX29610 device, which is not supplied with the board at this time, and therefore, all SONET/SDH-related modes and actions should be avoided.

Should the user attempt to use a SONET/SDH feature of the board, the results will be undefined and may hang the system.

### ***Requirement Changes***

Mindspeed Technologies no longer supplies an IBM PC compatible motherboard with the SONET Evaluation Module Board. The user must supply a PC that meets the following requirements:

- One empty PCI 2.1<sup>1</sup> compliant slot.
- Intel i8255x-based Ethernet card plus network cable.
- One floppy drive.

## DS3/E3 Framer on Board

The Evaluation Module contains the CX28344 Quad DS3/E3 framer, and accompanying LIU units. This device is not supported by the EVM software, but may be used for direct HDLC over DS3/E3 functionality. The device is accessible via the CX28500 EBUS using such driver functions as `MlCn850xCopyUINT32RangeArrayToEBUS()`.

The "byteEnabled" value<sup>2</sup> used to access the CX28344 is 0xB.

## Part Revision Changes

The Evaluation Module Board contains the following parts:

Part # - Rev	Quantity	Description	Data Sheet Reference
CX28500-12	1	1024-Channel HDLC Controller	28500-DSH-002-A
CX29503-13	1	DS3/E3 Broadband Access Multiplexer	29503-DSH-002-A
CX28344-11	1	Quad DS3/E3 Framer	28348-DSH-001-B
CX28333-18	1	Triple-port DS3/E3 LIU	28333-DSH-003-A
CX28332-18	2	Dual-port DS3/E3 LIU	28333-DSH-003-A

<sup>(1)</sup> PCI 2.1 compliant slots have both 5V and 3.3V power supplies. The EVM must have both.

<sup>(2)</sup> This value is used as chip-select in the EVM board.