ISDN transformers

U_{K0} interface, 2B1Q
RM 6, 14.5 mH, 0.8:0.8:1

Series/Type: **B78386P1114A005**

Date: October 2008

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Applications

- Use in NT and local central office
- Matched to the ICs
  Infineon PSB 8091, 8191, 24902, 24911;
  AMD AM2091

Features

- Complies with CCITT G.961
- Remote power feeding to NT
- RoHS-compatible

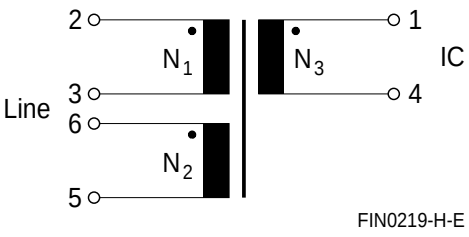
Marking

- Manufacturer, middle block of ordering code, date code

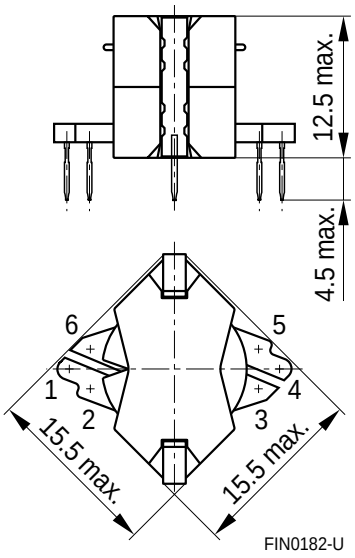
Delivery mode and packing unit

- Polyfoam tray
- Packing unit: 500 pcs.

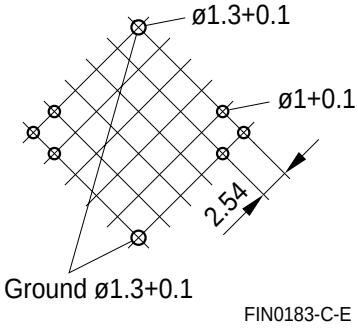
Pinning



Dimensional drawing



Recommended hole arrangement
(view in mounting direction)



Dimensions in mm

Technical data and measuring conditions

Main inductance L (2-5)	10 kHz, 50 mV, short 3-6
Stray inductance L_{stray} L (2-5)	10 kHz, 50 mV, short 3-6, 1-4
Interwinding capacitance C_i (1-2)	100 kHz, 50 mV, short 3-6
Resistance $R_{DC\ (Line)}$; $R_{DC\ (IC)}$	$R_{DC(Line)}$: short 3-6; $R_{DC(IC)}$: –
Test voltage V_{test}	50 Hz, 1 s; N_1, N_2 against N_3
DC current I_{DC}	With I_{DC} bias L drops < 5%
Transmission code	2B1Q
Operating temperature range	–25 °C … +85 °C
Weight	Approx. 8 g

Characteristics and ordering code

(electrical specifications at 25 °C)

Ordering code	B78386P1114A005	
Type/Core	RM 6	
$N_1 : N_2 : N_3$	0.8 : 0.8 : 1	
L	14.5 ±10%	mH
L_{stray} (typ.)	87	µH
C_i (typ.)	130	pF
$R_{DC\ (Line)}$ (typ.)	5.8	Ω
$R_{DC\ (IC)}$ (typ.)	4.2	Ω
V_{test}	500	V AC
I_{DC} (typ.)	50	mA

Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.
- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.
- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

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1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application.**

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2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified.** In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.

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