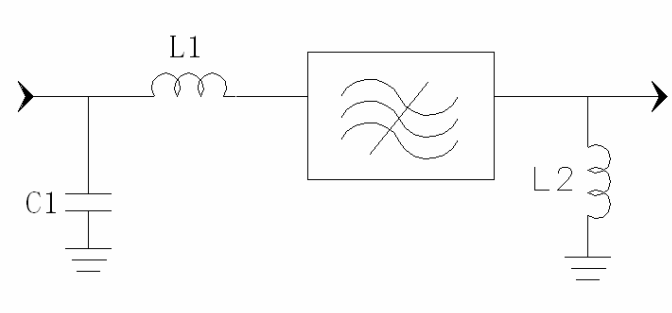
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	159.9	160	160.1
Insertion Loss	dB	-	29	32.5
1.5 dB Bandwidth	MHz	18.9	19.07	-
3 dB Bandwidth	MHz	19.2	19.34	-
40 dB Bandwidth	MHz	-	20.74	21
50 dB Bandwidth	MHz	-	20.92	-
Passband Variation(f0±9.375MHz)	dB	-	0.9	1.5
Absolute Delay	usec	-	2.7	-
Group Delay Variation(f0±9.375MHz)	nsec	-	30	100
Ultimate Rejection	dB	50	52	-
Material Temperature coefficient	KHz/°C	-2.88		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.0x12.8x4.7mm3)			

Notes:

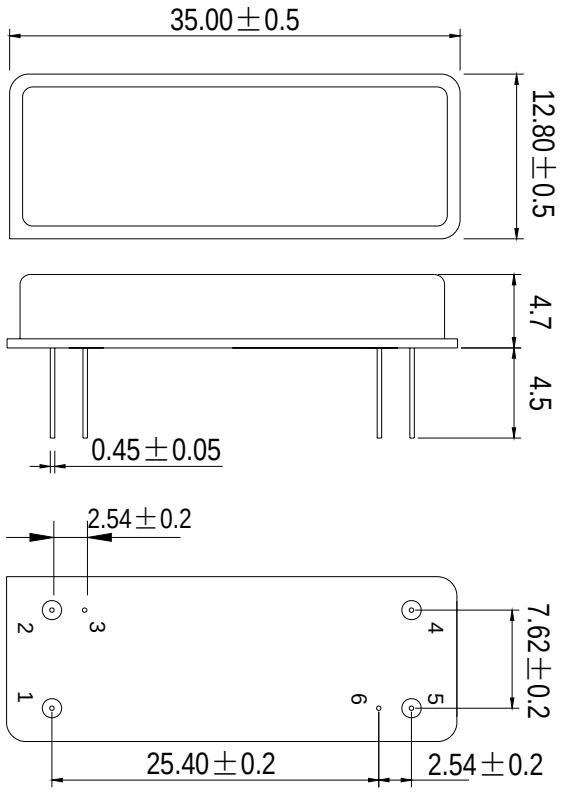
1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=39nH L2=22nH
C1=22pF
Source/Load Impedance=50 ohm
Notes - Component values may change depending on board layout.

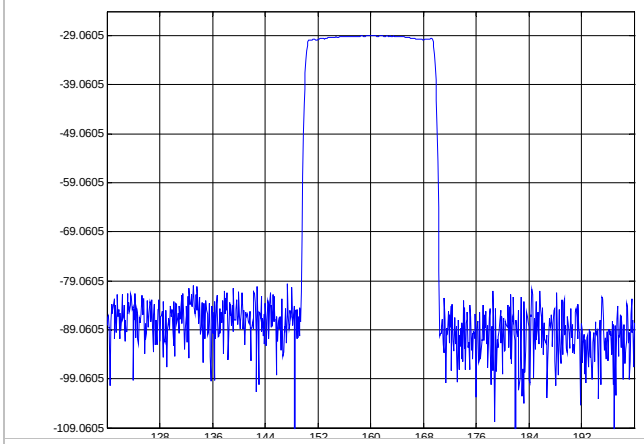
Package Dimension



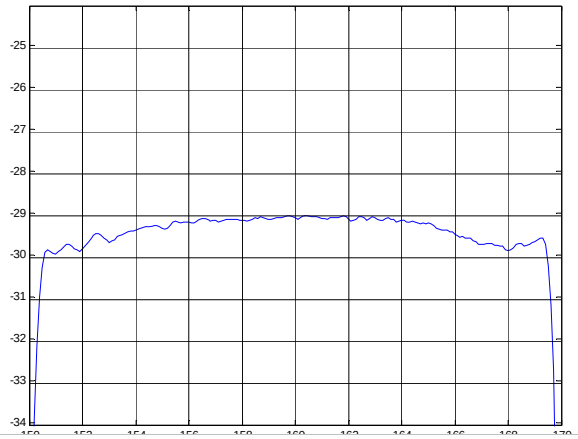
Input:1
Output:5

Typical Performance

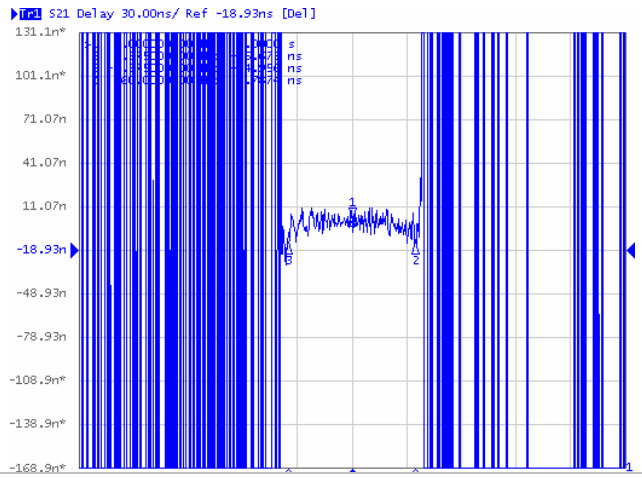
Frequency Respond



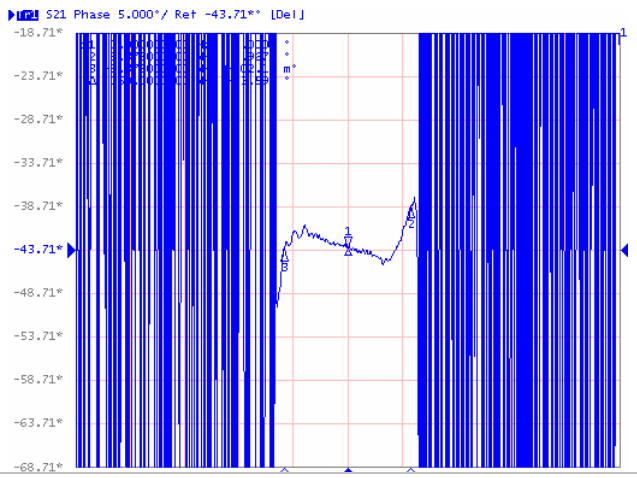
Passband Respond



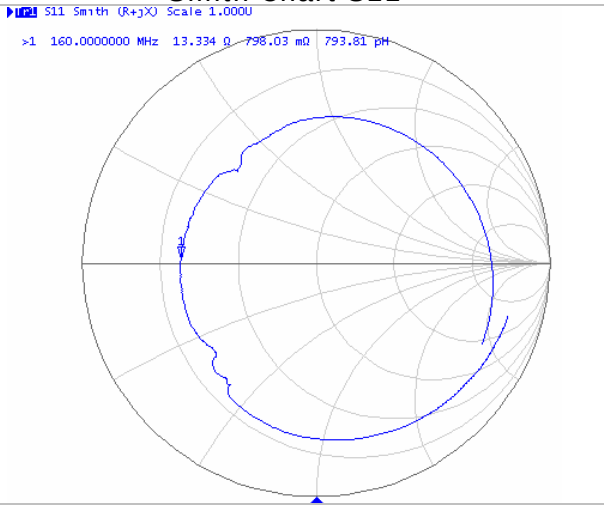
Group Delay Variation(f0±9.375MHz)



Phase Linearity(f0±9.375MHz)



Smith Chart S11



Smith Chart S22

