



General Description

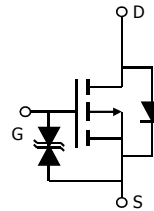
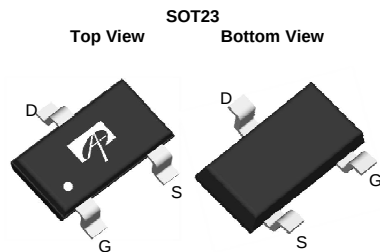
The AO3423 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch applications.

Product Summary

V_{DS}	-20V
I_D (at $V_{GS} = -10V$)	-2A
$R_{DS(ON)}$ (at $V_{GS} = -10V$)	< 92m Ω
$R_{DS(ON)}$ (at $V_{GS} = -4.5V$)	< 118m Ω
$R_{DS(ON)}$ (at $V_{GS} = -2.5V$)	< 166m Ω

Typical ESD protection

HBM Class 2



Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	-2	A
		-2	
Pulsed Drain Current ^C	I_{DM}	-17	
Power Dissipation ^B	P_D	1.4	W
		0.9	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$t \leq 10s$	$R_{\theta JA}$	65	90	$^\circ\text{C/W}$
Maximum Junction-to-Ambient ^{A D}	Steady-State		85	125	$^\circ\text{C/W}$
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	43	60	$^\circ\text{C/W}$

Electrical Characteristics (T_j=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =-250μA, V _{GS} =0V	-20			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-20V, V _{GS} =0V T _j =55°C			-1 -5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} = ±12V			±10	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-0.5	-0.85	-1.2	V
I _{D(ON)}	On state drain current	V _{GS} =-4.5V, V _{DS} =-5V	-17			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-2A T _j =125°C		76 99	92 119	mΩ
		V _{GS} =-4.5V, I _D =-2A		94	118	mΩ
		V _{GS} =-2.5V, I _D =-1A		128	166	mΩ
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-2A		6.8		S
V _{SD}	Diode Forward Voltage	I _S =-1A, V _{GS} =0V		-0.76	-1	V
I _S	Maximum Body-Diode Continuous Current				-1.5	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-10V, f=1MHz	250	325	400	pF
C _{oss}	Output Capacitance		40	63	85	pF
C _{rss}	Reverse Transfer Capacitance		22	37	52	pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		11.2	17	Ω
SWITCHING PARAMETERS						
Q _g	Total Gate Charge	V _{GS} =-4.5V, V _{DS} =-10V, I _D =-2A		3.2	4.5	nC
Q _{gs}	Gate Source Charge			0.6		nC
Q _{gd}	Gate Drain Charge			0.9		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =-10V, V _{DS} =-10V, R _L =5Ω, R _{GEN} =3Ω		11		ns
t _r	Turn-On Rise Time			5.5		ns
t _{D(off)}	Turn-Off DelayTime			22		ns
t _f	Turn-Off Fall Time			8		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =-2A, dI/dt=100A/μs		6.1		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-2A, dI/dt=100A/μs		1.4		nC

A. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C. The value in any given application depends on the user's specific board design.

B. The power dissipation P_D is based on T_{J(MAX)}=150° C, using ≤ 10s junction-to-ambient thermal resistance.

C. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150° C. Ratings are based on low frequency and duty cycles to keep initial T_J=25° C.

D. The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, assuming a maximum junction temperature of T_{J(MAX)}=150° C. The SOA curve provides a single pulse rating.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

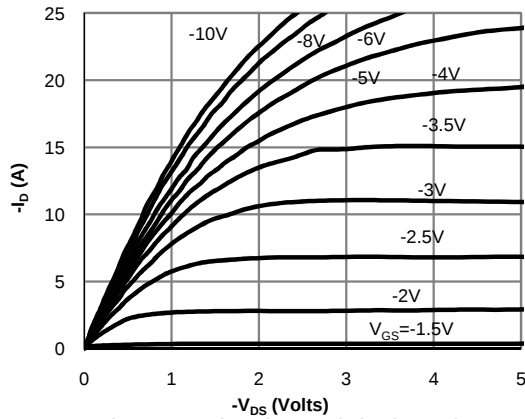


Fig 1: On-Region Characteristics (Note E)

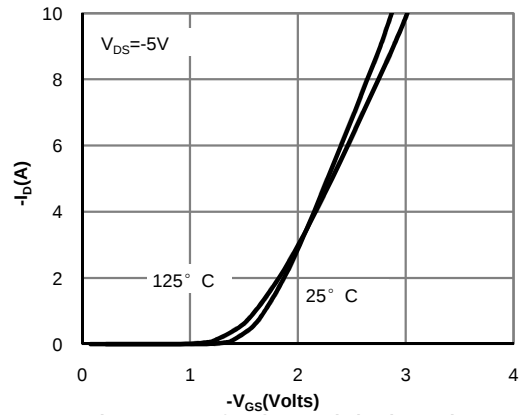


Figure 2: Transfer Characteristics (Note E)

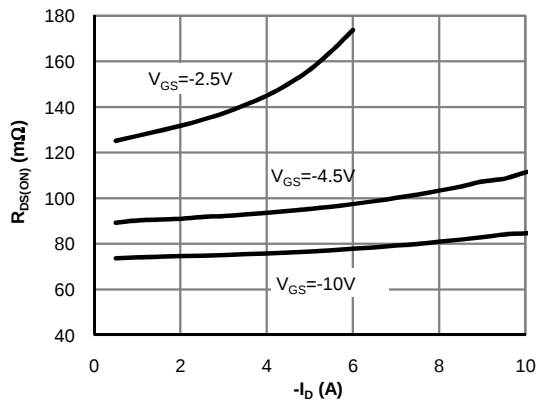


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

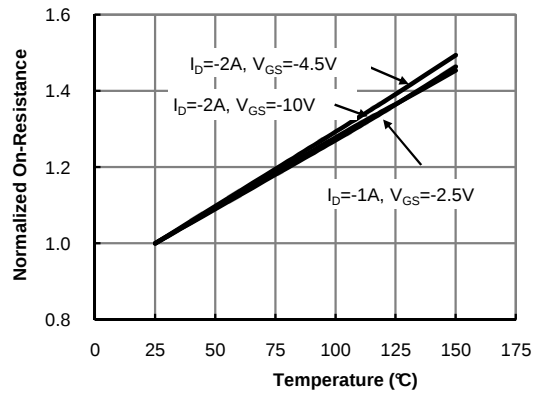


Figure 4: On-Resistance vs. Junction Temperature (Note E)

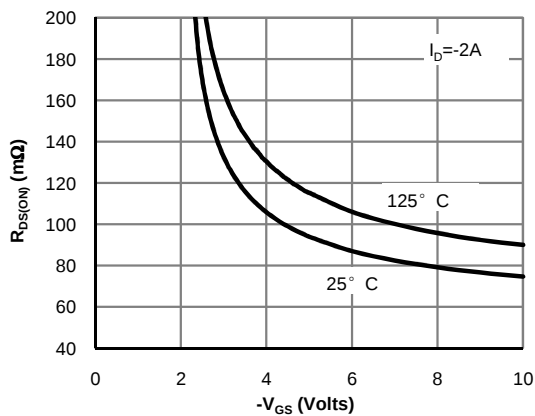


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

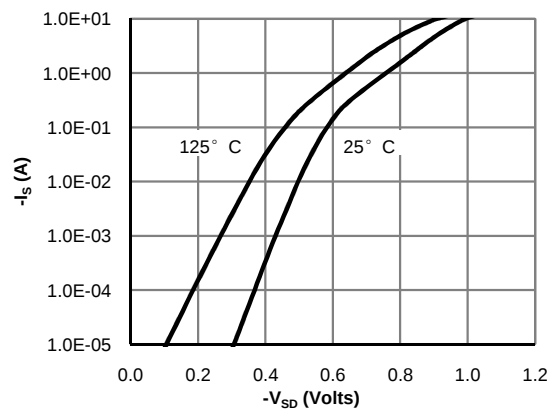


Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

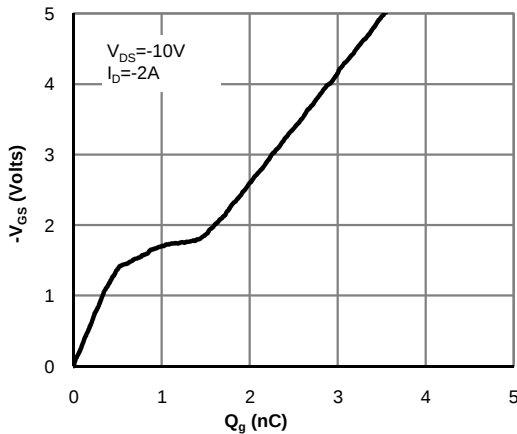


Figure 7: Gate-Charge Characteristics

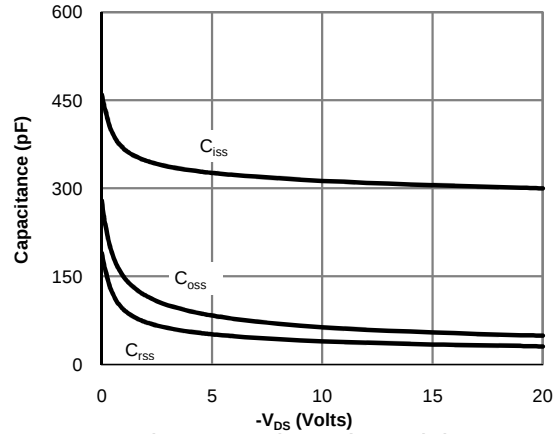


Figure 8: Capacitance Characteristics

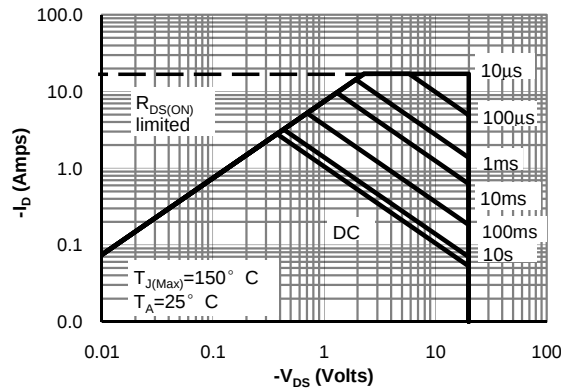


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

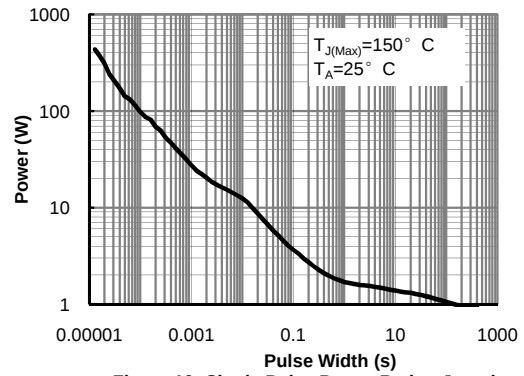


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

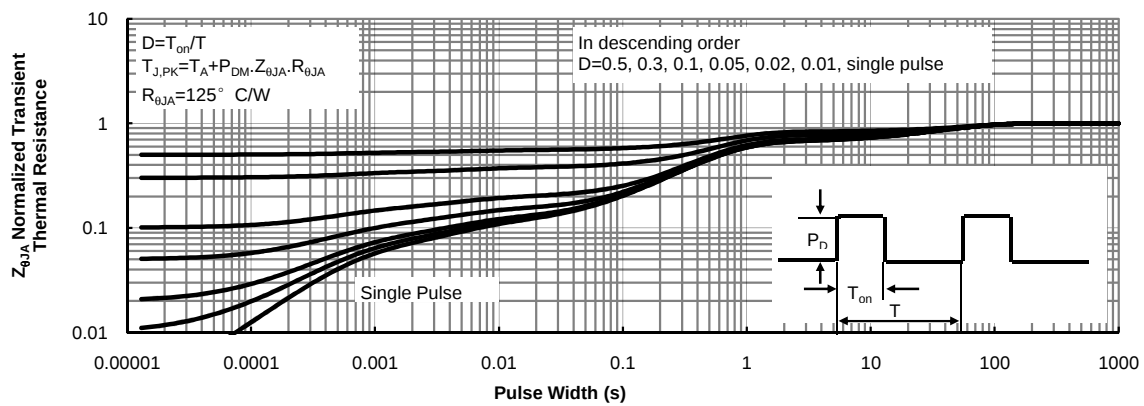
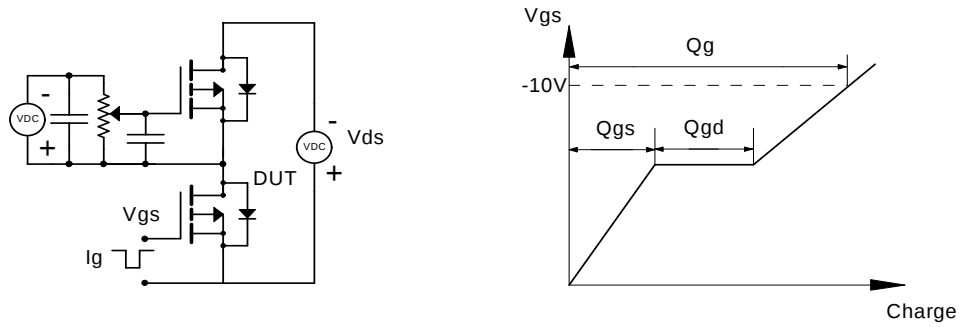
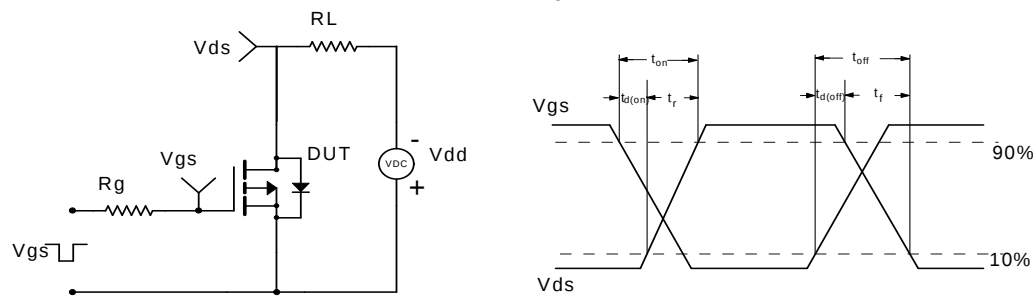


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

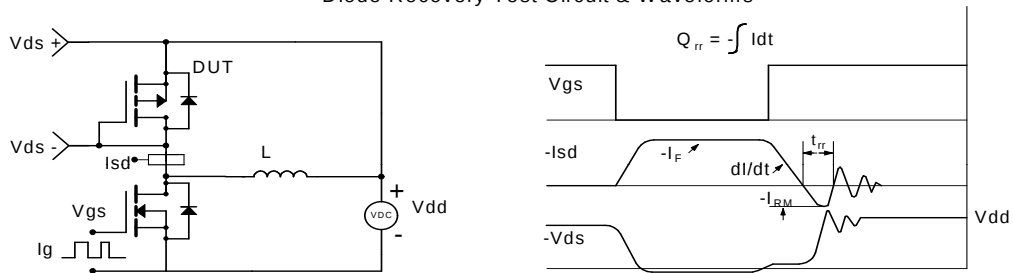
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



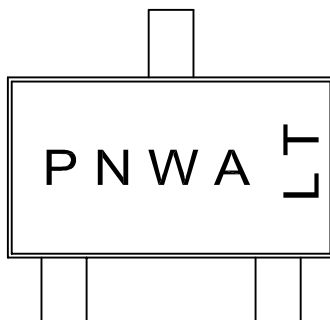
Diode Recovery Test Circuit & Waveforms





Document No.	PD-00464
Version	B
Title	AO3423 Marking Description

SOT-23 PACKAGE MARKING DESCRIPTION



Green product

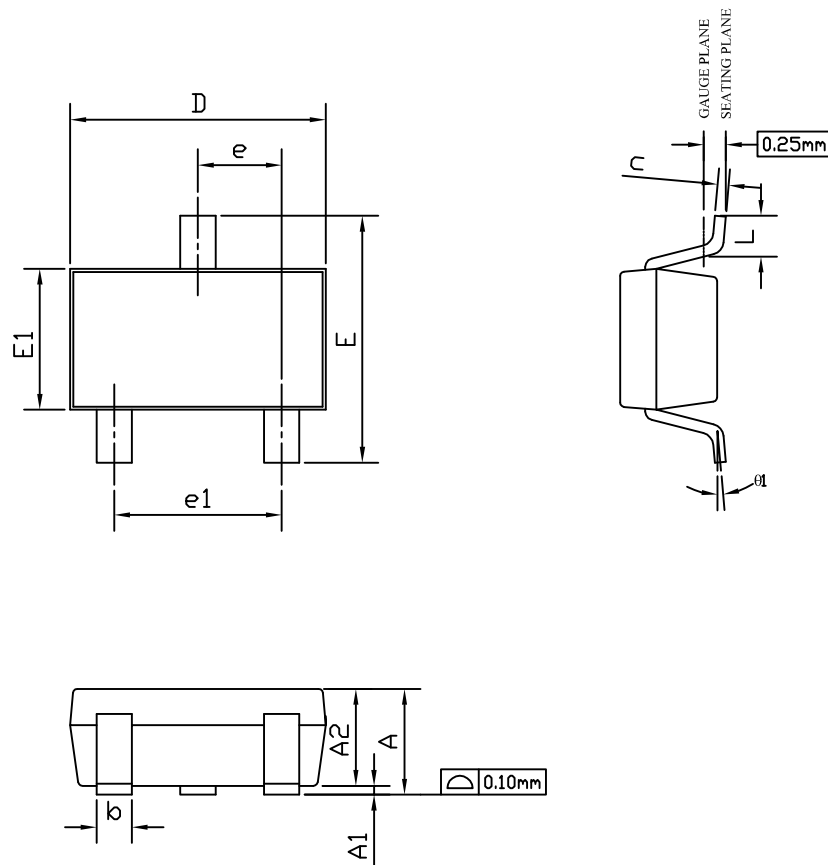
NOTE:

P - Package and product type
N - Last digital of product number
W - Year and week code
A - Assembly location code
L&T - Assembly lot code

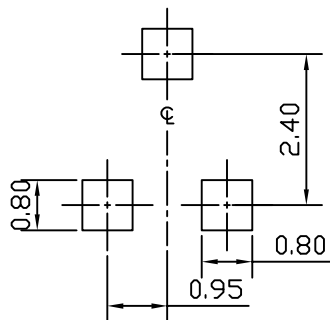
PART NO.	DESCRIPTION	CODE (PN)
AO3423	Green product	AS
AO3423L	Green product	AS



SOT23 PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	—	1.25	0.033	—	0.049
A1	0.00	—	0.13	0.000	—	0.005
A2	0.70	1.00	1.15	0.028	0.039	0.045
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.08	0.13	0.20	0.003	0.005	0.008
D	2.80	2.90	3.10	0.110	0.114	0.122
E	2.60	2.80	3.00	0.102	0.110	0.118
E1	1.40	1.60	1.80	0.055	0.063	0.071
e	0.95 BSC			0.037 BSC		
e1	1.90 BSC			0.075 BSC		
L	0.30	—	0.60	0.012	—	0.024
01	0°	5°	8°	0°	5°	8°

NOTE

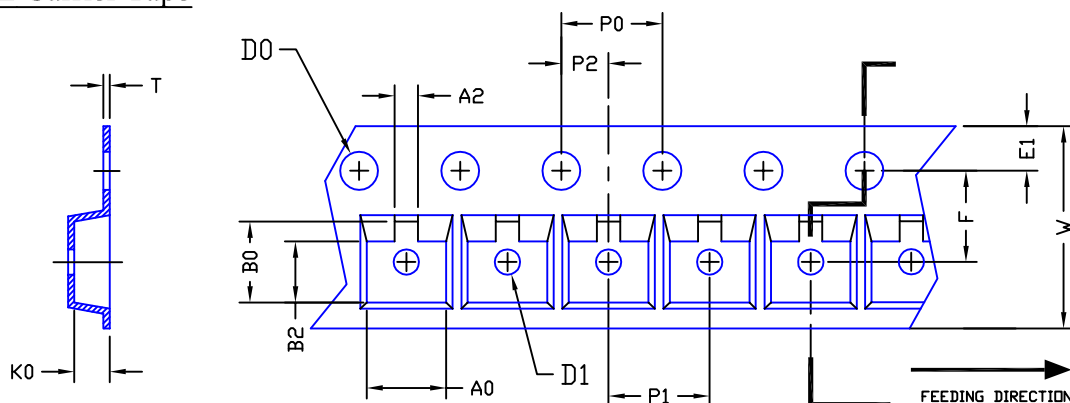
1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH OR GATE BURRS.
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 5 MILS EACH.
2. TOLERANCE ± 0.100 mm (4 mil) UNLESS OTHERWISE SPECIFIED.
3. DIMENSION L IS MEASURED IN GAUGE PLANE.
4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.
5. ALL DIMENSIONS ARE IN MILLIMETERS.



ALPHA & OMEGA
SEMICONDUCTOR, LTD.

SOT23-3L Tape and Reel Data

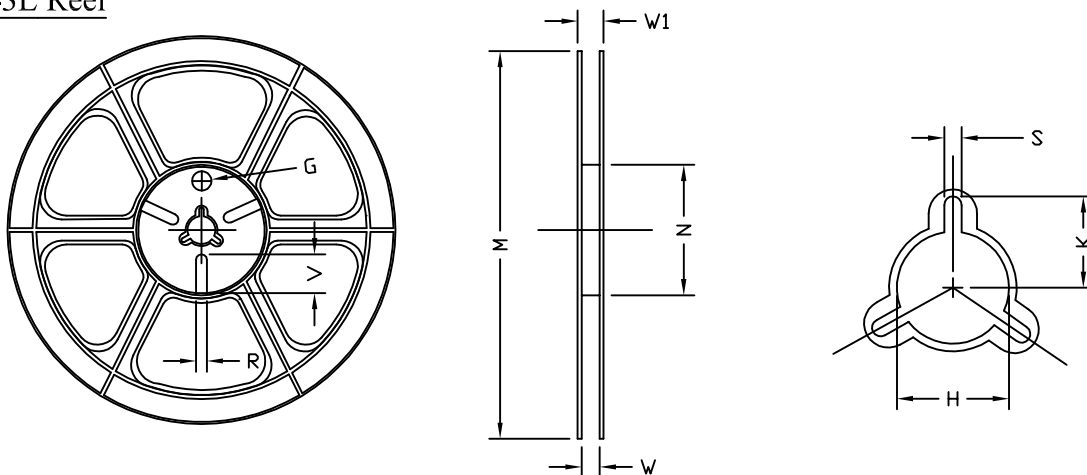
SOT23-3L Carrier Tape



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	W	E1	F	P0	P1	P2	T	A2	B2
SOT23-3L (8 mm)	3.05-3.40	3.00-3.38	1.20-1.47	1.55 ±0.05	1.00 ±0.25	8.00 ±0.30	1.75 ±0.10	3.50 ±0.05	4.00 ±0.10	4.00 ±0.10	2.00 ±0.05	0.18-0.25	0.84-1.24	2.29-2.69

SOT23-3L Reel



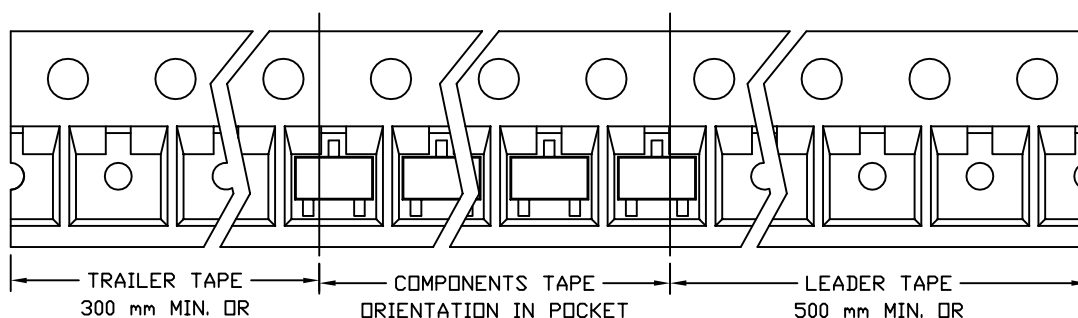
UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
8 mm	φ178	φ178.00 ±1.00	φ54.00 ±0.50	9.00 ±0.30	11.40 ±1.00	φ13.00 +0.50 -0.20	10.60	2.00 ±0.50	φ9.00	5.00	18.00

SOT23-3L Tape

Leader / Trailer
& Orientation

Unit Per Reel:
3000pcs





AOS Semiconductor Product Reliability Report

AO3423, rev C

Plastic Encapsulated Device

ALPHA & OMEGA Semiconductor, Inc

www.aosmd.com



This AOS product reliability report summarizes the qualification result for AO3423. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO3423 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be routine monitored for continuously improving the product quality.

Table of Contents:

- I. Product Description
- II. Package and Die information
- III. Reliability Stress Test Summary and Results
- IV. Reliability Evaluation

I. Product Description:

The AO3423 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation gate voltages as low as 2.5V. This device is suitable for use as a load switch applications.

Details refer to the datasheet.

II. Die / Package Information:

	AO3423
Process	Standard sub-micron 20V P-Channel MOSFET
Package Type	SOT23
Lead Frame	Bare Cu
Die Attach	Ag Epoxy
Bond	Au & Cu Wire
Mold Material	Epoxy resin with silica filler
Moisture Level	Up to Level 1

III. Reliability Stress Test Summary and Results

Test Item	Test Condition	Time Point	Total Sample Size	Number of Failures	Reference Standard
HTGB	Temp = 150°C , Vgs=100% of Vgsmax	168 / 500 / 1000 hours	924 pcs	0	JESD22-A108
HTRB	Temp = 150°C , Vds=80% of Vdsmax	168 / 500 / 1000 hours	924 pcs	0	JESD22-A108
MSL Precondition	168hr 85°C / 85%RH + 3 cycle reflow@260°C (MSL 1)	-	2772 pcs	0	JESD22-A113
HAST	130°C , 85%RH, 33.3 psia, Vds = 80% of Vdsmax	96 hours	924 pcs	0	JESD22-A110
H3TRB	85°C , 85%RH, Vds = 80% of Vdsmax	1000 hours	693 pcs	0	JESD22-A101
Autoclave	121°C , 29.7psia, RH=100%	96 hours	1848 pcs	0	JESD22-A102
Temperature Cycle	-65°C to 150°C , air to air,	250 / 500 cycles	1848 pcs	0	JESD22-A104
HTSL	Temp = 150°C	1000 hrs	924 pcs	0	JESD22-A103
Power Cycling	$\Delta T_j = 100^\circ\text{C}$	15000 cycles	462 pcs	0	AEC Q101

Note: The reliability data presents total of available generic data up to the published date.

IV. Reliability Evaluation

FIT rate (per billion): 3.43

MTTF = 33270 years

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size. Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

Failure Rate = $\text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)] = 3.43$

MTTF = $10^9 / \text{FIT} = 33270 \text{ years}$

Chi² = Chi Squared Distribution, determined by the number of failures and confidence interval

N = Total Number of units from burn-in tests

H = Duration of burn-in testing

Af = Acceleration Factor from Test to Use Conditions ($E_a = 0.7\text{eV}$ and $T_{\text{use}} = 55^\circ\text{C}$)

Acceleration Factor **[Af]** = $\text{Exp} [E_a / k (1/T_j u - 1/T_j s)]$

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	259	87	32	13	5.64	2.59	1

Tj s = Stressed junction temperature in degree (Kelvin), $K = C + 273.16$

Tj u = The use junction temperature in degree (Kelvin), $K = C + 273.16$

k = Boltzmann's constant, $8.617164 \times 10^{-5} \text{eV} / \text{K}$