

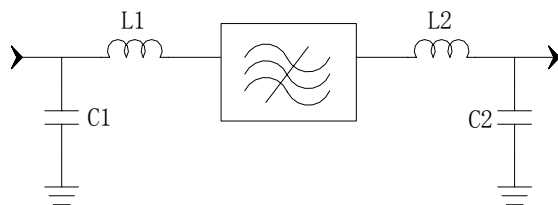
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	69.9	70	70.1
Insertion Loss	dB	-	26.2	27.5
3 dB Bandwidth	MHz	4.5	4.55	4.6
Selectivity	$f_0 \pm 4.55/2 + 0.4\text{MHz}$	dB	35	-
	$f_0 \pm 4.55/2 + 0.6\text{MHz}$	dB	45	-
	$f_0 \pm 4.55/2 + 1\text{MHz}$	dB	54	-
	$f_0 \pm 4.55/2 + 5\text{MHz}$	dB	59	-
Group Delay Variation($f_0 \pm 2.25\text{MHz}$)	nsec	-	90	-
Passband Variation	dB	-	0.9	1.3
Ultimate Rejection	dB	55	60	-
Absolute delay	usec	-	3.95	4
Substrate Material		112LT		
Ambient Temperature	°C	25		
Package Size		DIP3512 (35.2x12.7x5.2mm ³)		

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

Matching Configuration



$$L1 = L2 = 82 + 33\text{nH}$$

$$C1 = C2 = 82\text{pF}$$

Source / Load Impedance = 50 ohm

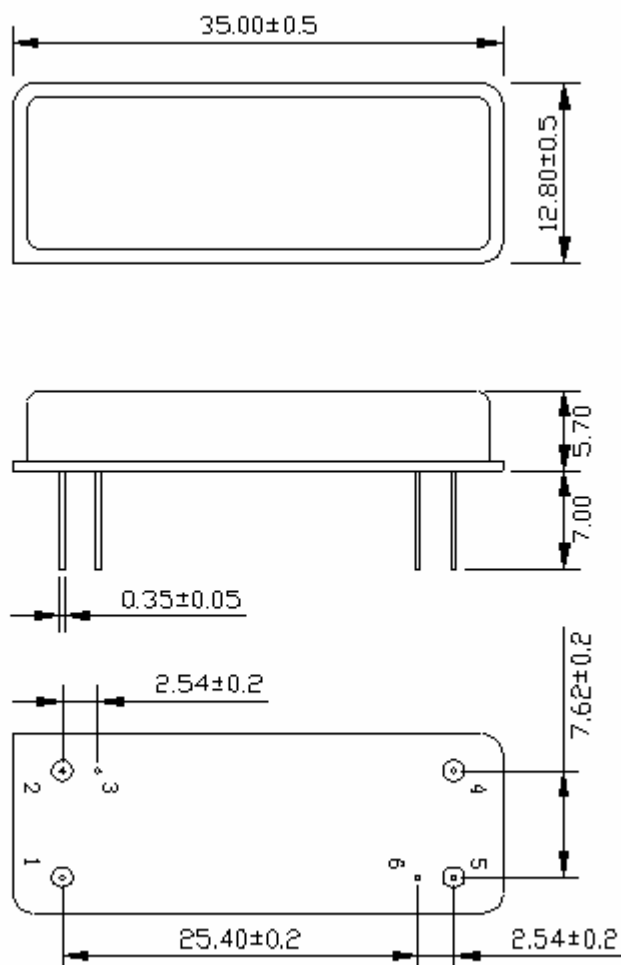
Notes - Component values may change depending
on board layout.



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Package Dimension

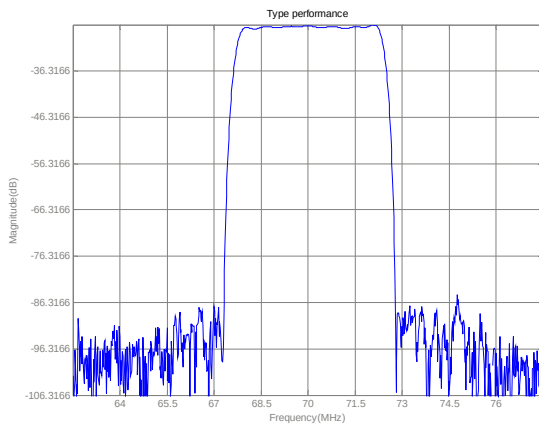


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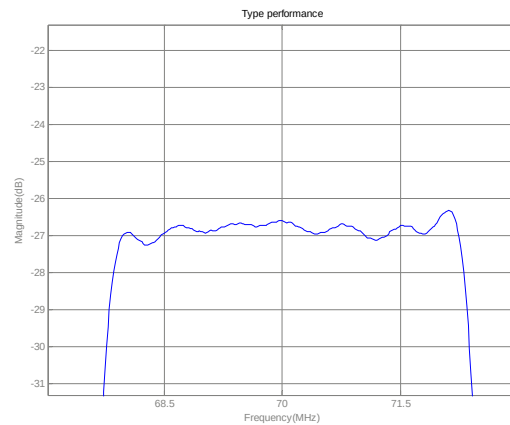
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Typical Performance

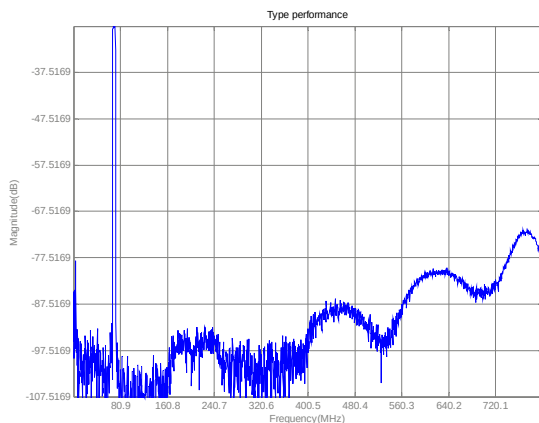
Frequency Respond



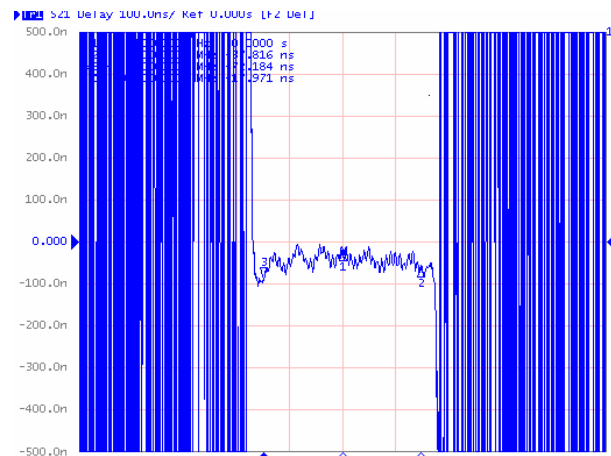
Passband Respond



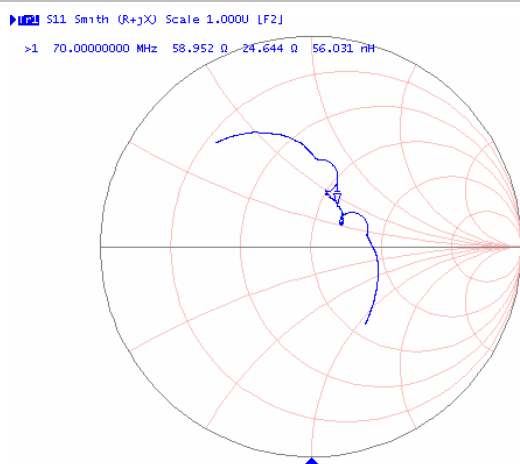
Wideband Respond



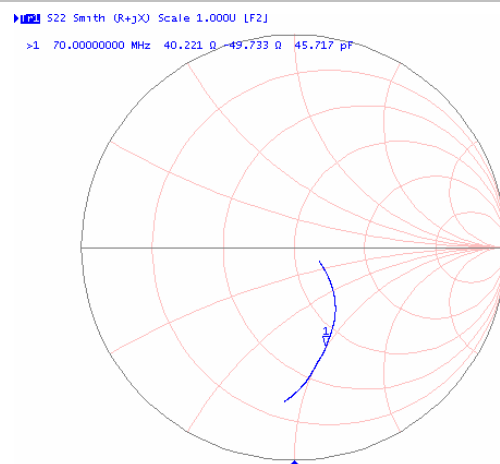
Group delay Variation($f_0 \pm 2.25$ MHz)



Simth Chart S11



Simth Chart S22



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