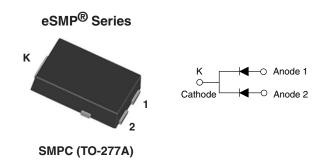
**Vishay Semiconductors** 

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# Hyperfast Rectifier, 2 x 5 A FRED Pt<sup>®</sup>



### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 5 A				
V <sub>R</sub>	100 V				
V <sub>F</sub> at I <sub>F</sub>	0.75 V				
t <sub>rr (typ.)</sub>	25 ns				
T <sub>J</sub> max.	175 °C				
Package	SMPC (TO-277A)				
Circuit configuration	Common cathode				

### **FEATURES**

- Hyperfast recovery time, reduced Q<sub>rr</sub>, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage		V <sub>RRM</sub>		100	V		
Average rectified forward current	per device	I <sub>F(AV)</sub>	T <sub>Sp</sub> = 155 °C	10	٨		
Average rectilied forward current	per diode			5			
Non-repetitive peak surge current	per device		T <sub>.1</sub> = 25 °C	130	A		
Non-repetitive peak surge current	per diode	IFSM	1j=25°C	70			
Operating junction and storage temp	eratures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	100	-	-		
Forward voltage, per diode	V <sub>F</sub>	I <sub>F</sub> = 5 A	-	0.92	0.98	V	
Forward voltage, per diode		I <sub>F</sub> = 5 A, T <sub>J</sub> = 150 °C	-	0.75	0.82		
Reverse leakage current, per diode	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	2		
neverse leakage current, per diode		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	4	80	μA	
Junction capacitance	CT	V <sub>R</sub> = 100 V	-	18	-	pF	

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COMPLIANT

HALOGEN

FREE



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ J}$	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		25	-		
	+	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	25		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	18	-	ns	
		T <sub>J</sub> = 125 °C		-	28	-		
Dook rooovery eurrent		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 5 A dI <sub>F</sub> /dt = 200 A/μs	-	2	-	А	
Peak recovery current I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$V_{\rm B} = 160 \text{ V}$	-	3.8	-			
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	18	-		
	T <sub>J</sub> = 125 °C		-	53	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to mount, per leg	R <sub>thJM</sub>		-	2.5	3.5	°C/W
Thermal resistance, junction to ambient, per leg	R <sub>thJA</sub>		-	80	-	°C/W
Anarovinata waiaht			0.1		g	
Approximate weight			0.0035		oz.	
Marking device		Case style SMPC (TO-277A)		SC	H1	

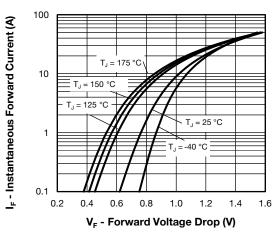


Fig. 1 - Typical Forward Voltage Drop Characteristics

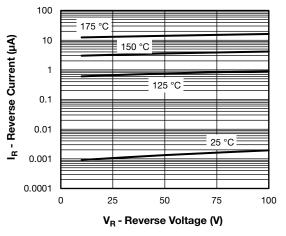
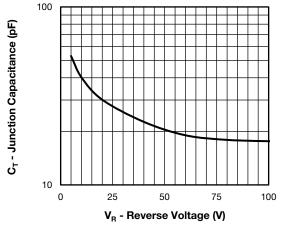


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

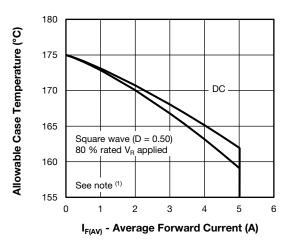


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

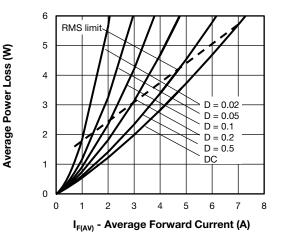
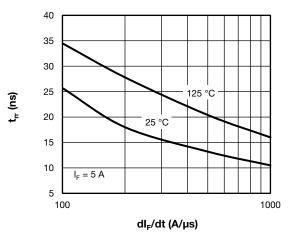


Fig. 5 - Forward Power Loss Characteristics





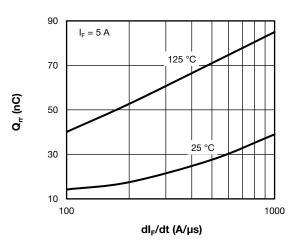


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $\begin{array}{l} \mbox{Pd} = \mbox{forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 5);} \\ \mbox{Pd}_{REV} = \mbox{inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 D); I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{rated } \mbox{V}_{R} \end{array}$

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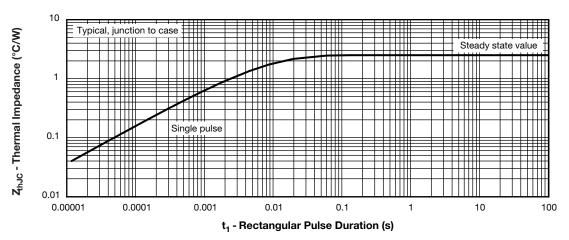
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# VS-10CSH01-M3

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Fig. 8 - Typical Transient Thermal Impedance, Junction to Case

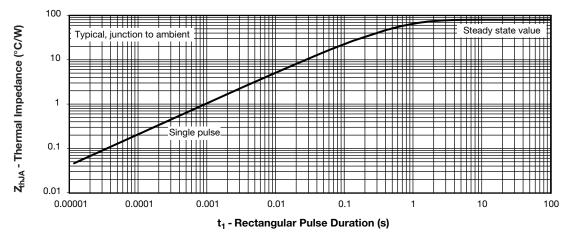


Fig. 9 - Typical Transient Thermal Impedance, Junction to Ambient

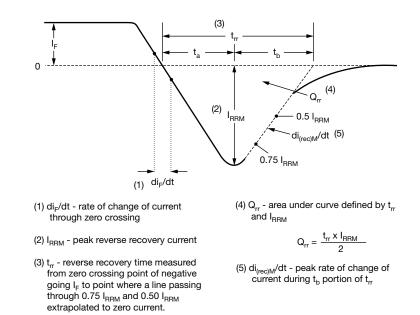


Fig. 10 - Reverse Recovery Waveform and Definitions

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**ORDERING INFORMATION TABLE** 

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SHAY

Device code VS-	10	С	s	н	01	-M3
	2	3	4	5	6	7
1 2 3	- Cur - Circ	rent rati cuit conf	niconduo ng (10 = iguration on catho	= 10 A) n:	oduct	
4 5 6 7	- Pro H = - Voli	cess typ hyper fa tage coo	package be, ast reco de (01 = gen-free	very 100 V)	-complia	ant, and

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-10CSH01-M3/86A	1500	1500	7" diameter plastic tape and reel			
VS-10CSH01-M3/87A	6500	6500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95570			
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96095			

## **Outline Dimensions**





SMPC (TO-277A)

### **DIMENSIONS** in inches (millimeters)





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