

CBRLD1 SERIES
SURFACE MOUNT
LOW PROFILE
SILICON BRIDGE RECTIFIER
1 AMP, 200 THRU 1000 VOLTS



• Device is **Halogen Free** by design



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRLD1 series is a full wave silicon bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

MARKING: SEE MARKING CODE TABLE ON FOLLOWING PAGE

APPLICATIONS:

- Solid State Lighting (SSL)
- Power supply input rectification
- Steering diode array

FEATURES:

- Low leakage current (10 μ A MAX @ V_{RRM})
- Low profile case (1.45mm MAX)

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

		CBRLD1					
	SYMBOL	-02	-04	-06	-08	-10	UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	200	400	600	800	1000	V
DC Blocking Voltage	V_R	200	400	600	800	1000	V
RMS Reverse Voltage	$V_{R(RMS)}$	140	280	420	560	700	V
Average Forward Current	I_O			1.0			A
Peak Forward Surge Current ($t_p=8.3\text{ms}$)	I_{FSM}			30			A
Operating & Storage Junction Temperature	T_J, T_{stg}			-55 to +150			$^\circ\text{C}$
Thermal Resistance	Θ_{JA}			95			$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^\circ\text{C}$ unless otherwise noted)

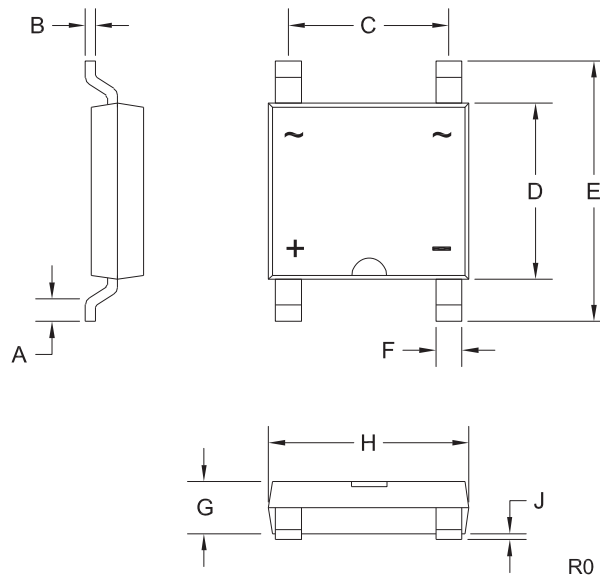
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_R	$V_R=V_{RRM}$		10	μA
V_F	$I_F=1.0\text{A}$		1.0	V
C_J	$V_R=4.0\text{V}, f=1.0\text{MHz}$		10	pF

R1 (18-March 2012)

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LPDIP CASE - MECHANICAL OUTLINE



DEVICE	MARKING CODE
CBRLD1-02	CBL2
CBRLD1-04	CBL4
CBRLD1-06	CBL6
CBRLD1-08	CBL8
CBRLD1-10	CBL10

DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.019	0.028	0.50	0.70
B	0.007	0.012	0.20	0.30
C	0.153	0.162	3.90	4.10
D	0.167	0.179	4.25	4.55
E	0.248	0.264	6.30	6.70
F	0.023	0.028	0.60	0.70
G	0.045	0.058	1.15	1.45
H	0.190	0.203	4.85	5.15
J	0.002	0.006	0.05	0.15
LPDIP (REV: R0)				

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