

SPECIFICATIONS FOR LCD MODULE

Module No. GG2015

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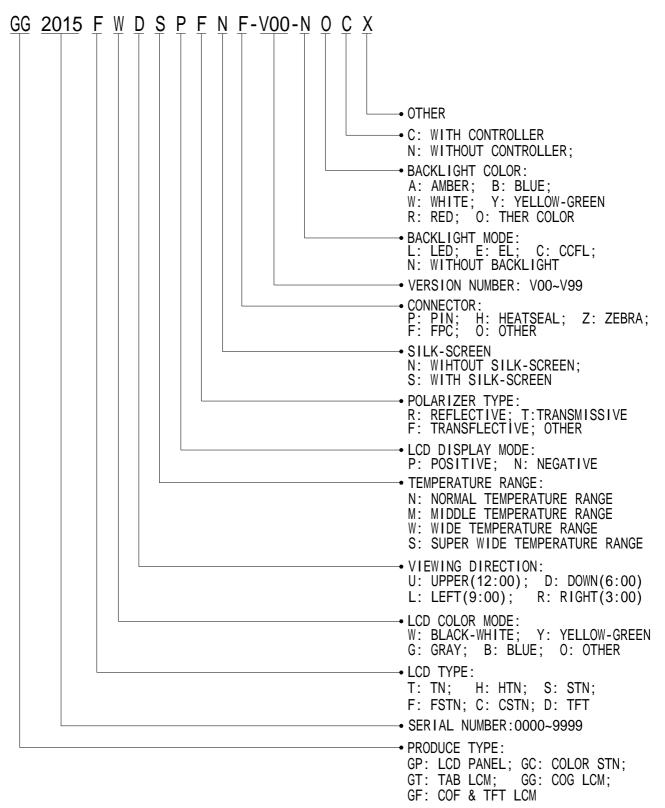
TABLE OF CONTENTS LCM NUMBER SYSTEM 2 1. 2. FEATURES......3 MECHANICAL SPECIFICATION 3 3. MECHANICAL DIMENSION.....4 4. 5. MAXIMUM RATINGS 5 ELECTRICAL CHARACTERISTICS......5 6. MODULE FUNCTION DESCRIPTION 6 7. ELECTRO-OPTICAL CHARACTERISTICS 17 8. 9. RELIABILITY......20

10. PRECAUTIONS FOR USING LCD MODULES 21

SAMPLE APPROVED REPORT26



LCM Number System



1. GENERAL DESCRIPTION

The GG2015 is a 96 x 64 Dots Graphic LCD module. It has a FSTN panel composed of 96 segments and 64 commons. The LCM can be easily accessed by microcontroller via parallel or series interface.

2. FEATURES

Diaplay Mada	Transflective and positive
Display Mode	FSTN module
Display Format	Graphic 96x64 dots
Input Data	8 bit parallel or series data input from MPU
Multiplexing Ratio	1/65 Duty
Bias	1/9 Bias
Viewing Direction	6 O'clock
Controller LSI	S6B0724
Backlight	None

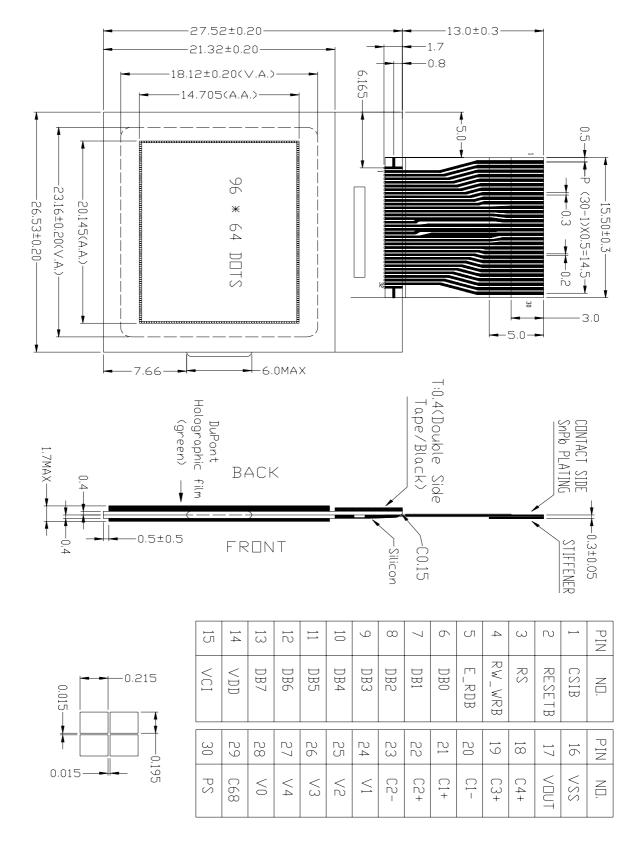
3. MECHANICAL SPECIFICATION

Item	Item Specifications					
Dimensional outline	26.53 x (27.52+13.0) x 1.7(max)	mm				
Resolution	96segs x 64coms	dots				
Active area	20.145(W) x 14.705(H)	mm				
Dots pitch	0.21(W)×0.23(H)	mm				
Dots size	0.195(W)×0.215(H)	mm				

GG2015 VER: 2.0 - 3 - Issue date: 2005/11/21



4. MECHANICAL DIMENSION



GG2015 VER: 2.0 -4- Issue date: 2005/11/21

5. MAXIMUM RATINGS

Item	Symbol	Min	Max	Unit	Note
G 1 1.	V _{DD} - V _{SS}	-0.3	4.0	V	
Supply voltage	V_{LCD}	-0.3	18.0	V	
Input Voltage	$V_{\rm IN}$	-0.3	V _{DD} +0.3	V	
Operating temperature	T_{OPR}	-20	+70	$^{\circ}\!\mathbb{C}$	
Storage temperature	T_{STR}	-30	+80	$^{\circ}\!\mathbb{C}$	
Humidity			90	%RH	

6. ELECTRICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
Supply Voltage	Logic	$V_{\scriptscriptstyle DD}$			3.0		V	
T (37.1)	H level	V_{IH}		$0.8V_{\mathrm{DD}}$		$V_{\scriptscriptstyle DD}$	7.7	
Input Voltage	L level	$V_{\scriptscriptstyle { m IL}}$		V_{ss}		$0.2V_{\scriptscriptstyle m DD}$	V	
Current Consur (LCD DRIV	•	${ m I}_{ m DD}$	V_{DD} =3.0V; V_{LCD} =9.0V, T_{amb} =25°C;			1.2	mA	
LCD Driving V	oltage	$V_{\scriptscriptstyle LCD}$	Bias=1/9 VLCD=V0-Vss 9.0			V		

GG2015 VER: 2.0 - 5 - Issue date: 2005/11/21

7. MODULE FUNCTION DESCRIPTION

7.1. PIN DESCRIPTION

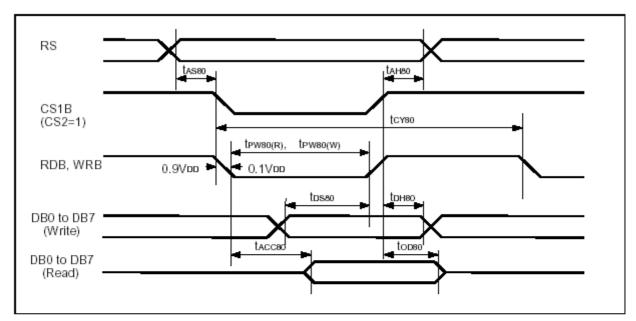
Pin No.	Symbol	Description
1	CS1B	Chip select signal input, Low active.
2	RESETB	Reset signal input, Low active
3	RS	Data/command select signal input, H: Data; L: Command
4	RW_WRB	When use 8080 MPU, this is active Low and Write signal input pin. When use 6800 MPU, H: Read; L: Write.
5	E_RDB	When use 8080 MPU, this is active Low and Read signal input pin. When use 6800 MPU, this is active High and Enable Clock input pin.
6-13	DB0-DB7	8-bit bi-directional Data bus. When "P/S=L", D0-D5 fixed to "H". D6 is serial clock input (SCL), D7 is serial data input (SDA).
14	VDD	Power supply for Positive (+3V).
15	VCI	Refer voltage for LCD voltage converter circuit
16	VSS	Power supply for Ground (0V).
17	VOUT	DC/DC voltage converter. Connect a capacitor between this pin VSS.
18	CAP4+	DC/DC voltage converter. Connect to Vout when use 4-time voltage converter, and connect a capacitor between this pin and CAP2- when use 5-times voltage converter.
19	CAP3+	DC/DC voltage converter. Connect a capacitor between this pin and CAP1
20	CAP1-	DC/DC voltage converter. Connect a capacitor between this pin and CAP1+.
21	CAP1+	DC/DC voltage converter. Connect a capacitor between this pin and CAP1
22	CAP2+	DC/DC voltage converter. Connect a capacitor between this pin and CAP2
23	CAP2-	DC/DC voltage converter. Connect a capacitor between this pin and CAP2+.
24-27	V1-V4	This is multi-level power supply for liquid crystal driver, and must maintain the relative magnitudes shown below:
28	V0	$V0 \ge V1 \ge V2 \ge V3 \ge V4 \ge Vss$
29	C86	MPU interface select pin, "H": 6800 MPU interface, "L":8080 MPU interface. When "P/S=L", this pin fixed to "H" or "L".
30	P/S	Parallel/serial data input select. "H": Parallel data input; "L": Serial data input.

GG2015 VER: 2.0 - 6 - Issue date: 2005/11/21

7.2 TIMING CHARACTERISTICS

1.SYSTEM BUS READ/WRITE CHARACTERISTIC

Read / Write Characteristics (8080-series MPU)



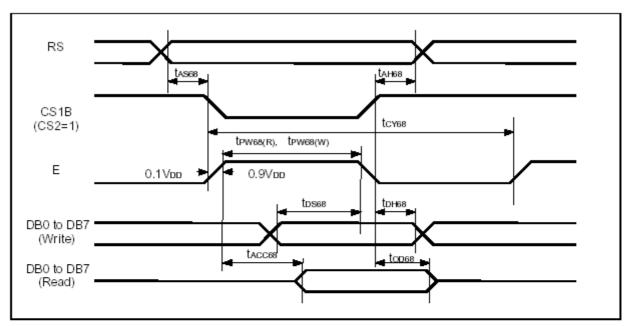
Read / Write Characteristics (8080-series MPU)

 $(VDD = 2.4 \text{ to } 3.6 \text{V}. \text{ Ta} = -40 \text{ to } +85^{\circ}\text{C})$

Item	Signal	Symbol	Min.	Тур.	Max.	Unit	Remark
Address setup time Address hold time	RS	tas80 tah80	0	-	-	ns	
System cycle time	RS	tcy80	300	-	-	ns	
Pulse width (WRB)	RW_WRB	tpw80(w)	60	,	ı	ns	
Pulse width (RDB)	E_RDB	tpw80(R)	60	1	1	ns	
Data setup time Data hold time	DB7	tDS80 tDH80	40 15	,	1	ns	
Read access time Output disable time	to DB0	tACC80 tOD80	- 10	-	140 100	ns	CL = 100 pF

GG2015 VER: 2.0 -7 - Issue date: 2005/11/21

Read / Write Characteristics (6800-series Microprocessor)



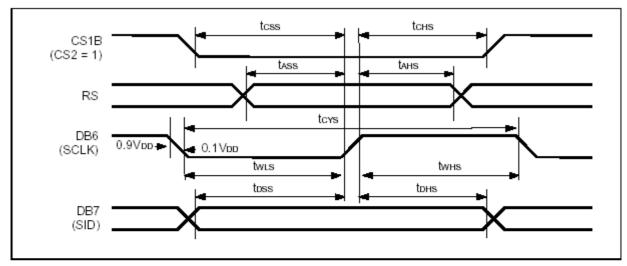
Read / Write Characteristics (6800-series Microprocessor)

(VDD = 2.4 to 3.6V, Ta = -40 to +85°C)

Item		Signal	Symbol	Min.	Тур.	Max.	Unit	Remark
Address setup Address hold t		RS	tas68 tah68	0	,	,	ns	
System cycle t	ime	RS	tcy68	300	-	-	ns	
Data setup tii Data hold tin		DB7 to	tDS68 tDH68	40 15	-	-	ns	
Access time Output disable time		DB0	tACC68 tOD68	- 10	,	140 100	ns	CL = 100 pF
Enable pulse width	Read Write	E_RDB	tPW68(R) tPW68(W)	120 60	-	-	-	

GG2015 VER: 2.0 - 8 - Issue date: 2005/11/21

Serial Interface Characteristics



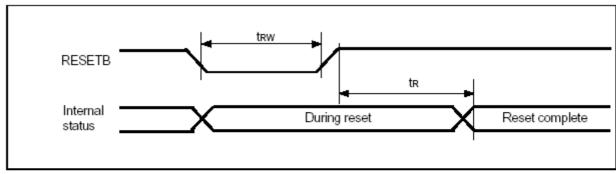
Serial Interface Characteristics

(VDD = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Тур.	Max.	Unit	Remark
Serial clock cycle SCLK high pulse width SCLK low pulse width	DB6 (SCLK)	tcys twns twls	250 100 100	1 1 1	1 1 1	ns	
Address setup time Address hold time	RS	tass tahs	150 150			ns	
Data setup time Data hold time	DB7 (SID)	toss tons	100 100	1 1	1 1	ns	
CS1B setup time CS1B hold time	CS1B	tcss tcнs	150 150		-	ns	

GG2015 VER: 2.0 -9 - Issue date: 2005/11/21

2. RESET TIMING



Reset Input Timing

(VDD = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Тур.	Max.	Unit	Remark
Reset low pulse width	RESETB	trw	1.0	-	,	ns	
Reset time	-	tR	-	-	1.0	ns	_

3. DISPLAY CONTROL TIMING



Display Control Output Timing

(VDD = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Тур.	Max.	Unit	Remark
FR delay time	FR	tofr	-	20	80	ns	CL = 50 pF

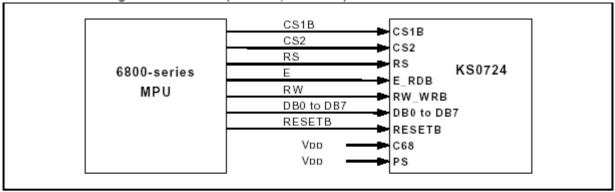
GG2015 VER: 2.0 - 10 - Issue date: 2005/11/21

7.3 APPLICATION OF LCM

■Reference circuit

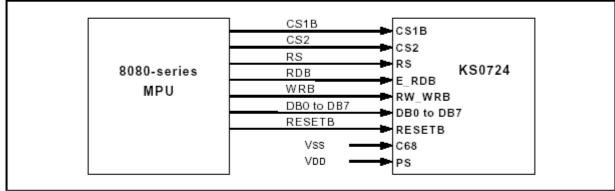
MICROPROCESSOR INTERFACE

In Case of Interfacing with 6800-series (PS = "H", C68 = "H")



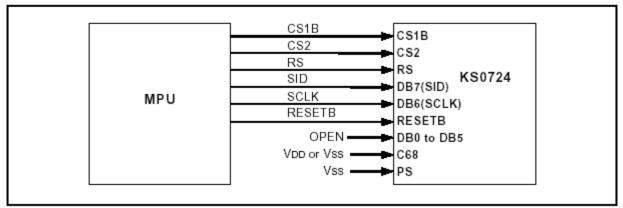
Interfacing with 6800-series (PS = "H", C68 = "H")





Interfacing with 8080-series (PS = "H", C68 = "L")

In Case of Serial Interface (PS = "L", C68 = "H or L")

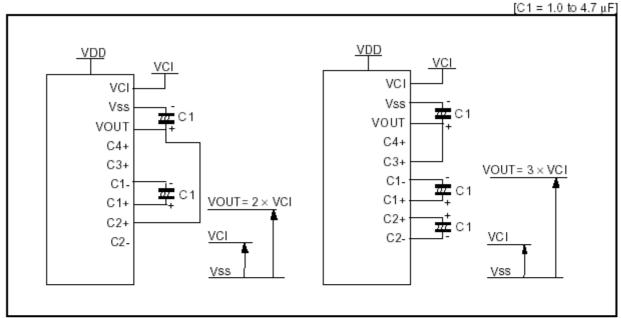


Serial Interface (PS = "L", C68 = "H or L")

GG2015 VER: 2.0 - 11 - Issue date: 2005/11/21

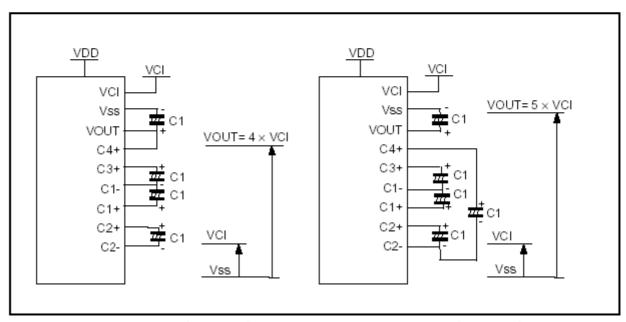
Voltage Converter Circuits

These circuits boost up the electric potential between VCI and Vss to 2, 3, 4 or 5 times toward positive side and boosted voltage is outputted from VOUT pin.



Two Times Boosting Circuit

Three Times Boosting Circuit



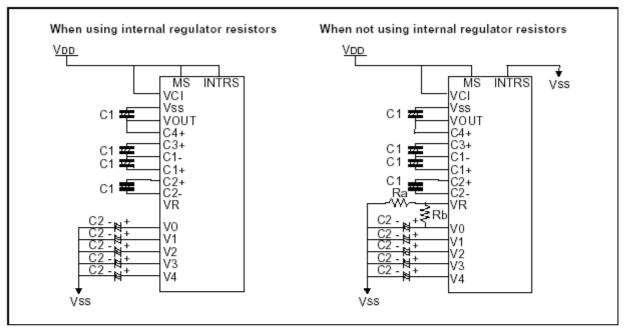
Four Times Boosting Circuit

Five Times Boosting Circuit

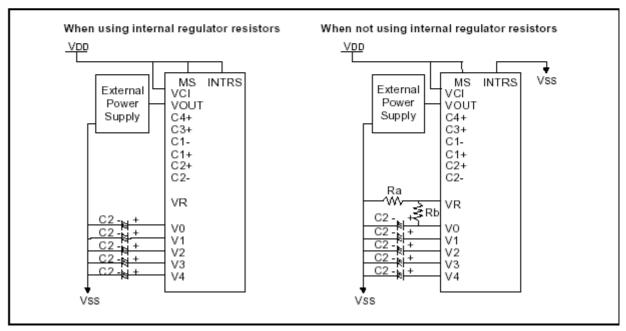
GG2015 VER: 2.0 - 12 - Issue date: 2005/11/21

^{*} The VCI voltage range must be set so that the VOUT voltage does not exceed the absolute maximum rated value

REFERENCE CIRCUIT EXAMPLES

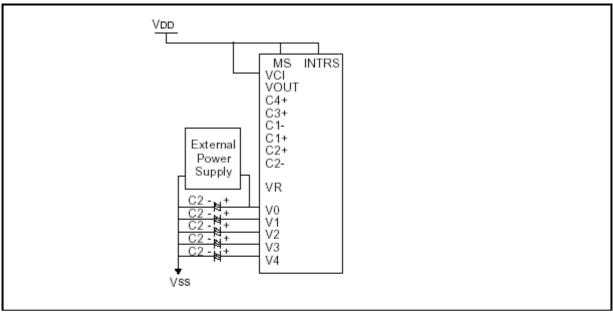


When Using all Internal LCD Power Circuits (VCI = VDD, 4-time V/C: ON, V/R: ON, V/F: ON)

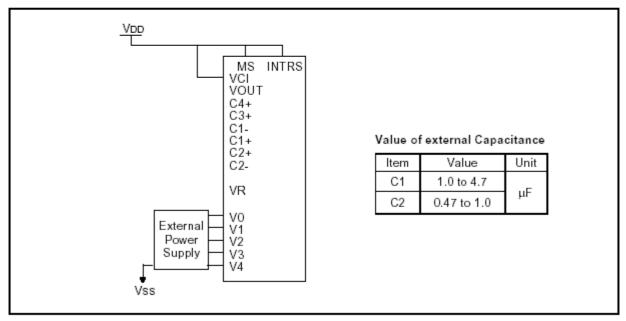


When Using some Internal LCD Power Circuits (VCI = VDD, V/C: OFF, V/R: ON, V/F: ON)

GG2015 VER: 2.0 - 13 - Issue date: 2005/11/21



When Using some Internal LCD Power Circuits (VCI = VDD, V/C: OFF, V/R: OFF, V/F: ON)



When Not Using any Internal LCD Power Supply Circuits (VCI = VDD, V/C: OFF, V/R: OFF, V/F: OFF)

GG2015 VER: 2.0 - 14 - Issue date: 2005/11/21

^{*} C1 and C2 are determined by the size of the LCD being driven. Select a value that will stabilize the liquid crystal drive voltage.



7.4 TABLE OF COMMAND

x: Don't care

											x: Don't care
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description
Display ON / OFF	0	0	1	0	1	0	1	1	1	DON	Turn on/off LCD panel When DON = 0: display OFF When DON = 1: display ON
Initial display line	0	0	0	1	ST5	ST4	ST3	ST2	ST1	ST0	Specify DDRAM line for COM0
Set page address	0	0	1	0	1	1	P3	P2	P1	PO	Set page address
Set column address MSB	0	0	0	0	0	1	Y7	Y6	Y5	Y4	Set column address MSB
Set column address LSB	0	0	0	0	0	0	Y3	Y2	Y1	Y0	Set column address LSB
Read status	0	1	BUSY	ADC	OVOFF	RESETB	0	0	0	0	Read the internal status
Write display data	1	0				Write	data				Write data into DDRAM
Read display data	1	1				Read	data				Read data from DDRAM
ADC select	0	0	1	0	1	0	0	0	0	ADC	Select SEG output direction When ADC = 0: normal direction (SEG0→SEG131) When ADC = 1: reverse direction (SEG131→SEG0)
Reverse display ON / OFF	0	0	1	0	1	0	0	1	1	REV	Select normal / reverse display When REV = 0: normal display When REV = 1: reverse display
Entire display ON / OFF	0	0	1	0	1	0	0	1	0	EON	Select normal/entire display ON When EON = 0: normal display. When EON = 1: entire display ON
LCD bias select	0	0	1	0	1	0	0	0	1	BIAS	Select LCD bias
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode
Reset modify-read	0	0	1	1	1	0	1	1	1	0	release modify-read mode
Reset	0	0	1	1	1	0	0	0	1	0	Initialize the internal functions
SHL select	0	0	1	1	0	0	SHL	×	×	×	Select COM output direction When SHL = 0: normal direction (COM0→COM63) When SHL = 1: reverse direction (COM63→COM0)
Power control	0	0	0	0	1	0	1	VC	۷R	VF	Control power circuit operation
Regulator resistor select	0	0	0	0	1	0	0	R2	R1	RO	Select internal resistance ratio of the regulator resistor
Set reference voltage mode	0	0	1	0	0	0	0	0	0	1	Set reference voltage mode
Set reference voltage register	0	0	×	×	SV5	SV4	SV3	SV2	SV1	SV0	Set reference voltage register
Set static indicator mode	0	0	1	0	1	0	1	1	0	SM	Set static indicator mode
Set static indicator register	0	0	×	×	×	×	×	×	S1	S0	Set static indicator register
Power save	-	-	-	-	-	-	-	-	-	-	Compound Instruction of display OFF and entire display ON
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description
NOP	0	0	1	1	1	0	0	0			
Test Instruction_1	0	0	1	1	1	1	×	×	×	×	Don't use this instruction
Test Instruction_2	0	0	1	0	0	1	×	×	×	×	Don't use this instruction

GG2015 VER: 2.0 - 15 - Issue date: 2005/11/21

8. ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Temp	Min	Тур.	Max	Units	Note
1 (7) 1 : 1			0°C	8.9	9.2	9.5		
LCD driving voltage	VLCD	$\theta = \phi = 0$	25℃	8.7	9.0	9.3	V	NOTE1
			50°C	8.5	8.8	9.1		
	Rise Time (Tr)		0°C					
	Decay Time (Tf)						msec	
	Rise Time (Tr)				225	340		
Response Time	Decay Time (Tf)	$\theta = \phi = 0$	25°C		240	360		NOTE2
	Rise Time (Tr)		50°C					
	Decay Time (Tf)		50°C					
Contrast Ratio	Cr	$\theta = \phi = 0$	25℃	5	10			NOTE4

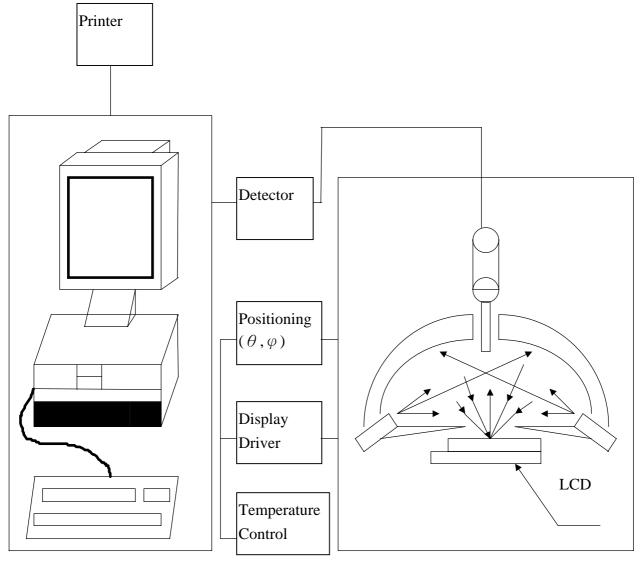
Viewing Angle Range	$\theta (\phi = 0^{\circ})$ (6")	$\phi = 90^{\circ}$ (3")	φ=180° (12")	φ=270° (9")	備註
θ (25°C) CR≥2	45	35	15	30	Deg NOTE3

• For panel only

GG2015 VER: 2.0 - 16 - Issue date: 2005/11/21



• Electro-Optical Characteristics Measuring Equipment(DMS501)

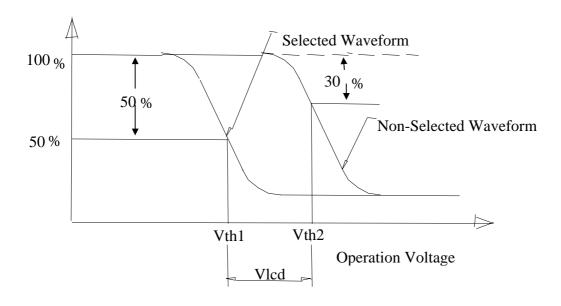


System Illumination (D65)

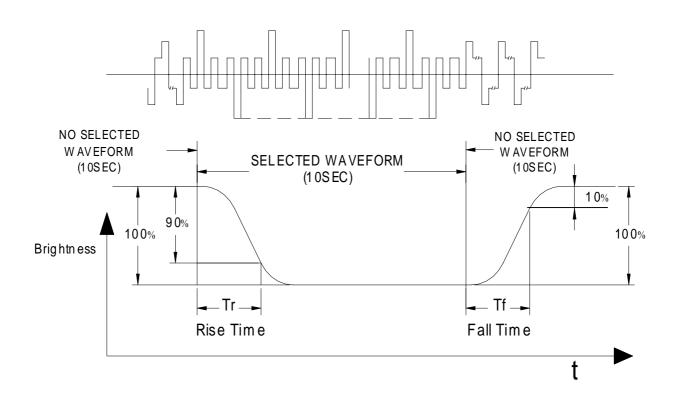
GG2015 VER: 2.0 - 17 - Issue date: 2005/11/21



• Note 1. Definition of Driving Voltage(Vlcd):



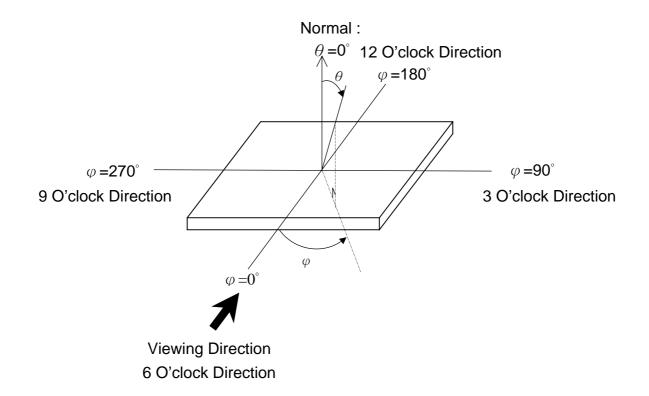
• Note 2. Definition of Optical Response Time :



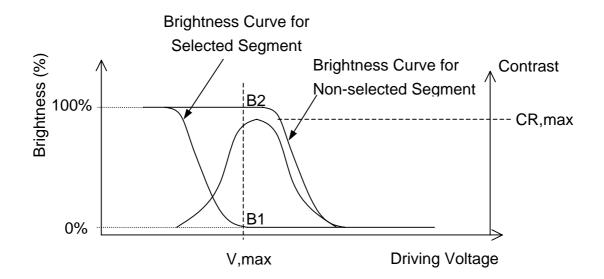
GG2015 VER: 2.0 - 18 - Issue date: 2005/11/21



• Note 3. Definition of Viewing Angle $\,\theta\,$ and $\,\phi\,$:



• Note 4. Definition of Contrast ratio(CR):



GG2015 VER: 2.0 - 19 - Issue date: 2005/11/21

9. RELIABILITY

9.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal. (25°C in the room without sunlight)

9.2. TESTS

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Operating	70°C 120Hrs	No Defect OfOperational Function InRoom Temperature Are
2	Low Temperature Operating	-20°C 120Hrs	Allowable. • IDD of LCM in
3	High Temperature/ Humidity Non-Operating	70°C ,90%RH ,120 Hrs	Pre-and post-test should follow specification
4	High Temperature Non-Operating	80°C 120Hrs	
5	Low Temperature Non-Operating	-30°C 120Hrs	
6	Temperature Cycling Non-Operating	-20°C (30Min) ↔ 70°C (30Min) 10 CYCLES	

Notes: Judgments should be mode after exposure in room temperature for two hours.

GG2015 VER: 2.0 - 20 - Issue date: 2005/11/21

10. PRECAUTIONS FOR USING LCD MODULES

10.1. HANDLING PRECAUTIONS

- (1) The display panel is made of glass. Do not subject it to a mechanical shock or impact by dropping it.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten a cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol
- (6) Solvents other than those above mentioned may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- (7) Extra care to minimize corrosion of the electrode. Water droplets, moisture condensation or a current flow in a high-humidity environment accelerates corrosion of the electrode.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD Module, make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD Module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling he LCD Module.
 - Tools required for assembling, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.

GG2015 VER: 2.0 - 21 - Issue date: 2005/11/21

-The LCD Module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

10.2. STORAGE CONDITIONS

When storing, avoid the LCD module to be exposed to direct sunlight of fluorescent lamps. For stability, to keep it away form high temperature and high humidity environment (The best condition is : 23 ± 5 °C, 45 ± 20 %RH). ESD protection is necessary for long-term storage also.

10.3. OTHERS

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD Module have been operating for a long time showing the same display patterns the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be recovered by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD Module resulting from destruction caused by static electricity etc. exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

GG2015 VER: 2.0 - 22 - Issue date: 2005/11/21

11. Using LCD modules

11.1 LIQUID CRYSTAL DISPLAY MODULES

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances, which will be damaged by chemicals such as acetone, toluene, ethanol and isopropyl alcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum ether. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degrade insulation between terminals (some cosmetics are determinate to the polarizers).
- (10)As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

11.2 INSTALLING LCD MODULE

Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

11.3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid for electrostatic discharge as for an ordinary CMOS IC.

GG2015 VER: 2.0 - 23 - Issue date: 2005/11/21

- (1) Make certain that you are grounded when handing LCM.
- (2) Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible, make the electric potential of your work clothes and that of the workbenches to the ground potential.
- (6) To reduce the generation of electro-static discharge, be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

11.4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (Vo). Adjust Vo to show the best contrast.
- (2) Driving the LCD in the voltage above the limit will shorten its lifetime.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, this product must be used and stored within the specified condition of 23±5°C, 45±20%RH.
- (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.

11.5 SAFETY

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

GG2015 VER: 2.0 - 24 - Issue date: 2005/11/21

12. REVISION HISTORY

Version	Revise record	Date
1.0	Original version	05-01-01
2.0	Change specification format	05-11-21

GG2015 VER: 2.0 - 25 - Issue date: 2005/11/21



SAMPLE APPROVED REPORT

(样品确认单)

SAMPLE MODEL NO. (样品型号)	GG2015			
SAMPLE SERIES NUMBER NO. (样品序号)				
SAMPLE QUANTITY (样品数量)				
COLOR/TYPE (底色/类型)	FSTN/POSITIVE			
VIEWING DIRECTION (视角)	6:00			
DRIVING METHOD (驱动参数)	1/65Duty, 1/9Bias			
LOGIC VOLTAGE (IC 工作电压)	3.0V			
LCD VOP (LCD 驱动电压)	9.0V			
OPERATING TEMP. (操作温度)	-20~70			
STORAGE TEMP. (储存温度)	-30~80			
POLARIZERFRONT (首偏光片)	TRANSMISSIVE			
POLARIZERBACK (后偏光片)	TRANSFLECTIVE			
CONTROLLER/DRIVER IC(控制/驱动 IC)	S6B0724			
BACKLIGHT COLOR/TYPE (背光源类型/颜色)	NONE			
DRAWING REV/NO./QUANTITY (图纸版本/数量)				
SPECIFICATION (规格书 份数)				
REMARKS:				
(备注)				
WRIT BY : DATE : APROV BY : _	DATE :			
CUSTOMER'S APPROVAL (客户确认):				
1) FUNCTION (功能): □ OK □ N.G.				
2) DRIVER CONDITION (驱动条件): □ OK □ N.G.				
3) DISPLAY MODE (显示模式): □ OK	□ N.G.			
4) VIEWING ANGLE (视角): □ OK	□ N.G.			
5) BACKLIGHT (背光源): □ OK				
6) DISPLAYING PATTERN (显示效果): □ (
CUSTOMER'S CONCLUSIONS (客户意见):				
	D . FD (F) HD)			
CUSTOMER'S SIGNATURE(客户签名):	_ DATE (日期):			