100V INPUT, 12V 30mA REGULATOR TRANSISTOR IN SOT23F

Description

The ZXTR2012FF monolithically integrates a transistor, Zener diode and resistor to function as a high-voltage linear regulator. The device regulates with a 12V nominal output at 15mA. It is designed for use in high-voltage applications where standard linear regulators cannot be used. This function is fully integrated into an SOT23F package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

Features

- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 15V to 100V
- Output Voltage = 12V ± 10%
- Fully Integrated into a SOT23F Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Applications

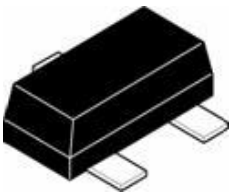
Supply voltage regulation in:

- Networking
- Telecommunications
- Power over Ethernet (PoE)

Mechanical Data

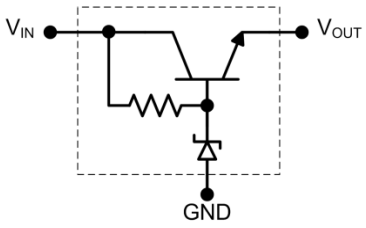
- Case: SOT23F
- Case material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.012 grams (Approximate)

SOT23F

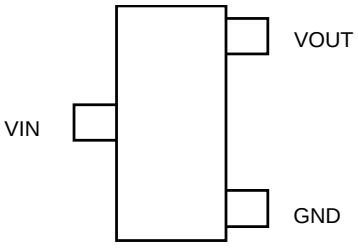


Top View

ZXTR2012FF



Internal Device Schematic



Top View Pin-Out

Pin Name	Pin Function
VIN	Input Supply
GND	Power Ground
VOUT	Voltage Output

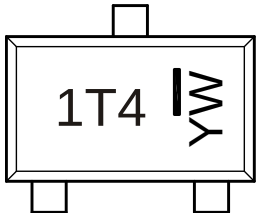
Ordering Information (Note 4)

Product	Package	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTR2012FF-7	SOT23F	1T4	7	8	3,000

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free,"Green" and Lead-Free.
3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

SOT23F



1T4 = Product Type Marking Code
YW = Date Code Marking
Y = Year : 0~9
W = Week : A~Z : 1~26
a~z : 27~52
z represents 52 & 53 week

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Absolute Maximum Ratings (Voltage relative to GND, @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Supply Voltage	V_{IN}	-0.3 to 100	V
Continuous Input & Output Current	I_{IN}, I_{OUT}	550	mA
Peak Pulsed Input & Output Current	I_{IM}, I_{OM}	2	A
Maximum Voltage Applied to V_{OUT}	$V_{OUT(MAX)}$	18	V

Maximum Current at V_{IN} = 48V (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Continuous Output Current	(Note 7)	I_{OUT}	36	mA
Pulsed Output Current	(Note 8)	I_{OM}	880	mA
	(Note 9)		180	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	P_D	1.3	W
	(Note 6)		1	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	95	°C/W
	(Note 6)		126	
Thermal Resistance, Junction to Lead	(Note 10)	$R_{\theta JL}$	59	
Thermal Resistance, Junction to Case	(Note 10)	$R_{\theta JC}$	38	
Maximum Operating Junction and Storage Temperature Range		T_J , T_{STG}	-65 to +150	°C

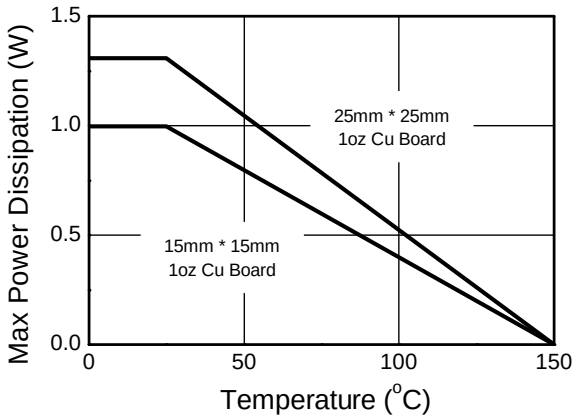
ESD Ratings (Note 11)

Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	C

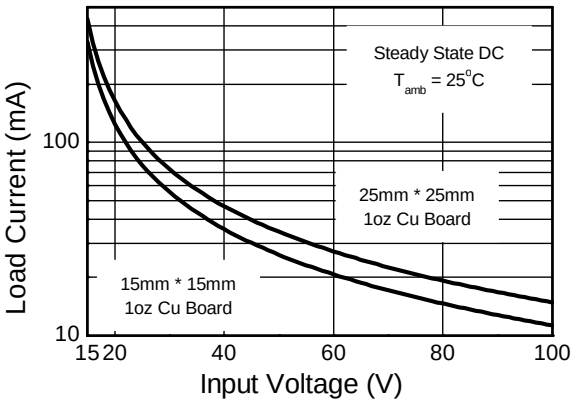
Notes:
5. For a device mounted with the exposed V_{IN} pad on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in steady-state.
6. Same as note 5, except mounted on 15mm x 15mm 1oz copper.
7. Same as note 5, whilst operating at V_{IN} = 48V. Refer to Safe Operating Area for other Input Voltages.
8. Same as note 5, except measured with a single pulse width = 100µs and V_{IN} = 48V.
9. Same as note 5, except measured with a single pulse width = 10ms and V_{IN} = 48V.
10. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (on the exposed V_{IN} pad).
    $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

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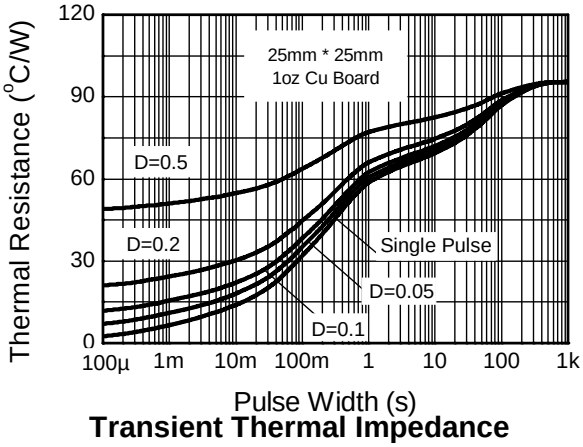
Thermal Characteristics and Derating Information



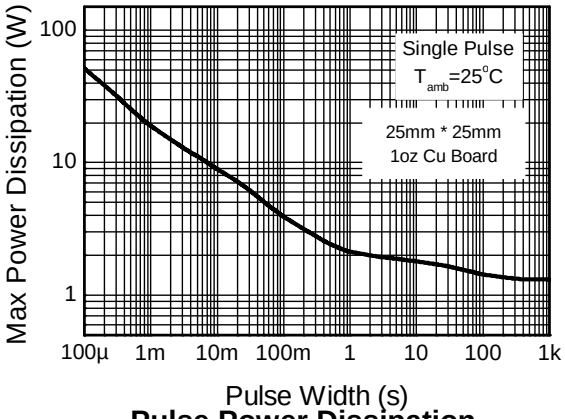
Derating Curve



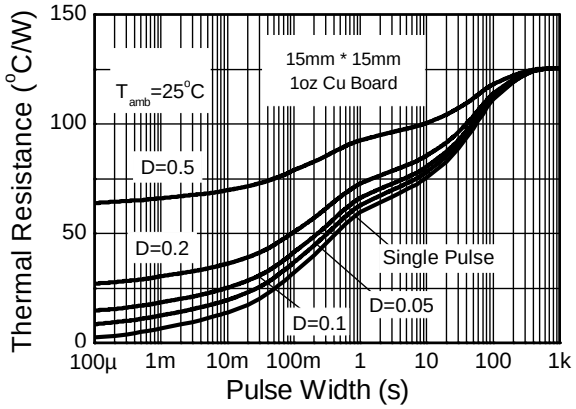
Safe Operating Area



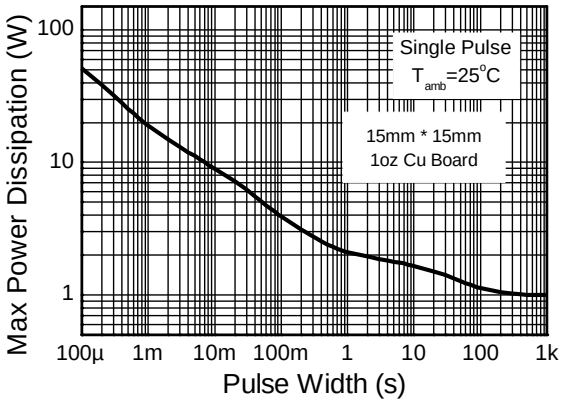
Transient Thermal Impedance



Pulse Power Dissipation



Transient Thermal Impedance



Pulse Power Dissipation

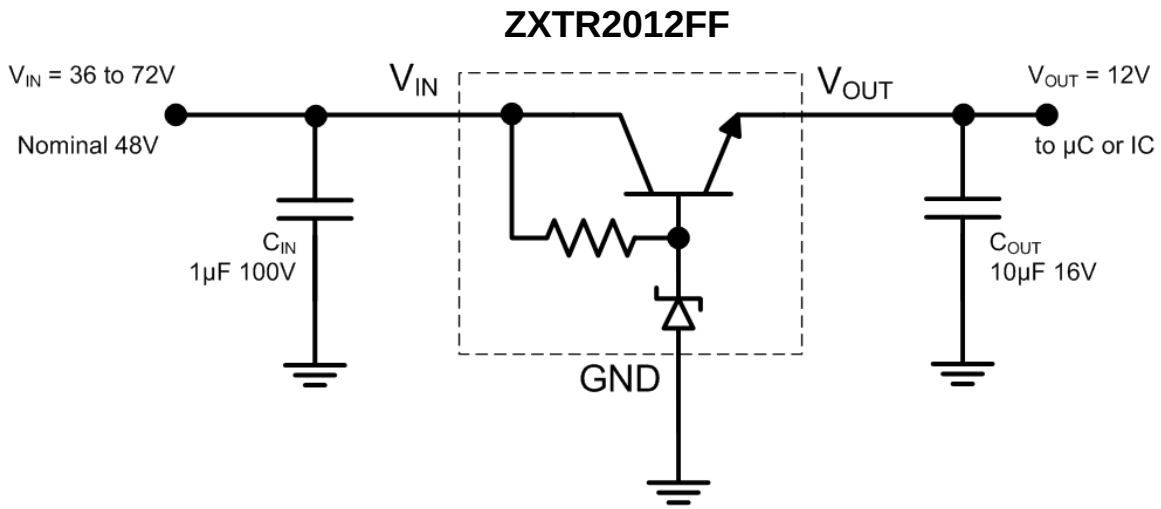
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Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Output Voltage (Note 12)	V_{OUT}	10.8	12	13.2	V	V_{IN} = 48V, I_{OUT} = 15mA
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	—	240	750	mV	V_{IN} = 15V to 72V , I_{OUT} = 15mA
Temperature Coefficient	$\Delta V_{OUT}/\Delta T$	—	8.0	—	mV/°C	T_J = -40°C to +125°C V_{IN} = 48V, I_{OUT} = 15mA
Load Regulation (Notes 12 & 14)	ΔV_{OUT}	—	-450 -600	-600 -750	mV	I_{OUT} = 0.1mA to 30mA, V_{IN} = 48V I_{OUT} = 0.1mA to 100mA, V_{IN} = 48V
Minimum Value of Input Voltage Required to Maintain Line Regulation	$V_{IN(MIN)}$	15	—	—	V	—
Quiescent Current	I_Q	—	240 590	400 900	µA	V_{IN} = 48V, I_{OUT} = 10µA V_{IN} = 100V, I_{OUT} = 10µA
Power Supply Rejection Ratio	$\Delta V_{IN}/\Delta V_{OUT}$	—	45	—	dB	C_{OUT} = 100nF, I_{OUT} = 15mA, V_{OUT} = 12V, V_{IN} =15V to 100V, f=100Hz

Notes: 12. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤ 2%.
13. Line regulation $\Delta V_{OUT} = V_{OUT}(@\ V_{IN} = 72V) – V_{OUT}(@\ V_{IN} = 15V)$.
14. Load regulation $\Delta V_{OUT} = V_{OUT}(@\ I_{OUT} = 30mA) – V_{OUT}(@\ I_{OUT} = 0.1mA)$.
$\Delta V_{OUT} = V_{OUT}(@\ I_{OUT} = 100mA) – V_{OUT}(@\ I_{OUT} = 0.1mA)$.

Typical Application Circuit



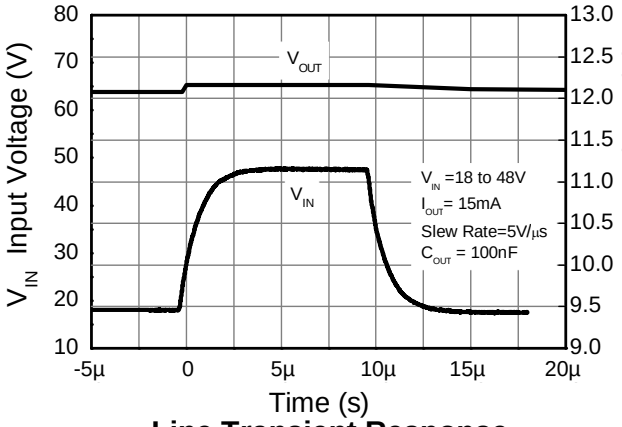
Example of an 12V regulated supply from a nominal 48V for powering a Controller IC.

Pin Functions

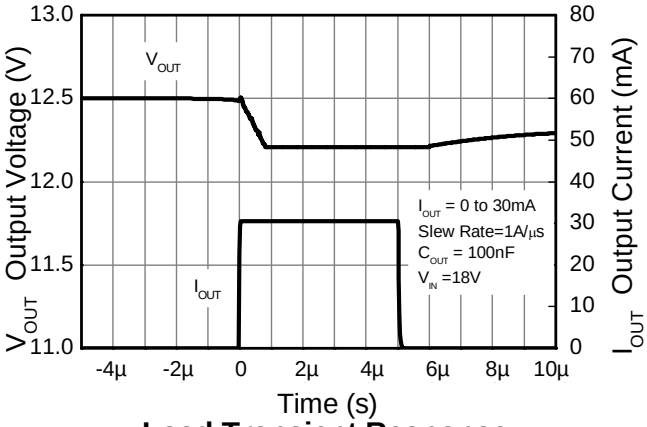
Pin Name	Pin Function	Notes
V_{IN}	Input Supply	To maintain output regulation the input voltage can vary from 15V to 100V with respect to the GND pin. It is recommended to connect a 1µF capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
V_{OUT}	Voltage Output	Outputs a regulated 12V. It is recommended to connect a 10µF capacitor to GND. Minimum of 10µA must be drawn from V_{OUT} to maintain regulation. The pin can be pulled high to a maximum of 18V with respect to ground.

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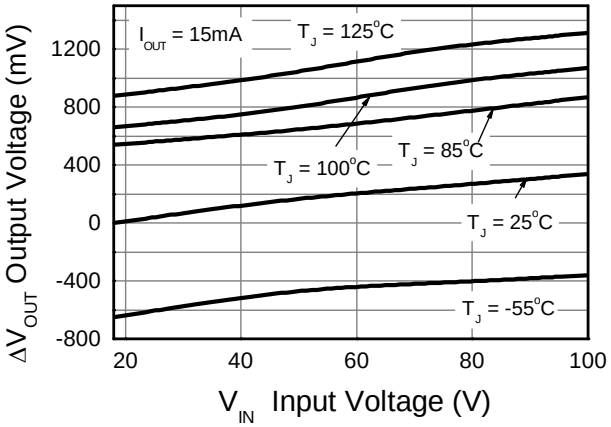
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)



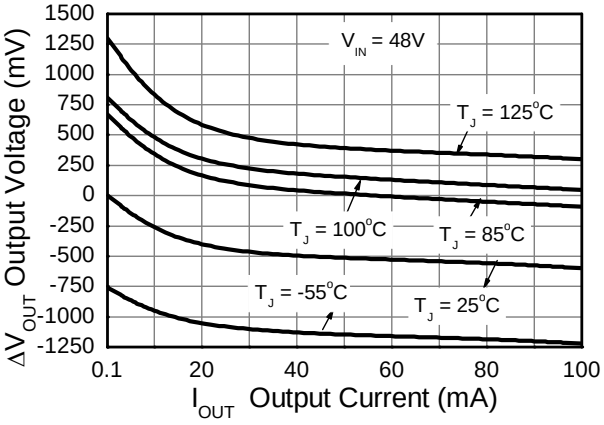
Line Transient Response



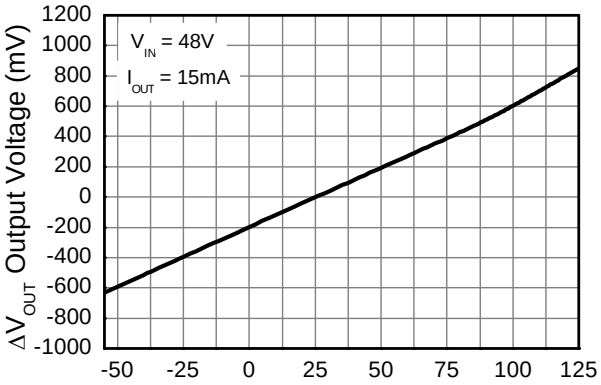
Load Transient Response



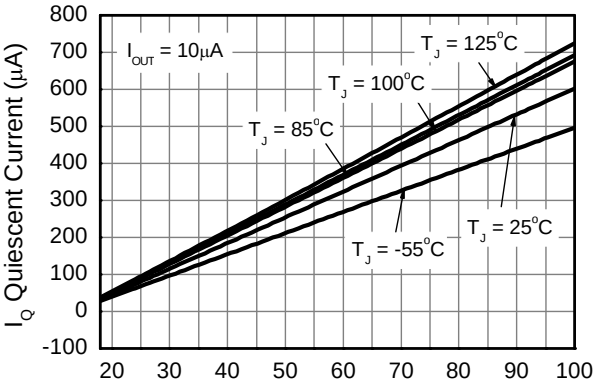
Line Regulation (Note 15)



Load Regulation (Note 16)



Temperature Coefficient (Note 17)



Quiescent Current

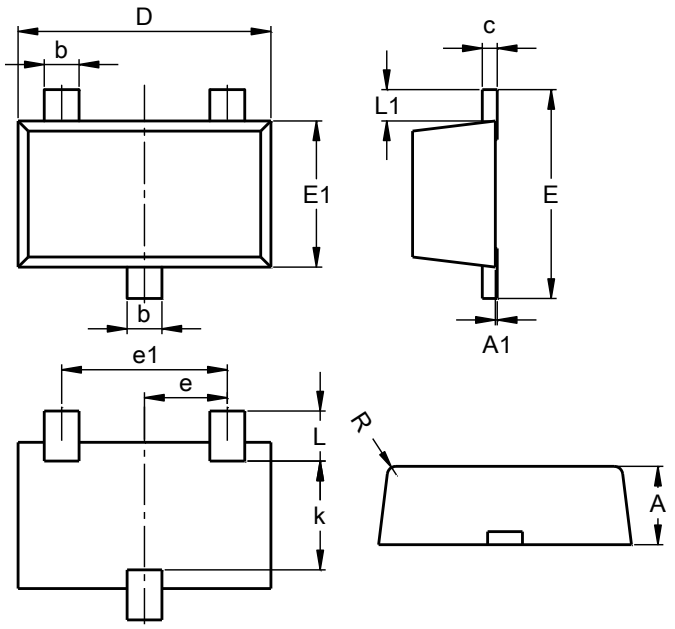
Notes: 15. Line regulation $\Delta V_{OUT} = V_{OUT} – V_{OUT}$ (@ V_{IN} = 15V, I_{OUT} = 15mA, T_J = +25°C).
16. Load regulation $\Delta V_{OUT} = V_{OUT} – V_{OUT}$ (@ V_{IN} = 48V, I_{OUT} = 0.1mA, T_J = +25°C).
17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} – V_{OUT}$ (@ V_{IN} = 48V, I_{OUT} = 15mA, T_J = +25°C).

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23F

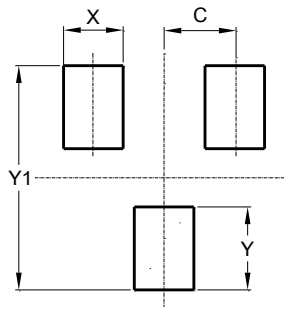


SOT23F			
Dim	**Min**	**Max**	**Typ**
A	0.80	1.00	0.90
A1	0.00	0.10	0.01
b	0.35	0.50	0.44
c	0.10	0.20	0.16
D	2.80	3.00	2.90
e	0.95 REF		
e1	1.90 REF		
E	2.30	2.50	2.40
E1	1.50	1.70	1.65
k	1.20	-	-
L	0.30	0.65	0.50
L1	0.30	0.50	0.40
R	0.05	0.15	-
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23F



Dimensions	Value (in mm)
C	0.95
X	0.80
Y	1.110
Y1	3.000

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