

BAV19WS-V-G, BAV20WS-V-G, BAV21WS-V-G

Vishay Semiconductors

Small Signal Switching Diodes, High Voltage



FEATURES

- Silicon epitaxial planar diodes
- For general purpose
- AEC-Q101 qualified
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





COMPLIANT

GREEN
(5-2008)

MECHANICAL DATA

Case: SOD-323
Weight: approx. 4 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE							
PART	TYPE DIFFERENTIATION	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS		
BAV19WS-V-G	V _R = 100 V	BAV19WS-V-G-18 or BAV19WS-V-G-08	AS	Single diode	Tape and reel		
BAV20WS-V-G	V _R = 150 V	BAV20WS-V-G-18 or BAV20WS-V-G-08	AT	Single diode	Tape and reel		
BAV21WS-V-G	V _R = 200 V	BAV21WS-V-G-18 or BAV21WS-V-G-08	AU	Single diode	Tape and reel		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	SYMBOL	VALUE	UNIT	
		BAV19WS-V-G	V_{R}	100	V	
Continuous reverse voltage		BAV20WS-V-G	V _R	150	V	
		BAV21WS-V-G	V _R	200	V	
		BAV19WS-V-G	V _{RRM}	120	V	
Repetitive peak reverse voltage		BAV20WS-V-G	V _{RRM}	200	V	
		BAV21WS-V-G	V_{RRM}	250	V	
Forward continuous current (1)			I _F	250	mA	
Rectified current (average) half wave recitification with resistive load (1)			I _{F(AV)}	200	mA	
Repetitive peak forward current (1)	f ≥ 50 Hz, θ = 180 °C		I _{FRM}	625	mA	
Surge forward current	t < 1 s, T _J = 25 °C		I _{FSM}	1	Α	
Power dissipation (1)			P _{tot}	200	mW	

Note

⁽¹⁾ Valid provided that leads are kept at ambient temperature

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air (1)		R_{thJA}	650	K/W		
Junction temperature (1)		T _j	150	°C		
Storage temperature range (1)		T_{stg}	- 65 to + 175	°C		

Note

⁽¹⁾ Valid provided that leads are kept at ambient temperature



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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100 \text{ mA}$		V_{F}			1	V
Forward voitage	$I_F = 200 \text{ mA}$		V_{F}			1.25	V
	V _R = 100 V	BAV19WS-V-G	I _R			100	nA
	$V_R = 100 \text{ V}, T_j = 100 ^{\circ}\text{C}$	BAV20WS-V-G	I _R			15	μΑ
Leakage current	V _R = 150 V	BAV21WS-V-G	I _R			100	nA
Leakage current	V _R = 150 V, T _j = 100 °C	BAV19WS-V-G	I _R			15	μA
	V _R = 200 V	BAV20WS-V-G	I _R			100	nA
	$V_R = 200 \text{ V}, T_j = 100 ^{\circ}\text{C}$	BAV21WS-V-G	I _R			15	μΑ
Dynamic Forward resistance	I _F = 10 mA		r _f		5		Ω
Diode capacitance	V _R = 0 V, f = 1 MHz		C _D		1.5		pF
Reverse recovery time	I_F = 30 mA, I_R = 30 mA, i_R = 3 mA, R_L = 100 Ω		t _{rr}			50	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

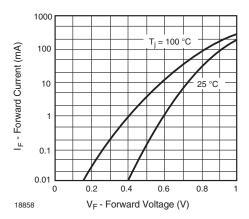


Fig. 1 - Forward Current vs. Forward Voltage

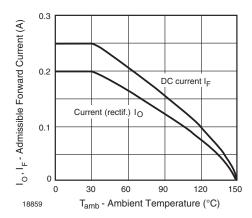


Fig. 2 - Admissible Forward Current vs. Ambient Temperature

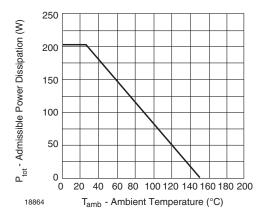


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

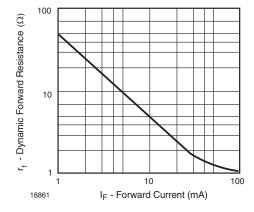


Fig. 4 - Dynamic Forward Resistance vs. Forward Current



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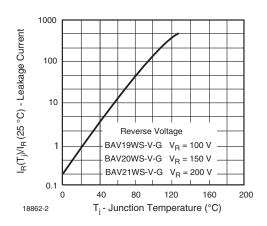


Fig. 5 - Leakage Current vs. Junction Temperature

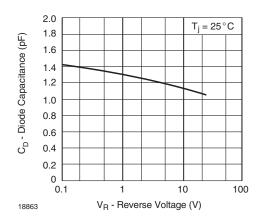
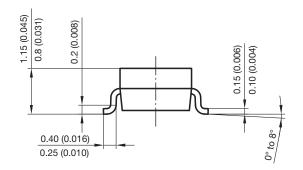
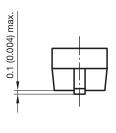
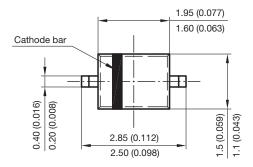


Fig. 6 - Capacitance vs. Reverse Voltage

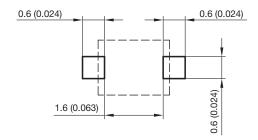
PACKAGE DIMENSIONS in millimeters (inches): SOD-323







Foot print recommendation:



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