

Operational Amplifiers

# Low Voltage Operation Ground Sense Operational Amplifier

TLR342xxx TLR344F

**General Description**

TLR342xxx series and TLR344F are dual and quad CMOS operational amplifier with low supply voltage operation and full swing output. It is suitable for battery-operated equipment. The MOSFET input stage provides low input bias current. It is capable to use for sensor applications.

**Features**

- Low Operating Supply Voltage
- Output Full Swing / Input Ground Sense
- High Large Signal Voltage Gain
- Low Input Bias Current
- Low Supply Current
- Low Input Offset Voltage

**Applications**

- Consumer Electronics
- Buffer
- Sensor Amplifier
- Mobile Equipment
- Battery-Operated Equipment

**Key Specifications**

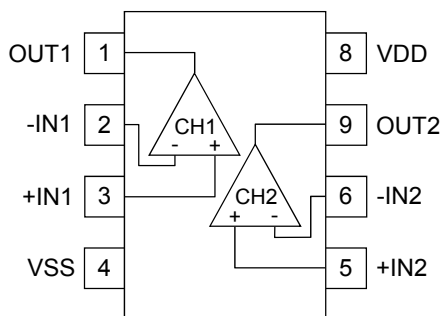
- Operating Supply Voltage (Single Supply): +1.8V to +5.5V
- Supply Current: TLR342xxx 150uA(Typ)  
TLR344F 300uA(Typ)
- Voltage Gain (R<sub>L</sub>=2kΩ): 105dB(Typ)
- Temperature Range: -40°C to +85°C
- Input Offset Voltage: 4mV (Max)
- Input Bias Current: 1pA (Typ)
- Gain Bandwidth: 2.3MHz (Typ)
- Slew Rate: 1V/μs (Typ)

**Packages**

- |           |                                 |
|-----------|---------------------------------|
|           | <b>W(Typ) x D(Typ) x H(Max)</b> |
| SOP8      | 5.00mm x 6.20mm x 1.71mm        |
| SOP-J8    | 4.90mm x 6.00mm x 1.65mm        |
| TSSOP-B8  | 3.00mm x 6.40mm x 1.20mm        |
| TSSOP-B8J | 3.00mm x 4.90mm x 1.10mm        |
| SOP14     | 8.70mm x 6.20mm x 1.71mm        |

**Pin Configuration**

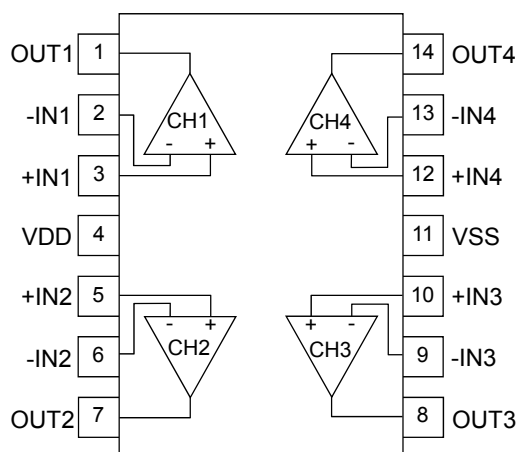
- TLR342F : SOP8  
 TLR342FJ : SOP-J8  
 TLR342FVT : TSSOP-B8  
 TLR342FVJ : TSSOP-B8J



Pin No.	Pin Name
1	OUT1
2	-IN1
3	+IN1
4	VSS
5	+IN2
6	-IN2
7	OUT2
8	VDD

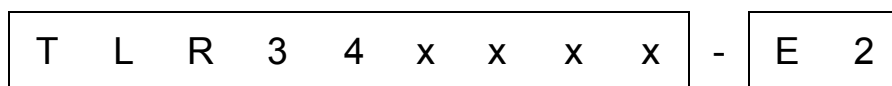
○Product structure : Silicon monolithic integrated circuit ○This product has no designed protection against radioactive rays.

TLR344F : SOP14



Pin No.	Pin Name
1	OUT1
2	-IN1
3	+IN1
4	VDD
5	+IN2
6	-IN2
7	OUT2
8	OUT3
9	-IN3
10	+IN3
11	VSS
12	+IN4
13	-IN4
14	OUT4

**Ordering Information**



Part Number TLR342F TLR342FJ TLR342FVT TLR342FVJ TLR344F	Package F : SOP8 : SOP14 FJ : SOP-J8 FVT : TSSOP-B8 FVJ : TSSOP-B8J	Packaging and forming specification E2: Embossed tape and reel (SOP8, SOP-J8, TSSOP-B8, TSSOP-B8J, SOP14)
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**Line-up**

T <sub>opr</sub>	Channels	Package		Orderable Part Number
-40°C to +85°C	2ch	SOP8	Reel of 2500	TLR342F-E2
		SOP-J8	Reel of 2500	TLR342FJ-E2
		TSSOP-B8	Reel of 2500	TLR342FVT-E2
		TSSOP-B8J	Reel of 2500	TLR342FVJ-E2
	4ch	SOP14	Reel of 2500	TLR344F-E2

Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ )

Parameter	Symbol	Rating		Unit	
		TLR342xxx	TLR344F		
Supply Voltage	$V_{DD}-V_{SS}$	+7		V	
Power Dissipation	$P_d$	SOP8	0.68 <sup>(Note 1,6)</sup>	-	W
		SOP-J8	0.67 <sup>(Note 2,6)</sup>	-	
		TSSOP-B8	0.62 <sup>(Note 3,6)</sup>	-	
		TSSOP-B8J	0.58 <sup>(Note 4,6)</sup>	-	
		SOP14	-	0.56 <sup>(Note 5,6)</sup>	
Differential Input Voltage <sup>(Note 7)</sup>	$V_{ID}$	$V_{DD}$ to $V_{SS}$		V	
Input Common-mode Voltage Range	$V_{ICM}$	$(V_{SS}-0.3)$ to $(V_{DD}+0.3)$		V	
Input Current <sup>(Note 8)</sup>	$I_I$	$\pm 10$		mA	
Operating Voltage	$V_{opr}$	+1.8 to +5.5		V	
Operating Temperature	$T_{opr}$	-40 to +85		$^\circ\text{C}$	
Storage Temperature	$T_{stg}$	-55 to +150		$^\circ\text{C}$	
Maximum Junction Temperature	$T_{jmax}$	+150		$^\circ\text{C}$	

(Note 1) To use at temperature above  $T_A=25^\circ\text{C}$  reduce 5.5mW/ $^\circ\text{C}$ .

(Note 2) To use at temperature above  $T_A=25^\circ\text{C}$  reduce 5.4mW/ $^\circ\text{C}$ .

(Note 3) To use at temperature above  $T_A=25^\circ\text{C}$  reduce 5.0mW/ $^\circ\text{C}$ .

(Note 4) To use at temperature above  $T_A=25^\circ\text{C}$  reduce 4.7mW/ $^\circ\text{C}$ .

(Note 5) To use at temperature above  $T_A=25^\circ\text{C}$  reduce 4.5mW/ $^\circ\text{C}$ .

(Note 6) Mounted on a FR4 glass epoxy PCB (70mm×70mm×1.6mm).

(Note 7) The voltage difference between inverting input and non-inverting input is the differential input voltage.

The input pin voltage is set to more than  $V_{SS}$ .

(Note 8) An excessive input current will flow when input voltages of more than  $V_{DD}+0.6\text{V}$  or less than  $V_{SS}-0.6\text{V}$  are applied.

The input current can be set to less than the rated current by adding a limiting resistor.

Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

## Electrical Characteristics

OTLR342xxx (Unless otherwise specified  $V_{DD}=+1.8V$ ,  $V_{SS}=0V$ )

Parameter	Symbol	Temperature Range	Limit			Unit	Conditions
			Min	Typ	Max		
Input Offset Voltage <sup>(Note 9,10)</sup>	$V_{IO}$	25°C	-	0.3	4	mV	-
		Full Range	-	-	4.5		
Input Offset Voltage Drift <sup>(Note 9,10)</sup>	$\Delta V_{IO}/\Delta T$	Full Range	-	1.9	-	$\mu V/^\circ C$	-
Input Bias Current <sup>(Note 9)</sup>	$I_B$	25°C	-	1	100	pA	-
Input Offset Current <sup>(Note 9)</sup>	$I_{IO}$	25°C	-	1	-	pA	-
Supply Current	$I_{DD}$	25°C	-	150	300	$\mu A$	-
		Full Range	-	-	400		
Common-mode Rejection Ratio	CMRR	25°C	60	85	-	dB	$V_{ICM}=0V$ to 0.7V
Power Supply Rejection Ratio	PSRR	25°C	75	95	-	dB	$V_{DD}=1.8V$ to 5.0V
Input Common-mode Voltage Range	$V_{ICM}$	25°C	0	-	0.8	V	CMRR $\geq$ 60 dB
Large Signal Voltage Gain	$A_v$	25°C	70	110	-	dB	$R_L=10k\Omega$ , $V_{RL}=0.9V$
			65	100	-		$R_L=2k\Omega$ , $V_{RL}=0.9V$
Maximum Output Voltage(High)	$V_{OH}$	25°C	$V_{DD}-0.05$	$V_{DD}-0.03$	-	V	$R_L=2k\Omega$ , $V_{RL}=0.9V$
			$V_{DD}-0.02$	$V_{DD}-0.01$	-		$R_L=10k\Omega$ , $V_{RL}=0.9V$
Maximum Output Voltage(Low)	$V_{OL}$	25°C	-	0.022	0.055	V	$R_L=2k\Omega$ , $V_{RL}=0.9V$
			-	0.014	0.02		$R_L=10k\Omega$ , $V_{RL}=0.9V$
Output Source Current <sup>(Note 11)</sup>	$I_{SOURCE}$	25°C	6	8	-	mA	$V_{OUT}=0V$ , short current
Output Sink Current <sup>(Note 11)</sup>	$I_{SINK}$	25°C	10	13	-	mA	$V_{OUT}=1.8V$ , short current
Slew Rate	SR	25°C	-	1	-	V/ $\mu s$	$R_L=10k\Omega$ , $V_{+IN}=0.7V_{P-P}$
Gain Bandwidth	GBW	25°C	-	2.2	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Unity Gain Frequency	$f_t$	25°C	-	1.2	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Phase Margin	$\theta_M$	25°C	-	55	-	deg	$C_L=20pF$ , $R_L=100k\Omega$
Gain Margin	GM	25°C	-	7	-	dB	$C_L=20pF$ , $R_L=100k\Omega$
Input Referred Noise Voltage	$V_N$	25°C	-	33	-	$nV/\sqrt{Hz}$	$f=1kHz$
Total Harmonic Distortion + Noise	THD+N	25°C	-	0.015	-	%	$f=1kHz$ , $R_L=600\Omega$ $A_V=0dB$ , DIN-AUDIO
Channel Separation	CS	25°C	-	110	-	dB	$A_V=40dB$ , $V_{OUT}=1V_{rms}$

(Note 9) Absolute value

(Note 10) Full Range:  $T_A=-40^\circ C$  to  $+85^\circ C$ 

(Note 11) Consider the power dissipation of the IC under high temperature environment when selecting the output current value.

There may be a case where the output current value is reduced due to the rise in IC temperature caused by the heat generated inside the IC.

## Electrical Characteristics – continued

OTLR342xxx (Unless otherwise specified  $V_{DD}=+5V$ ,  $V_{SS}=0V$ )

Parameter	Symbol	Temperature Range	Limit			Unit	Conditions
			Min	Typ	Max		
Input Offset Voltage <sup>(Note 12,13)</sup>	$V_{IO}$	25°C	-	0.3	4	mV	-
		Full Range	-	-	4.5		
Input Offset Voltage Drift <sup>(Note 12,13)</sup>	$\Delta V_{IO}/\Delta T$	Full Range	-	1.9	-	$\mu V/^{\circ}C$	-
Input Bias Current <sup>(Note 12)</sup>	$I_B$	25°C	-	1	200	pA	-
Input Offset Current <sup>(Note 12)</sup>	$I_{IO}$	25°C	-	1	-	pA	-
Supply Current	$I_{DD}$	25°C	-	150	300	$\mu A$	-
		Full Range	-	-	400		
Common-mode Rejection Ratio	CMRR	25°C	75	90	-	dB	$V_{ICM}=0V$ to 3.9V
Power Supply Rejection Ratio	PSRR	25°C	75	95	-	dB	$V_{DD}=1.8V$ to 5.0V
Input Common-mode Voltage Range	$V_{ICM}$	25°C	0	-	4.0	V	CMRR $\geq 70$ dB
Large Signal Voltage Gain	$A_v$	25°C	80	110	-	dB	$R_L=10k\Omega$ , $V_{RL}=2.5V$
			75	105	-		$R_L=2k\Omega$ , $V_{RL}=2.5V$
Maximum Output Voltage(High)	$V_{OH}$	25°C	$V_{DD}-0.06$	$V_{DD}-0.03$	-	V	$R_L=2k\Omega$ , $V_{RL}=2.5V$
			$V_{DD}-0.02$	$V_{DD}-0.01$	-		$R_L=10k\Omega$ , $V_{RL}=2.5V$
Maximum Output Voltage(Low)	$V_{OL}$	25°C	-	0.04	0.06	V	$R_L=2k\Omega$ , $V_{RL}=2.5V$
			-	0.02	0.03		$R_L=10k\Omega$ , $V_{RL}=2.5V$
Output Source Current <sup>(Note 14)</sup>	$I_{SOURCE}$	25°C	60	100	-	mA	$V_{OUT}=0V$ , short current
Output Sink Current <sup>(Note 14)</sup>	$I_{SINK}$	25°C	80	120	-	mA	$V_{OUT}=5V$ , short current
Slew Rate	SR	25°C	-	1	-	V/ $\mu s$	$R_L=10k\Omega$ , $V_{+IN}=2V_{P-P}$
Gain Bandwidth	GBW	25°C	-	2.3	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Unity Gain Frequency	$f_t$	25°C	-	1.3	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Phase Margin	$\theta_M$	25°C	-	55	-	deg	$C_L=20pF$ , $R_L=100k\Omega$
Gain Margin	GM	25°C	-	7	-	dB	$C_L=20pF$ , $R_L=100k\Omega$
Input Referred Noise Voltage	$V_N$	25°C	-	33	-	$nV/\sqrt{Hz}$	$f=1kHz$
Total Harmonic Distortion + Noise	THD+N	25°C	-	0.012	-	%	$V_{+IN}=1V_{P-P}$ , $f=1kHz$ $R_L=600\Omega$ , $A_v=0dB$ , DIN-AUDIO
Channel Separation	CS	25°C	-	110	-	dB	$A_v=40dB$ , $V_{OUT}=1V_{rms}$

(Note 12) Absolute value

(Note 13) Full Range:  $T_A=-40^{\circ}C$  to  $+85^{\circ}C$ 

(Note 14) Consider the power dissipation of the IC under high temperature environment when selecting the output current value.

There may be a case where the output current value is reduced due to the rise in IC temperature caused by the heat generated inside the IC.

## Electrical Characteristics – continued

OTLR344F (Unless otherwise specified  $V_{DD}=+1.8V$ ,  $V_{SS}=0V$ )

Parameter	Symbol	Temperature Range	Limit			Unit	Conditions
			Min	Typ	Max		
Input Offset Voltage <sup>(Note 15,16)</sup>	$V_{IO}$	25°C	-	0.3	4	mV	-
		Full Range	-	-	4.5		
Input Offset Voltage Drift <sup>(Note 15,16)</sup>	$\Delta V_{IO}/\Delta T$	Full Range	-	1.9	-	$\mu V/^\circ C$	-
Input Bias Current <sup>(Note 15)</sup>	$I_B$	25°C	-	1	100	pA	-
Input Offset Current <sup>(Note 15)</sup>	$I_{IO}$	25°C	-	1	-	pA	-
Supply Current	$I_{DD}$	25°C	-	280	600	$\mu A$	-
		Full Range	-	-	800		
Common-mode Rejection Ratio	CMRR	25°C	60	85	-	dB	$V_{ICM}=0V$ to 0.7V
Power Supply Rejection Ratio	PSRR	25°C	75	95	-	dB	$V_{DD}=1.8V$ to 5.0V
Input Common-mode Voltage Range	$V_{ICM}$	25°C	0	-	0.8	V	CMRR $\geq$ 60 dB
Large Signal Voltage Gain	$A_v$	25°C	70	110	-	dB	$R_L=10k\Omega$ , $V_{RL}=0.9V$
			65	100	-		$R_L=2k\Omega$ , $V_{RL}=0.9V$
Maximum Output Voltage(High)	$V_{OH}$	25°C	$V_{DD}-0.05$	$V_{DD}-0.03$	-	V	$R_L=2k\Omega$ , $V_{RL}=0.9V$
			$V_{DD}-0.02$	$V_{DD}-0.01$	-		$R_L=10k\Omega$ , $V_{RL}=0.9V$
Maximum Output Voltage(Low)	$V_{OL}$	25°C	-	0.022	0.055	V	$R_L=2k\Omega$ , $V_{RL}=0.9V$
			-	0.014	0.02		$R_L=10k\Omega$ , $V_{RL}=0.9V$
Output Source Current <sup>(Note 17)</sup>	$I_{SOURCE}$	25°C	6	8	-	mA	$V_{OUT}=0V$ , short current
Output Sink Current <sup>(Note 17)</sup>	$I_{SINK}$	25°C	10	13	-	mA	$V_{OUT}=1.8V$ , short current
Slew Rate	SR	25°C	-	1	-	V/ $\mu s$	$R_L=10k\Omega$ , $V_{+IN}=0.7V_{P-P}$
Gain Bandwidth	GBW	25°C	-	2.2	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Unity Gain Frequency	$f_t$	25°C	-	1.2	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Phase Margin	$\theta_M$	25°C	-	55	-	deg	$C_L=20pF$ , $R_L=100k\Omega$
Gain Margin	GM	25°C	-	7	-	dB	$C_L=20pF$ , $R_L=100k\Omega$
Input Referred Noise Voltage	$V_N$	25°C	-	33	-	$nV/\sqrt{Hz}$	$f=1kHz$
Total Harmonic Distortion + Noise	THD+N	25°C	-	0.015	-	%	$f=1kHz$ , $R_L=600\Omega$ $A_v=0dB$ , DIN-AUDIO
Channel Separation	CS	25°C	-	110	-	dB	$A_v=40dB$ , $V_{OUT}=1V_{rms}$

(Note 15) Absolute value

(Note 16) Full Range:  $T_A=-40^\circ C$  to  $+85^\circ C$ 

(Note 17) Consider the power dissipation of the IC under high temperature environment when selecting the output current value.

There may be a case where the output current value is reduced due to the rise in IC temperature caused by the heat generated inside the IC.

## Electrical Characteristics – continued

OTLR344F (Unless otherwise specified  $V_{DD}=+5V$ ,  $V_{SS}=0V$ )

Parameter	Symbol	Temperature Range	Limit			Unit	Conditions
			Min	Typ	Max		
Input Offset Voltage <sup>(Note 18,19)</sup>	$V_{IO}$	25°C	-	0.3	4	mV	-
		Full Range	-	-	4.5		
Input Offset Voltage Drift <sup>(Note 18,19)</sup>	$\Delta V_{IO}/\Delta T$	Full Range	-	1.9	-	$\mu V/^\circ C$	-
Input Bias Current <sup>(Note 18)</sup>	$I_B$	25°C	-	1	200	pA	-
Input offset current <sup>(Note 18)</sup>	$I_{IO}$	25°C	-	1	-	pA	-
Supply current	$I_{DD}$	25°C	-	300	600	$\mu A$	-
		Full Range	-	-	800		
Common Mode Rejection Ratio	CMRR	25°C	75	90	-	dB	$V_{ICM}=0V$ to 3.9V
Power Supply Rejection Ratio	PSRR	25°C	75	95	-	dB	$V_{DD}=1.8V$ to 5.0V
Input Common Mode Voltage Range	$V_{ICM}$	25°C	0	-	4.0	V	CMRR $\geq 70$ dB
Large Signal Voltage Gain	$A_v$	25°C	80	110	-	dB	$R_L=10k\Omega$ , $V_{RL}=2.5V$
			75	105	-		$R_L=2k\Omega$ , $V_{RL}=2.5V$
Maximum Output Voltage(High)	$V_{OH}$	25°C	$V_{DD}-0.06$	$V_{DD}-0.03$	-	V	$R_L=2k\Omega$ , $V_{RL}=2.5V$
			$V_{DD}-0.02$	$V_{DD}-0.01$	-		$R_L=10k\Omega$ , $V_{RL}=2.5V$
Maximum Output Voltage(Low)	$V_{OL}$	25°C	-	0.04	0.06	V	$R_L=2k\Omega$ , $V_{RL}=2.5V$
			-	0.02	0.03		$R_L=10k\Omega$ , $V_{RL}=2.5V$
Output Source Current <sup>(Note 20)</sup>	$I_{SOURCE}$	25°C	60	100	-	mA	$V_{OUT}=0V$ , short current
Output Sink Current <sup>(Note 20)</sup>	$I_{SINK}$	25°C	80	120	-	mA	$V_{OUT}=5V$ , short current
Slew Rate	SR	25°C	-	1	-	V/ $\mu s$	$R_L=10k\Omega$ , $V_{+IN}=2V_{P-P}$
Gain Bandwidth	GBW	25°C	-	2.3	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Unity Gain Frequency	$f_t$	25°C	-	1.3	-	MHz	$C_L=200pF$ , $R_L=100k\Omega$
Phase Margin	$\theta_M$	25°C	-	55	-	deg	$C_L=20pF$ , $R_L=100k\Omega$
Gain Margin	GM	25°C	-	7	-	dB	$C_L=20pF$ , $R_L=100k\Omega$
Input Referred Noise Voltage	$V_N$	25°C	-	33	-	$nV/\sqrt{Hz}$	$f=1kHz$
Total Harmonic Distortion + Noise	THD+N	25°C	-	0.012	-	%	$V_{+IN}=1V_{P-P}$ , $f=1kHz$ $R_L=600\Omega$ , $A_v=0dB$ , DIN-AUDIO
Channel Separation	CS	25°C	-	110	-	dB	$A_v=40dB$ , $V_{OUT}=1V_{rms}$

(Note 18) Absolute value

(Note 19) Full Range:  $T_A=-40^\circ C$  to  $+85^\circ C$ 

(Note 20) Consider the power dissipation of the IC under high temperature environment when selecting the output current value.

There may be a case where the output current value is reduced due to the rise in IC temperature caused by the heat generated inside the IC.

## Description of Electrical Characteristics

Described below are descriptions of the relevant electrical terms used in this datasheet. Items and symbols used are also shown. Note that item name and symbol and their meaning may differ from those on another manufacturer's document or general document.

### 1. Absolute maximum ratings

Absolute maximum rating items indicate the condition which must not be exceeded. Application of voltage in excess of absolute maximum rating or use out of absolute maximum rated temperature environment may cause deterioration of characteristics.

- (1) Supply Voltage ( $V_{DD}/V_{SS}$ )  
Indicates the maximum voltage that can be applied between the VDD pin and VSS pin without deterioration or destruction of characteristics of internal circuit.
- (2) Differential Input Voltage ( $V_{ID}$ )  
Indicates the maximum voltage that can be applied between non-inverting and inverting pins without damaging the IC.
- (3) Input Common-mode Voltage Range ( $V_{ICM}$ )  
Indicates the maximum voltage that can be applied to the non-inverting and inverting pins without deterioration or destruction of electrical characteristics. Input common-mode voltage range of the maximum ratings does not assure normal operation of IC. For normal operation, use the IC within the input common-mode voltage range characteristics.
- (4) Power Dissipation ( $P_d$ )  
Indicates the power that can be consumed by the IC when mounted on a specific board at the ambient temperature 25°C (normal temperature). As for package product,  $P_d$  is determined by the temperature that can be permitted by the IC in the package (maximum junction temperature) and the thermal resistance of the package.

### 2. Electrical characteristics

- (1) Input Offset Voltage ( $V_{IO}$ )  
Indicates the voltage difference between non-inverting pin and inverting pins. It can be translated into the input voltage difference required for setting the output voltage at 0V.
- (2) Input Offset Voltage drift ( $\Delta V_{IO}/\Delta T$ )  
Denotes the ratio of the input offset voltage fluctuation to the ambient temperature fluctuation.
- (3) Input Offset Current ( $I_{IO}$ )  
Indicates the difference of input bias current between the non-inverting and inverting pins.
- (4) Input Bias Current ( $I_B$ )  
Indicates the current that flows into or out of the input pin. It is defined by the average of input bias currents at the non-inverting and inverting pins.
- (5) Supply Current ( $I_{DD}$ )  
Indicates the current that flows within the IC under specified no-load conditions.
- (6) Maximum Output Voltage(High) / Maximum Output Voltage(Low) ( $V_{OH}/V_{OL}$ )  
Indicates the voltage range of the output under specified load condition. It is typically divided into maximum output voltage high and low. Maximum output voltage high indicates the upper limit of output voltage. Maximum output voltage low indicates the lower limit.
- (7) Large Signal Voltage Gain ( $A_V$ )  
Indicates the amplifying rate (gain) of output voltage against the voltage difference between non-inverting pin and inverting pin. It is normally the amplifying rate (gain) with reference to DC voltage.  
 $A_V = (\text{Output Voltage}) / (\text{Differential Input Voltage})$
- (8) Input Common-mode Voltage Range ( $V_{ICM}$ )  
Indicates the input voltage range where IC normally operates.
- (9) Common-mode Rejection Ratio (CMRR)  
Indicates the ratio of fluctuation of input offset voltage when the input common-mode voltage is changed. It is normally the fluctuation of DC.  
 $CMRR = (\text{Change of Input Common-mode voltage}) / (\text{Input offset fluctuation})$
- (10) Power Supply Rejection Ratio (PSRR)  
Indicates the ratio of fluctuation of input offset voltage when supply voltage is changed.  
It is normally the fluctuation of DC.  
 $PSRR = (\text{Change of power supply voltage}) / (\text{Input offset fluctuation})$
- (11) Output Source Current/ Output Sink Current ( $I_{SOURCE} / I_{SINK}$ )  
The maximum current that can be output from the IC under specific output conditions. The output source current indicates the current flowing out from the IC, and the output sink current indicates the current flowing into the IC.
- (12) Slew Rate (SR)  
Indicates the ratio of the change in output voltage with time when a step input signal is applied.
- (13) Unity Gain Frequency ( $f_T$ )  
Indicates a frequency where the voltage gain of operational amplifier is 1.
- (14) Gain Bandwidth (GBW)  
The product of the open-loop voltage gain and the frequency at which the voltage gain decreases 6dB/octave.
- (15) Phase Margin ( $\theta_M$ )  
Indicates the margin of phase from 180 degree phase lag at unity gain frequency.



## (16) Gain Margin (GM)

Indicates the difference between 0dB and the gain where operational amplifier has 180 degree phase delay.

(17) Input Referred Noise Voltage ( $V_N$ )

Indicates a noise voltage generated inside the operational amplifier equivalent by ideal voltage source connected in series with input pin.

## (18) Total Harmonic Distortion + Noise (THD+N)

Indicates the fluctuation of input offset voltage or that of output voltage with reference to the change of output voltage of driven channel.

Typical Performance Curves

○ TLR342xxx

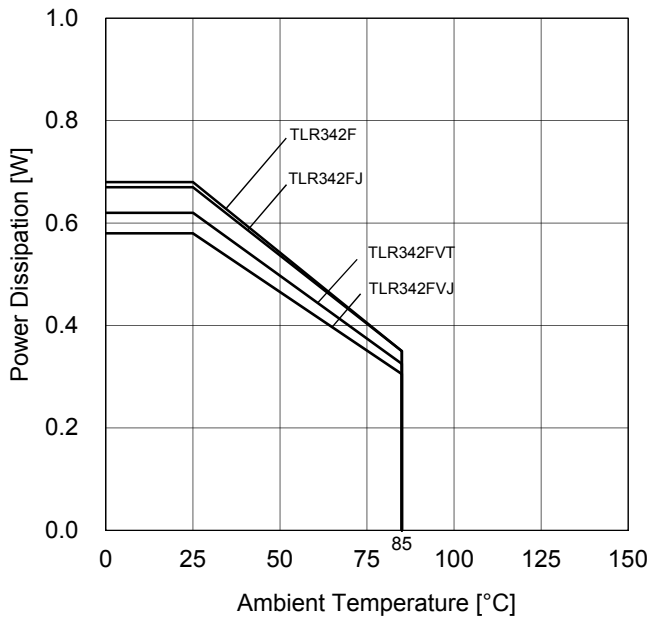


Figure 1. Power Dissipation vs Ambient Temperature (Derating Curve)

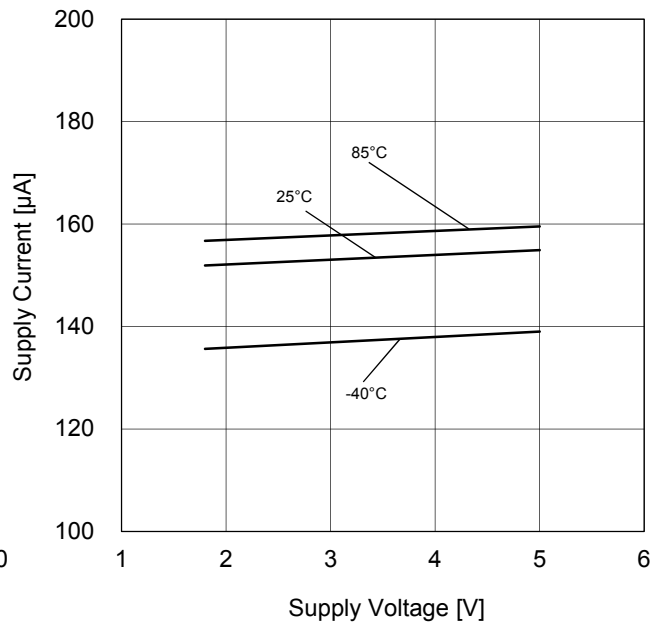


Figure 2. Supply Current vs Supply Voltage

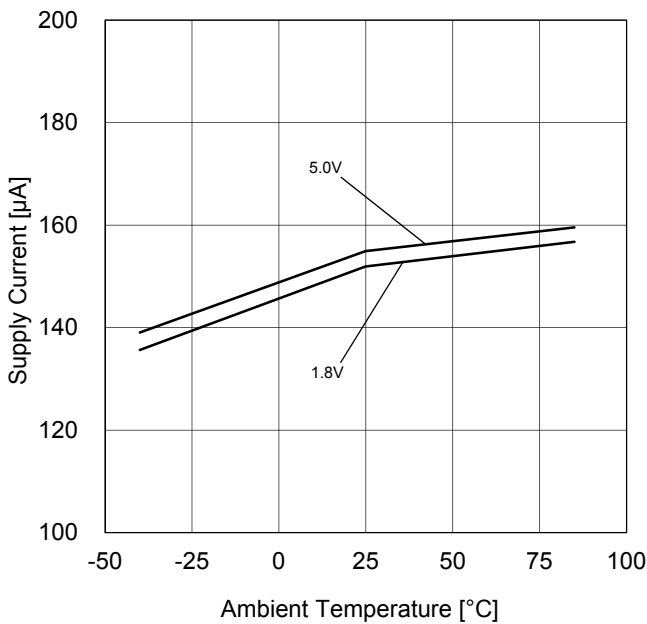


Figure 3. Supply Current vs Ambient Temperature

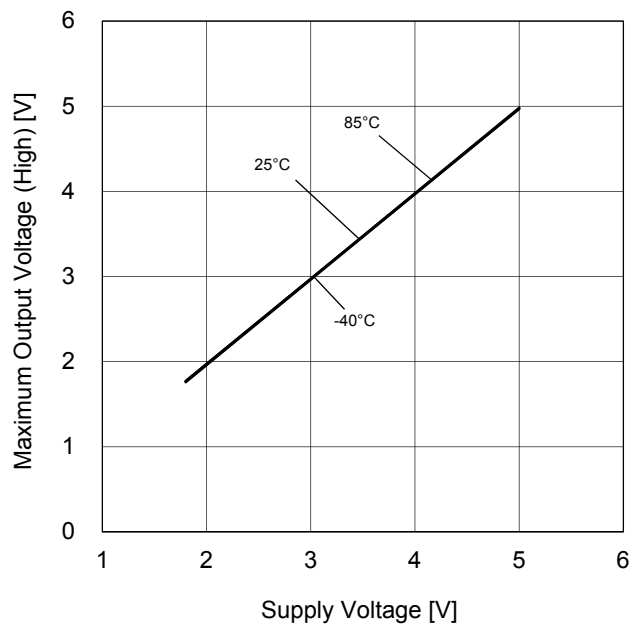


Figure 4. Maximum Output Voltage High vs Supply Voltage ( $R_L=2k\Omega$ )

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR342xxx

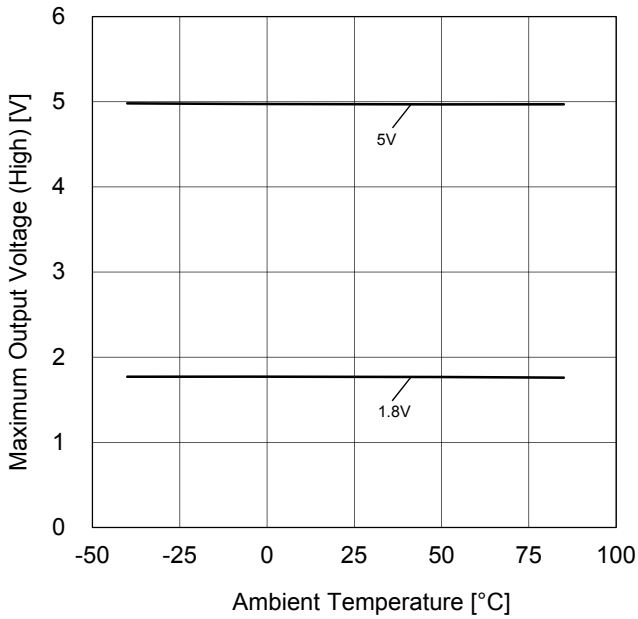


Figure 5. Maximum Output Voltage High vs Ambient Temperature ( $R_L=2k\Omega$ )

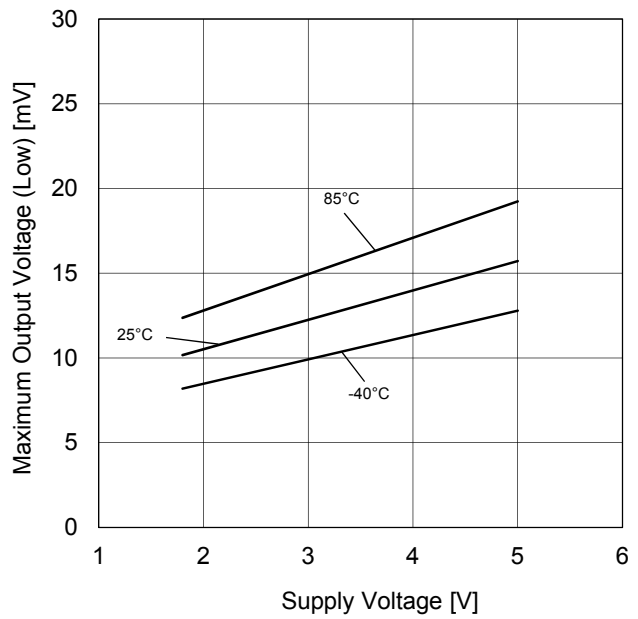


Figure 6. Maximum Output Voltage Low vs Supply Voltage ( $R_L=2k\Omega$ )

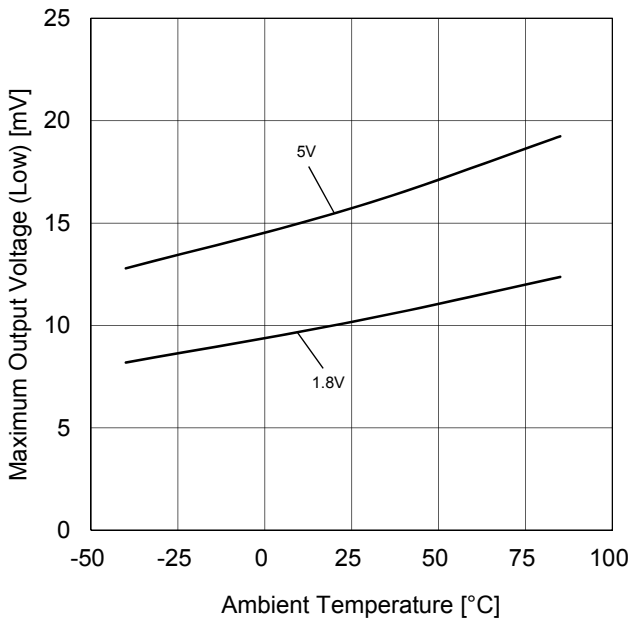


Figure 7. Maximum Output Voltage (Low) vs Ambient Temperature ( $R_L=2k\Omega$ )

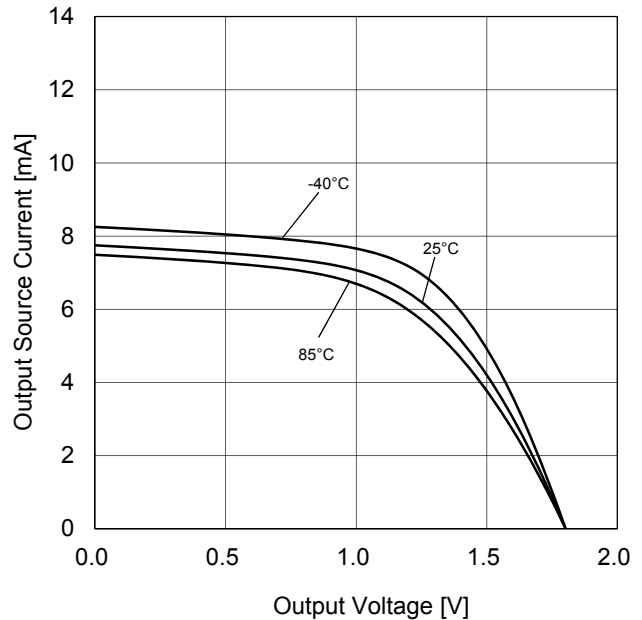


Figure 8. Output Source Current vs Output Voltage ( $V_{DD}=1.8V$ )

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR342xxx

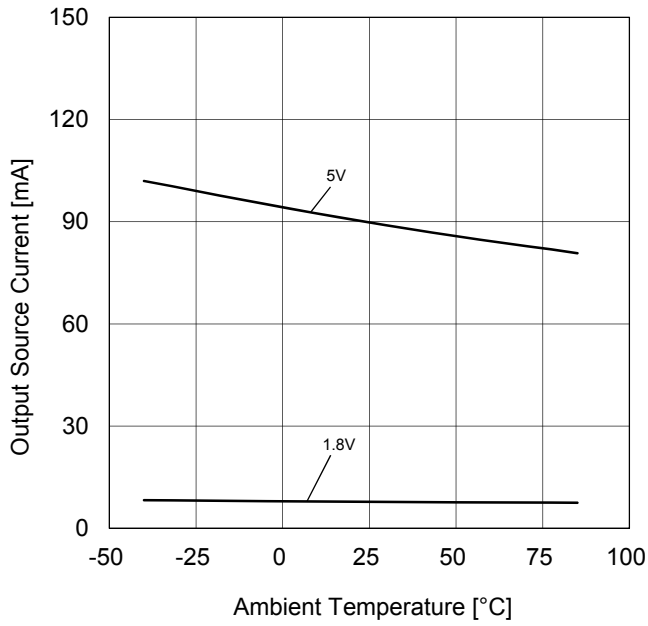


Figure 9. Output Source Current vs Ambient Temperature ( $V_{OUT}=0V$ )

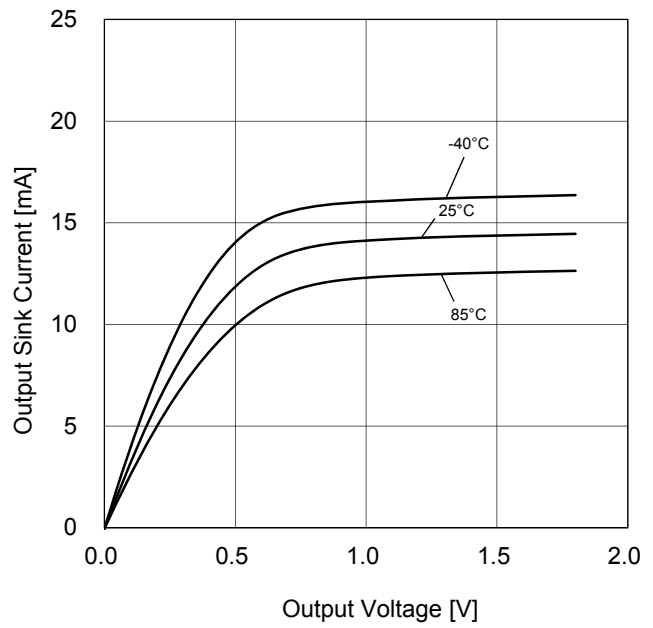


Figure 10. Output Sink Current vs Output Voltage ( $V_{DD}=1.8V$ )

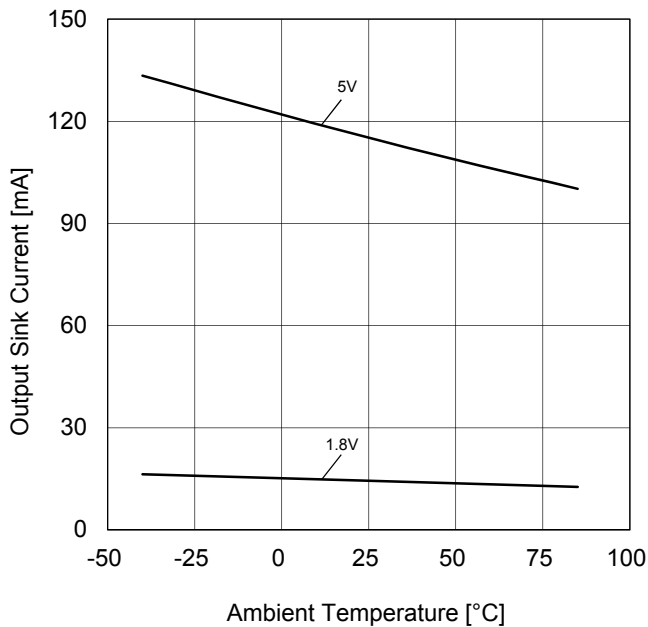


Figure 11. Output Sink Current vs Ambient Temperature ( $V_{OUT}=V_{DD}$ )

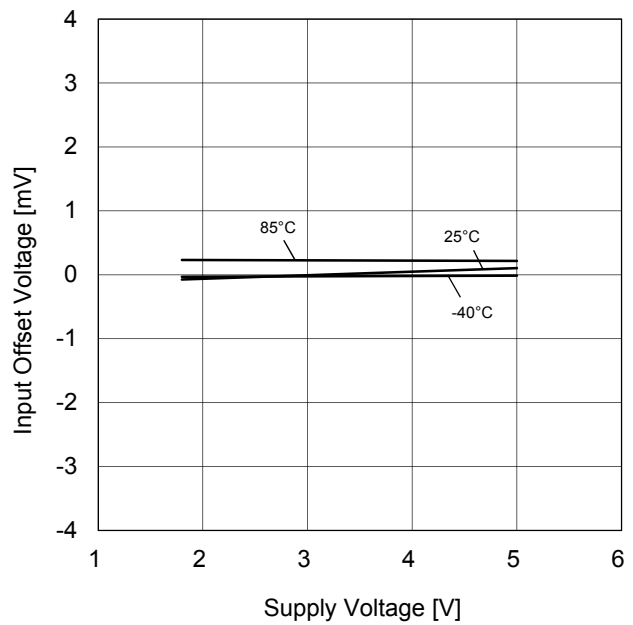


Figure 12. Input Offset Voltage vs Supply Voltage

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR342xxx

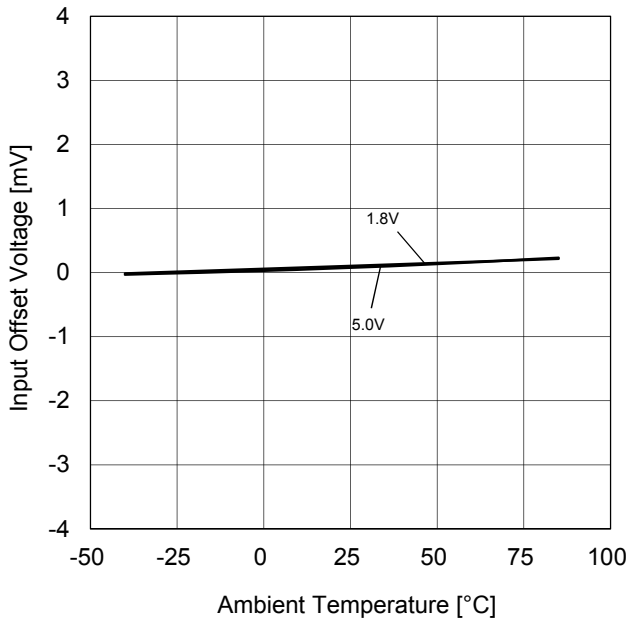


Figure 13. Input Offset Voltage vs Ambient Temperature

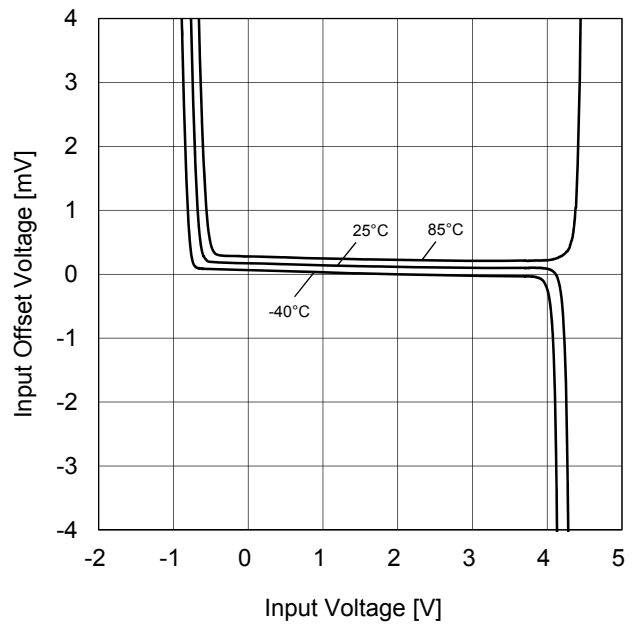


Figure 14. Input Offset Voltage vs Input Voltage ( $V_{DD}=5V$ )

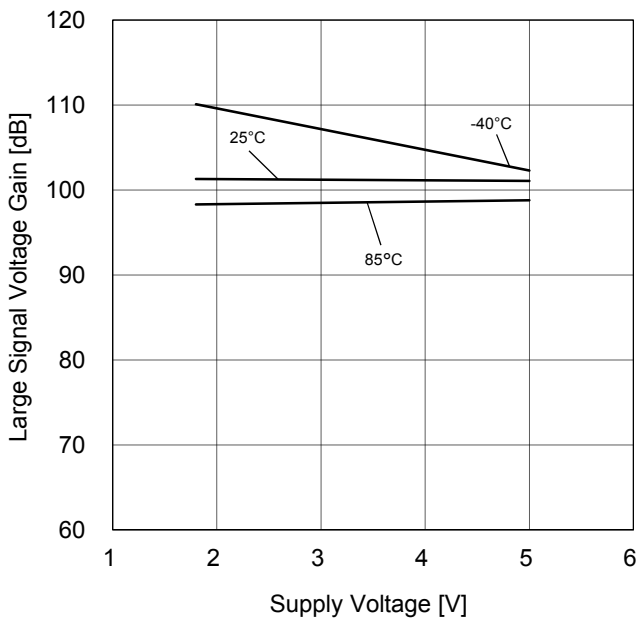


Figure 15. Large Signal Voltage Gain vs Supply Voltage ( $R_L=2k\Omega$ )

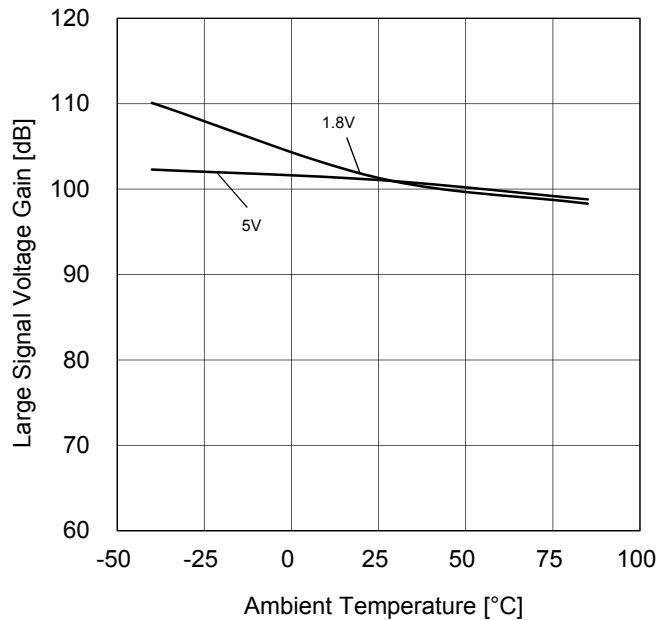


Figure 16. Large Signal Voltage Gain vs Ambient Temperature ( $R_L=2k\Omega$ )

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR342xxx

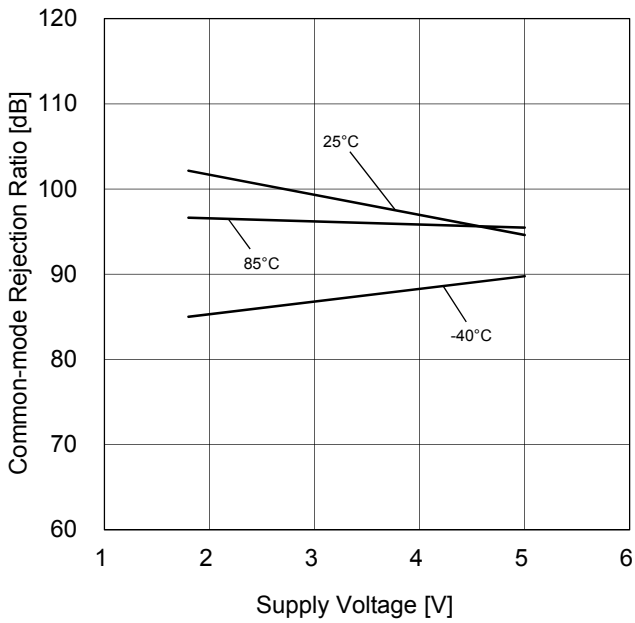


Figure 17. Common-mode Rejection Ratio vs Supply Voltage ( $V_{DD}=1.8V$ )

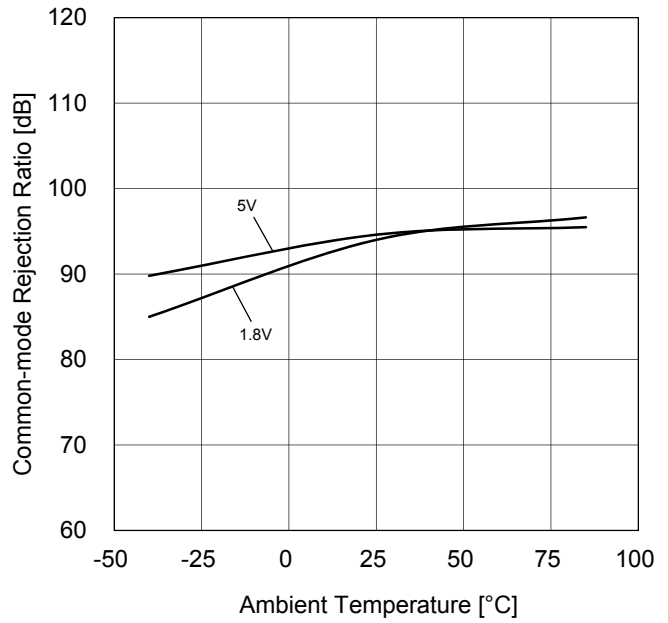


Figure 18. Common-mode Rejection Ratio vs Ambient Temperature

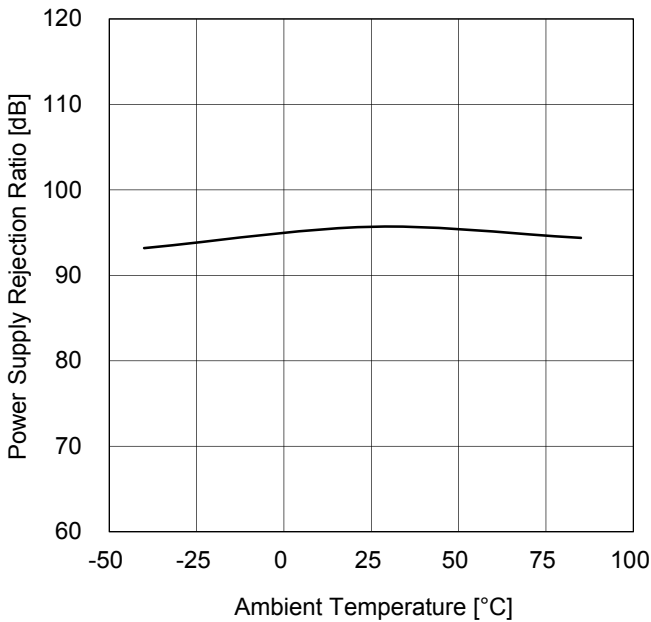


Figure 19. Power Supply Rejection Ratio vs Ambient Temperature ( $V_{DD}=1.8V$  to  $5.0V$ )

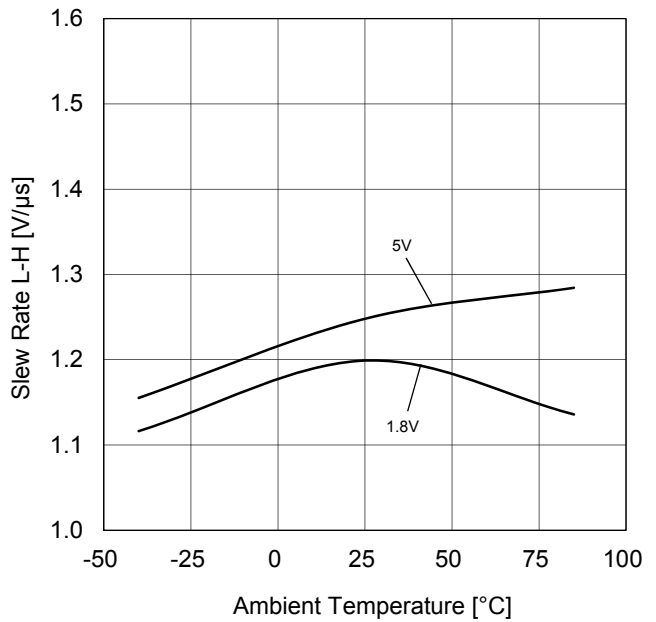


Figure 20. Slew Rate L-H vs Ambient Temperature

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR342xxx

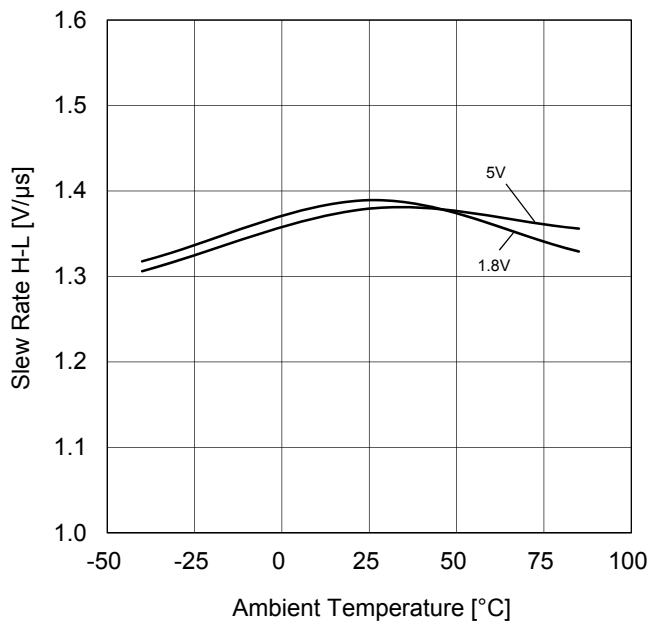


Figure 21. Slew Rate H-L vs Ambient Temperature

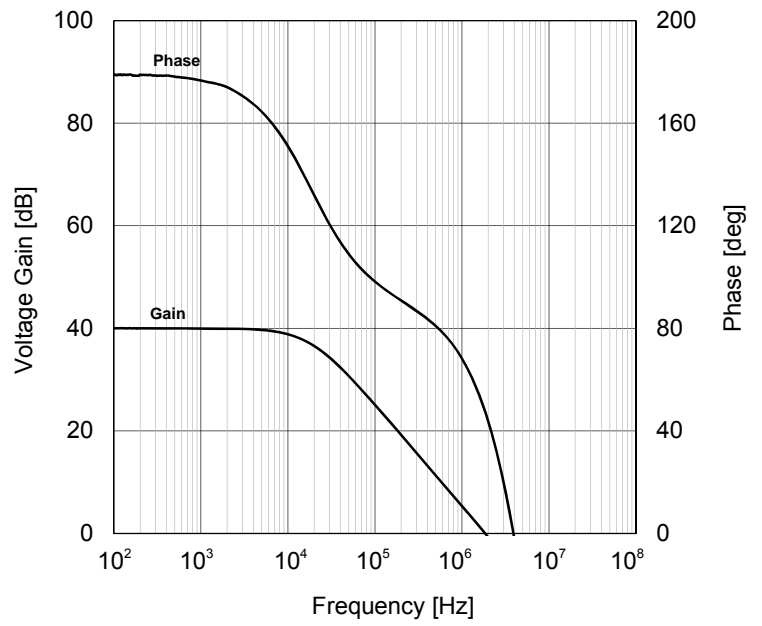


Figure 22. Voltage Gain, Phase vs Frequency (V<sub>DD</sub>=1.8V, T<sub>A</sub>=25°C)

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR344F

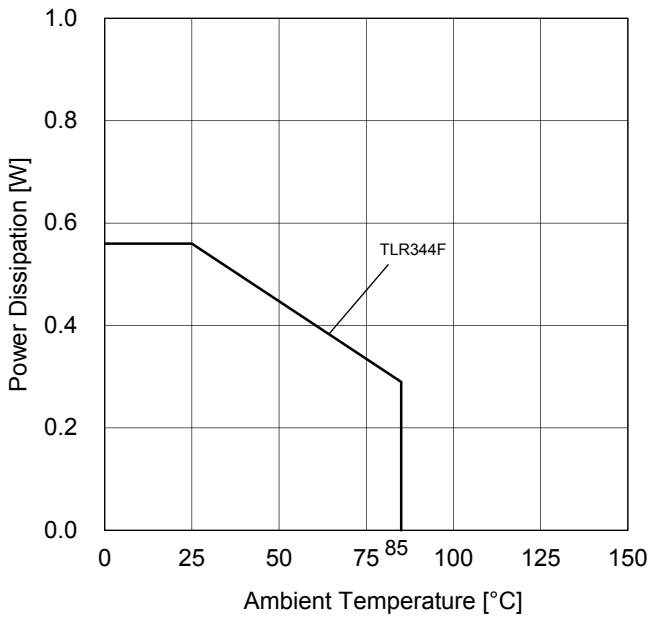


Figure 23. Power Dissipation vs Ambient Temperature (Derating Curve)

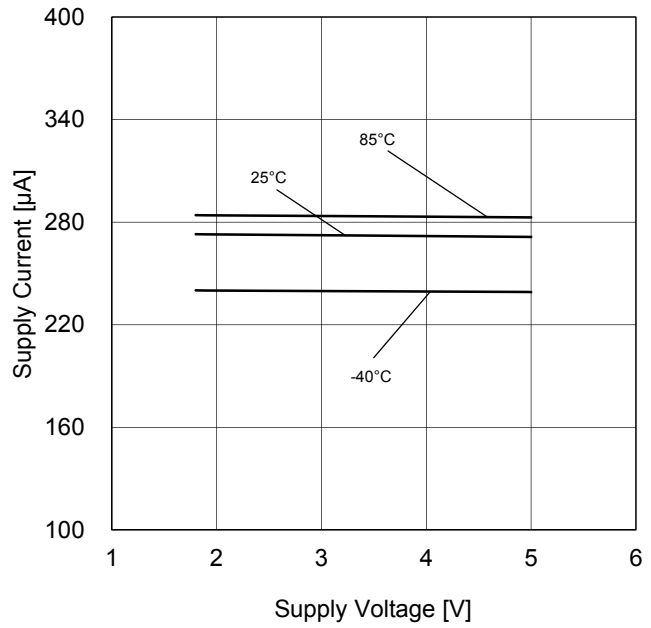


Figure 24. Supply Current vs Supply Voltage

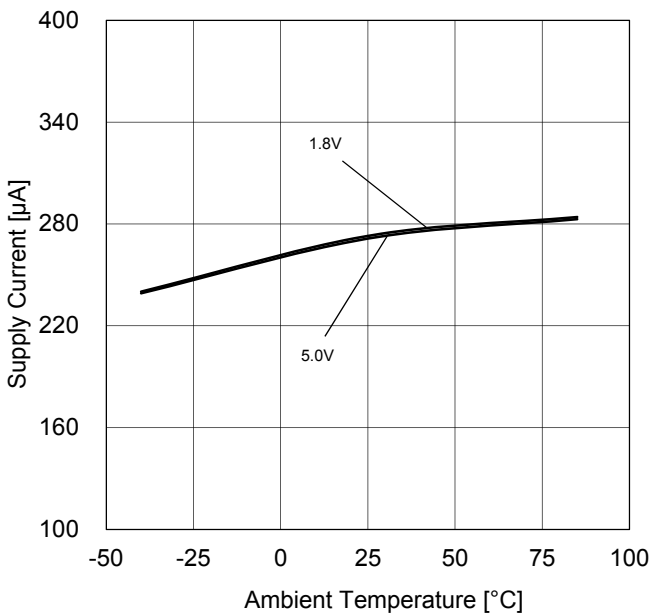


Figure 25. Supply Current vs Ambient Temperature

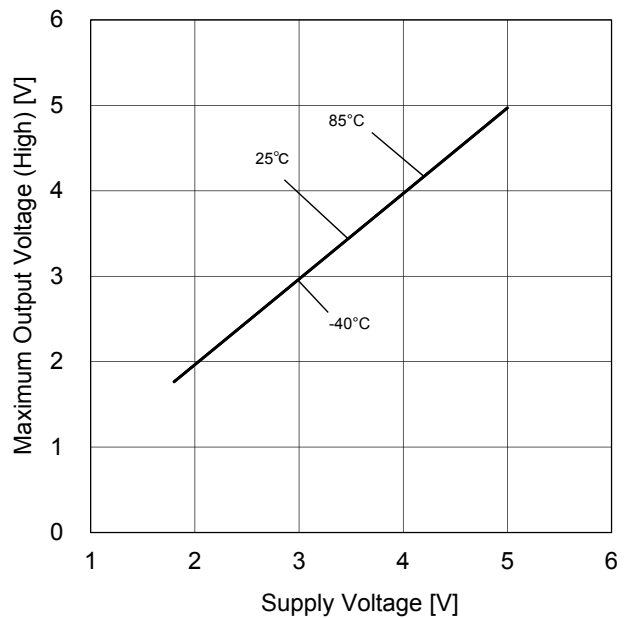


Figure 26. Maximum Output Voltage High vs Supply Voltage (RL=2kΩ)

(\*The data above is measurement value of typical sample, it is not guaranteed.



Typical Performance Curves – continued

○ TLR344F

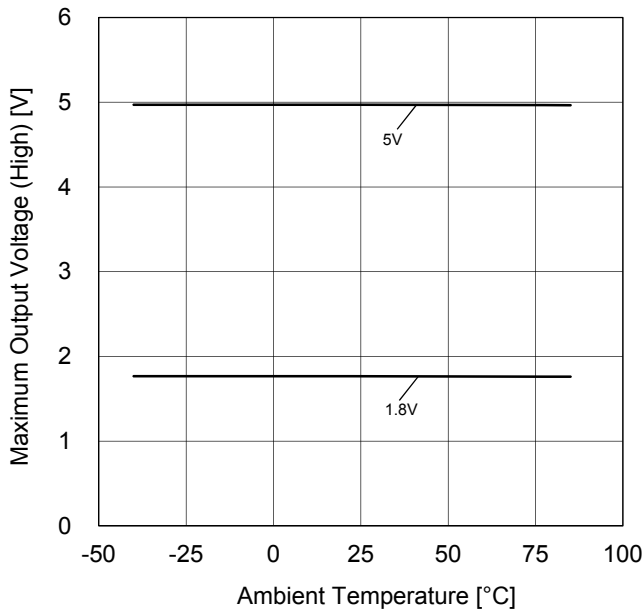


Figure 27. Maximum Output Voltage High vs Ambient Temperature ( $R_L=2k\Omega$ )

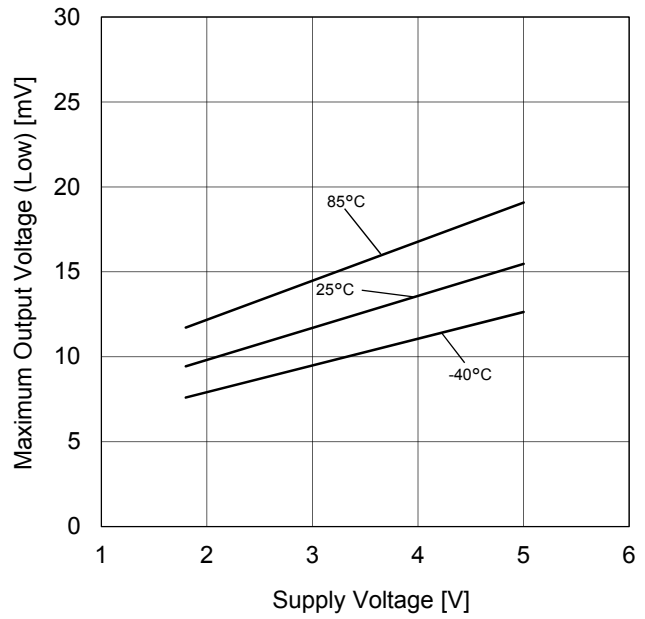


Figure 28. Maximum Output Voltage Low vs Supply Voltage ( $R_L=2k\Omega$ )

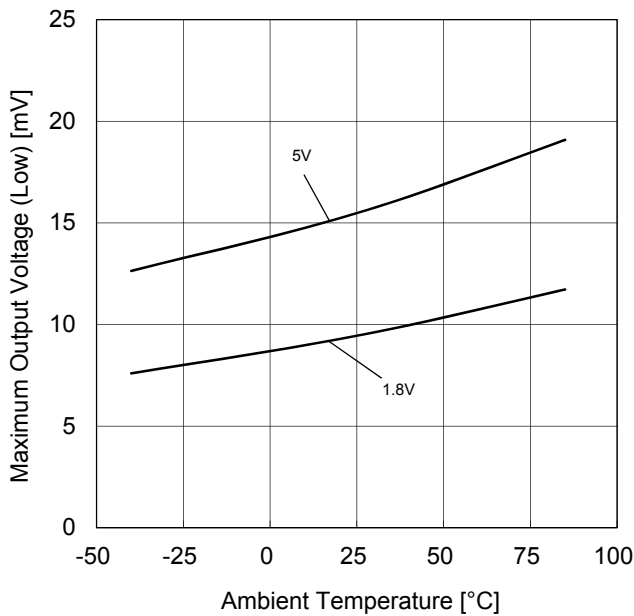


Figure 29. Maximum Output Voltage (Low) vs Ambient Temperature ( $R_L=2k\Omega$ )

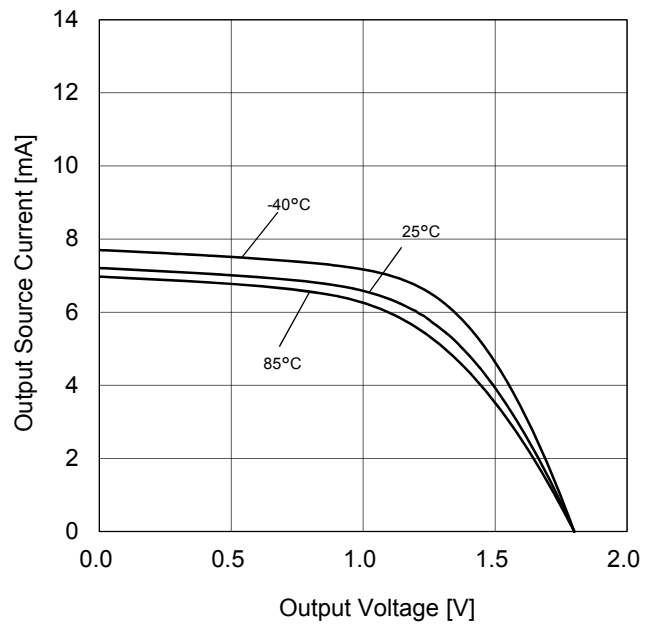


Figure 30. Output Source Current vs Output Voltage ( $V_{DD}=1.8V$ )

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR344F

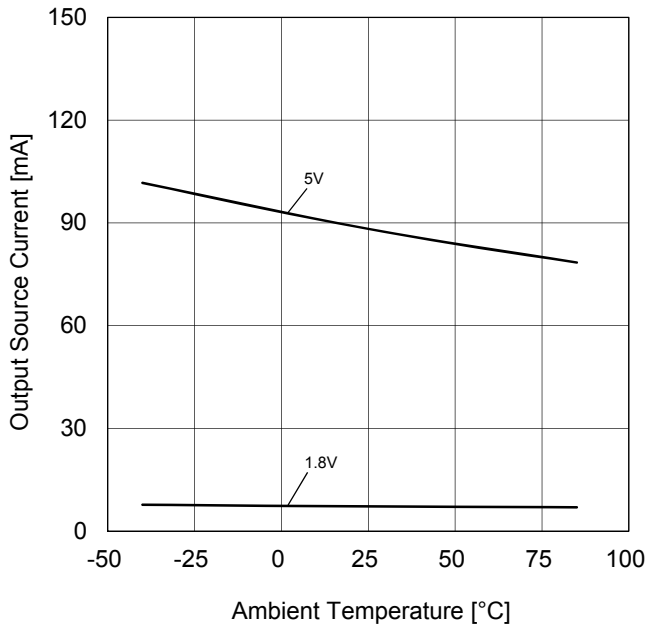


Figure 31. Output Source Current vs Ambient Temperature ( $V_{OUT}=0V$ )

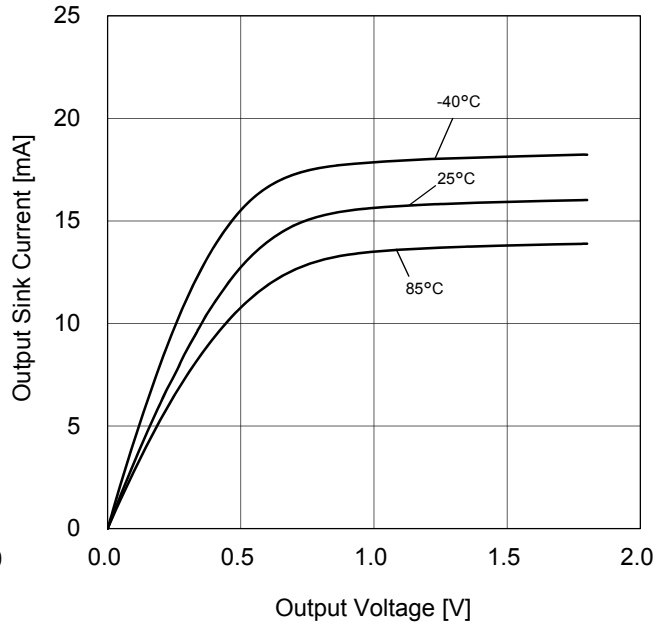


Figure 32. Output Sink Current vs Output Voltage ( $V_{DD}=1.8V$ )

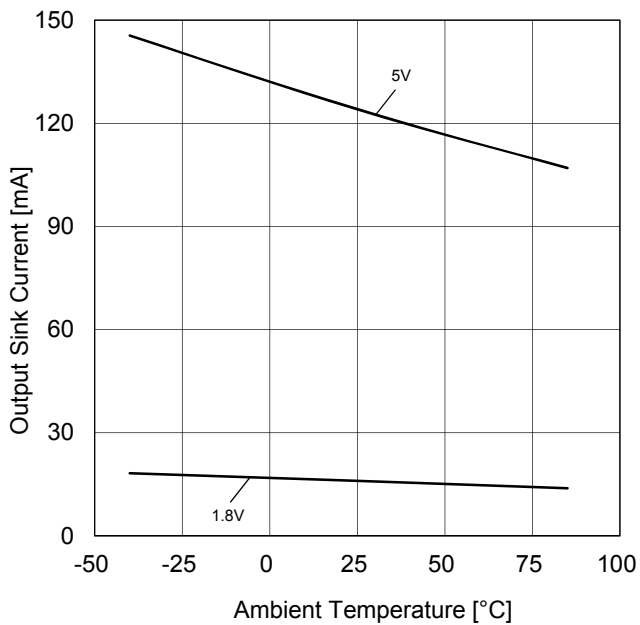


Figure 33. Output Sink Current vs Ambient Temperature ( $V_{OUT}=V_{DD}$ )

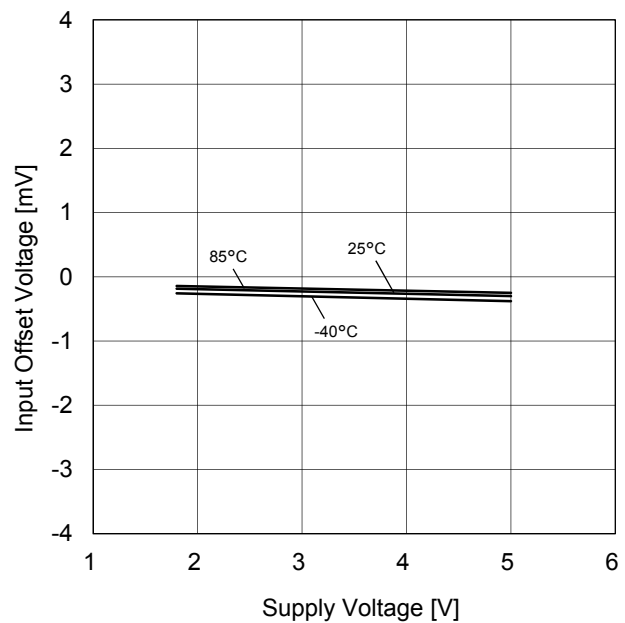


Figure 34. Input Offset Voltage vs Supply Voltage

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR344F

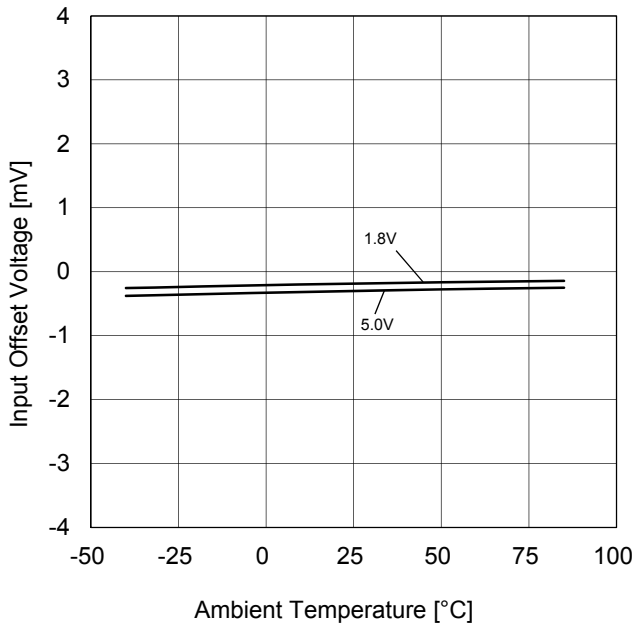


Figure 35. Input Offset Voltage vs Ambient Temperature

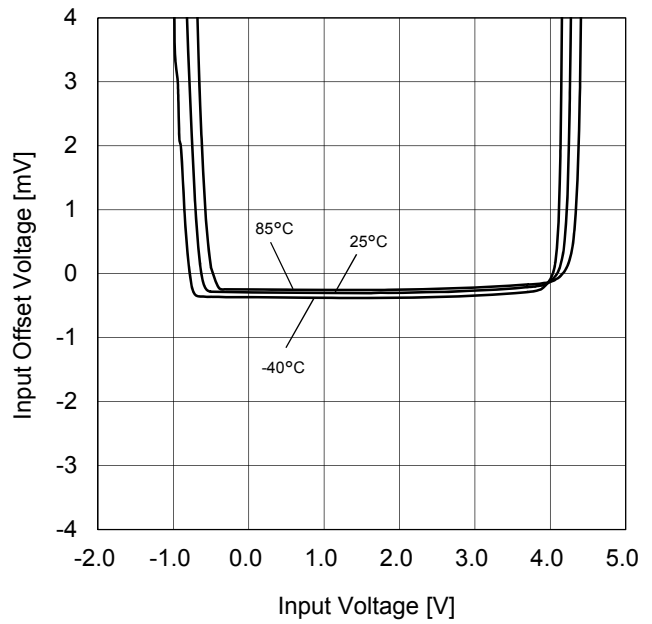


Figure 36. Input Offset Voltage vs Input Voltage (V<sub>DD</sub>=5V)

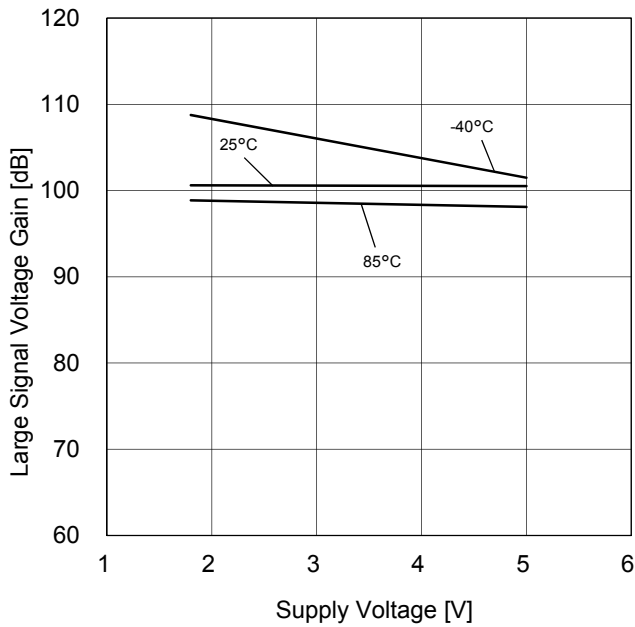


Figure 37. Large Signal Voltage Gain vs Supply Voltage (R<sub>L</sub>=2kΩ)

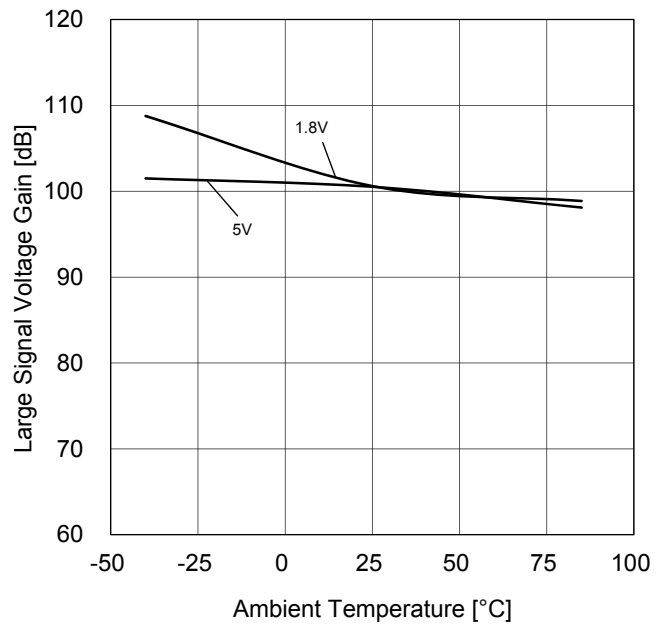


Figure 38. Large Signal Voltage Gain vs Ambient Temperature (R<sub>L</sub>=2kΩ)

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR344F

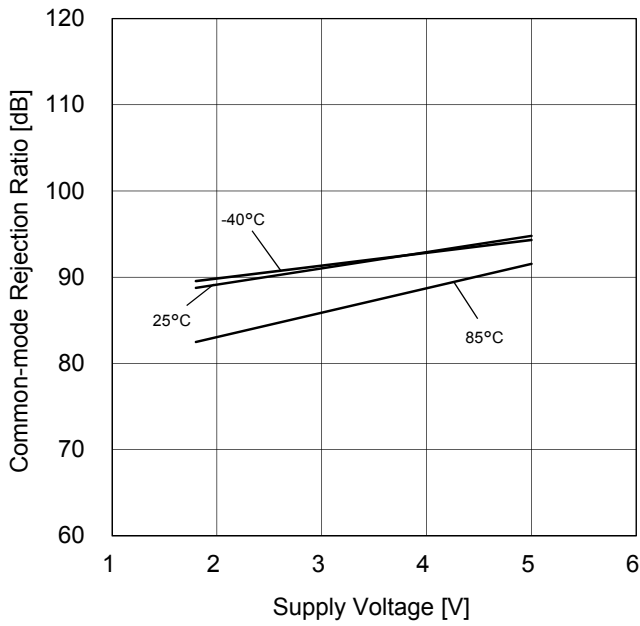


Figure 39. Common-mode Rejection Ratio vs Supply Voltage

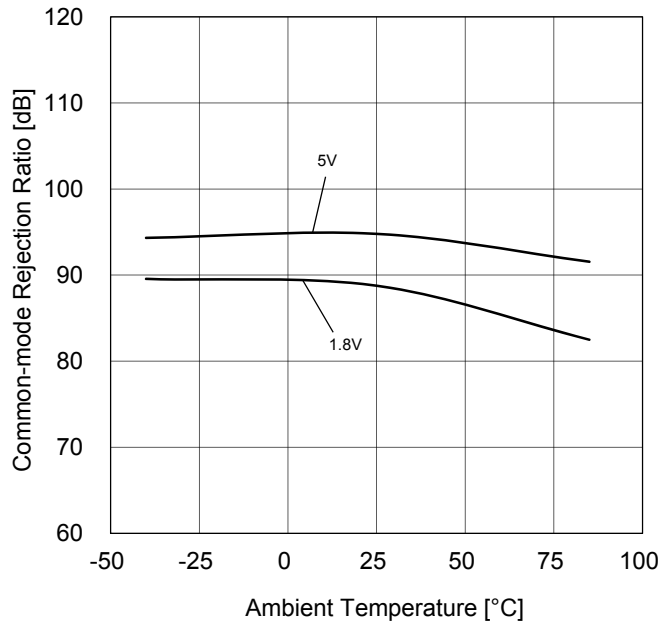


Figure 40. Common-mode Rejection Ratio vs Ambient Temperature

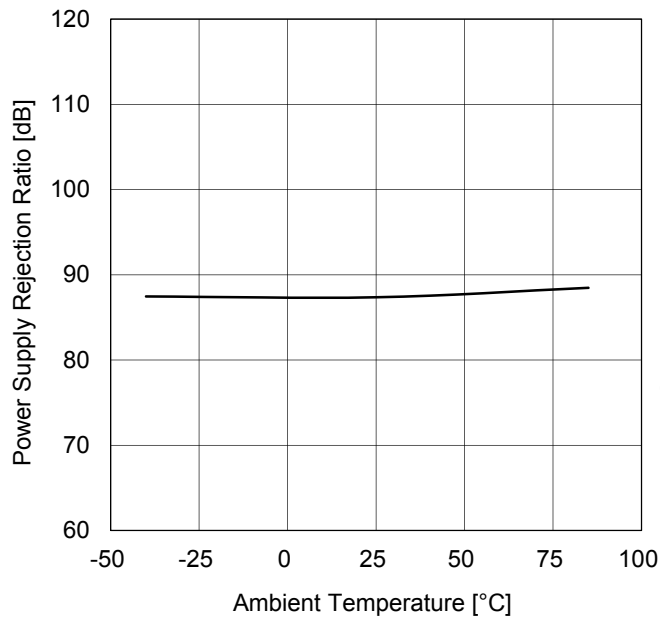


Figure 41. Power Supply Rejection Ratio vs Ambient Temperature (V<sub>DD</sub>=1.8V to 5.0V)

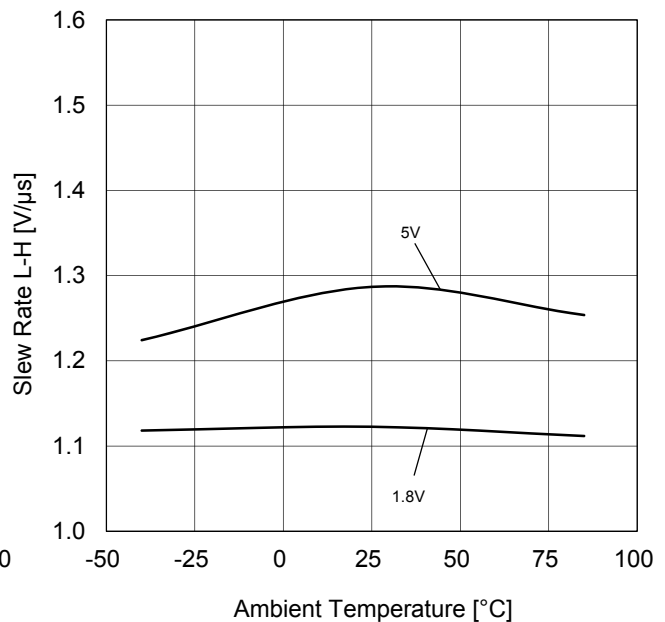


Figure 42. Slew Rate L-H vs Ambient Temperature

(\*The data above is measurement value of typical sample, it is not guaranteed.

Typical Performance Curves – continued

○ TLR344F

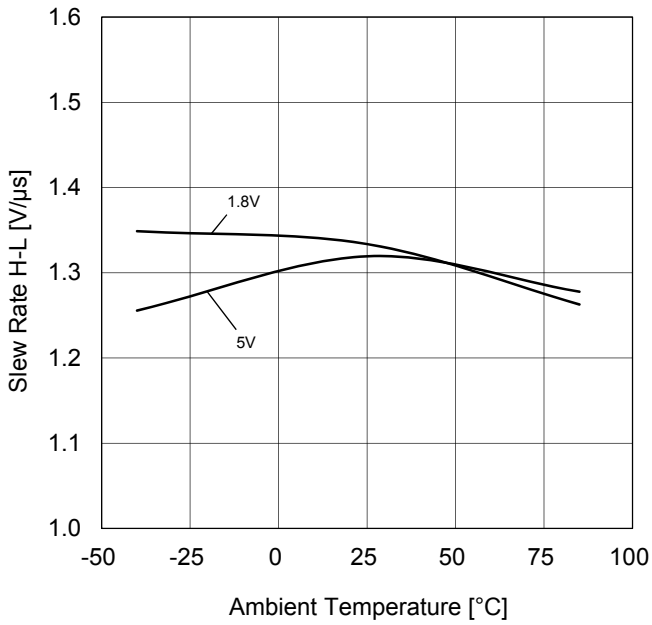


Figure 43. Slew Rate H-L vs Ambient Temperature

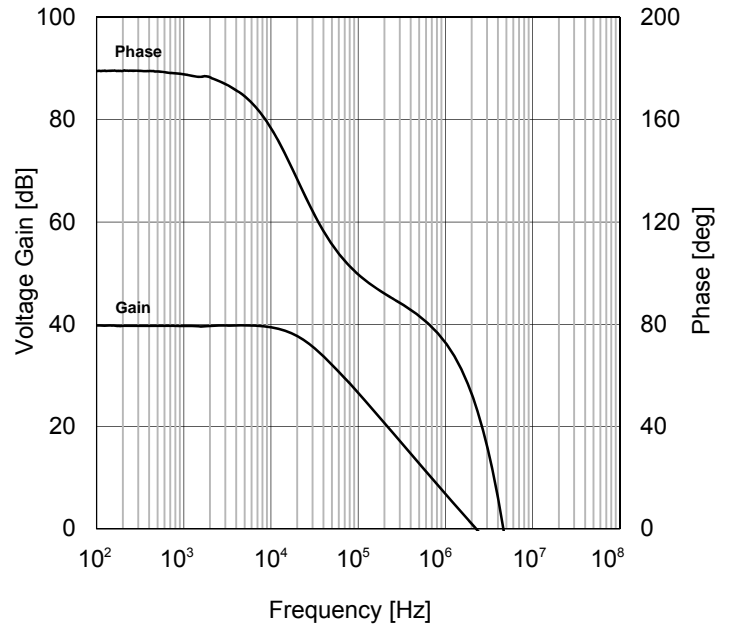


Figure 44. Voltage Gain, Phase vs Frequency (V<sub>DD</sub>=1.8V, T<sub>A</sub>=25°C)

(\*The data above is measurement value of typical sample, it is not guaranteed.

**Application Information**  
**NULL method condition for Test Circuit 1**

VDD, VSS, E<sub>K</sub>, V<sub>ICM</sub> Unit:V

Parameter	V <sub>F</sub>	SW1	SW2	SW3	VDD	VSS	E <sub>K</sub>	V <sub>ICM</sub>	Calculation
Input Offset Voltage	V <sub>F1</sub>	ON	ON	OFF	5	0	-2.5	2.5	1
Large Signal Voltage Gain	V <sub>F2</sub>	ON	ON	ON	5	0	-0.5	2.5	2
	V <sub>F3</sub>						-3.5		
Common-mode Rejection Ratio (Input Common-mode Voltage Range)	V <sub>F4</sub>	ON	ON	OFF	5	0	-2.5	0	3
	V <sub>F5</sub>						-2.5	3	
Power Supply Rejection Ratio	V <sub>F6</sub>	ON	ON	OFF	1.8	0	-0.9	0.5	4
	V <sub>F7</sub>				5				

– Calculation –

- Input Offset Voltage (V<sub>IO</sub>) 
$$V_{IO} = \frac{|V_{F1}|}{1 + R_F/R_S} \quad [V]$$
- Large Signal Voltage Gain (A<sub>V</sub>) 
$$A_v = 20\text{Log} \frac{\Delta E_K \times (1 + R_F/R_S)}{|V_{F2} - V_{F3}|} \quad [\text{dB}]$$
- Common-mode Rejection Ratio (CMRR) 
$$\text{CMRR} = 20\text{Log} \frac{\Delta V_{ICM} \times (1 + R_F/R_S)}{|V_{F4} - V_{F5}|} \quad [\text{dB}]$$
- Power Supply Rejection Ratio (PSRR) 
$$\text{PSRR} = 20\text{Log} \frac{\Delta V_{DD} \times (1 + R_F/R_S)}{|V_{F6} - V_{F7}|} \quad [\text{dB}]$$

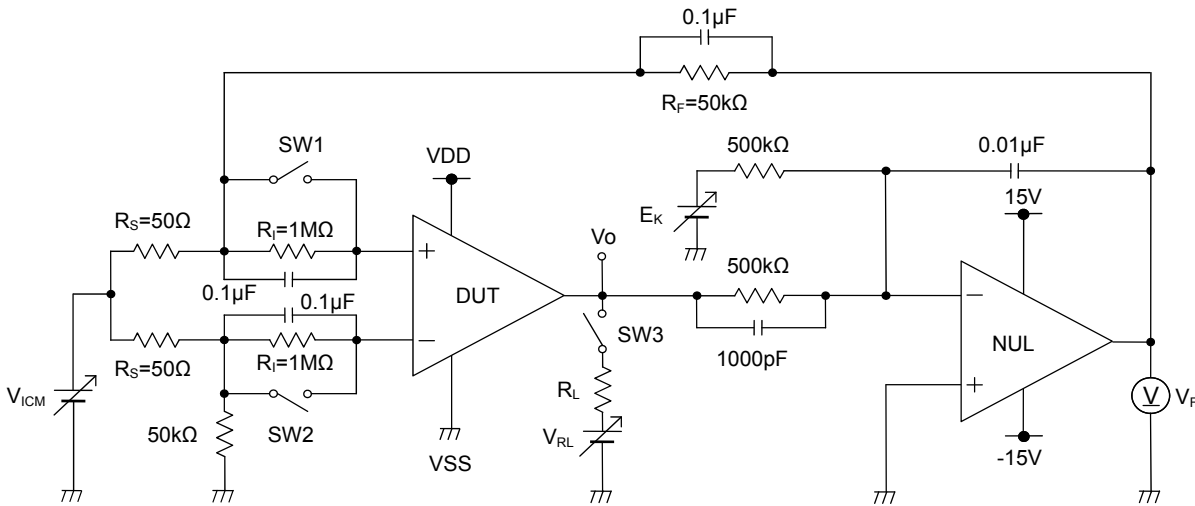


Figure 45. Test Circuit 1

Application Information – continued  
Switch Condition for Test Circuit 2

SW No.	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW9	SW10	SW11	SW12
Supply Current	OFF	OFF	ON	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF
Maximum Output Voltage $R_L=10k\Omega$	OFF	ON	OFF	OFF	ON	OFF	OFF	ON	OFF	OFF	ON	OFF
Output Current	OFF	ON	OFF	OFF	ON	OFF	OFF	OFF	OFF	ON	OFF	OFF
Slew Rate	OFF	OFF	ON	OFF	OFF	OFF	ON	OFF	ON	OFF	OFF	ON
Unit gain frequency	ON	OFF	OFF	ON	ON	OFF	OFF	OFF	ON	OFF	OFF	ON

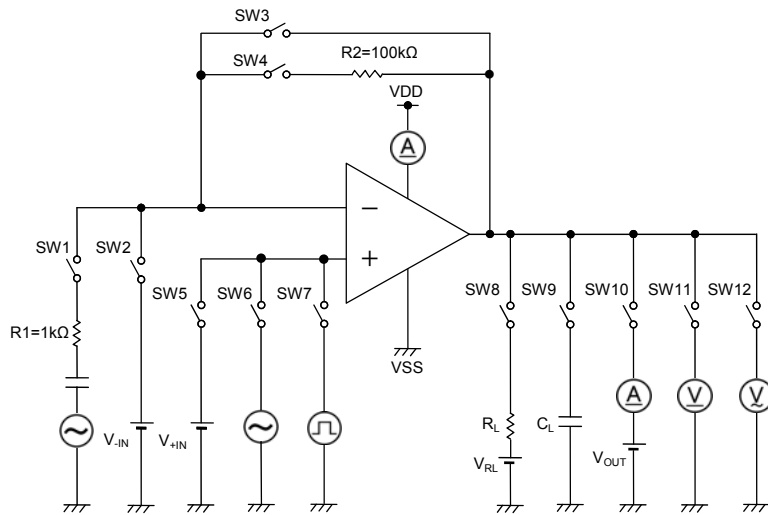


Figure 46. Test Circuit 2

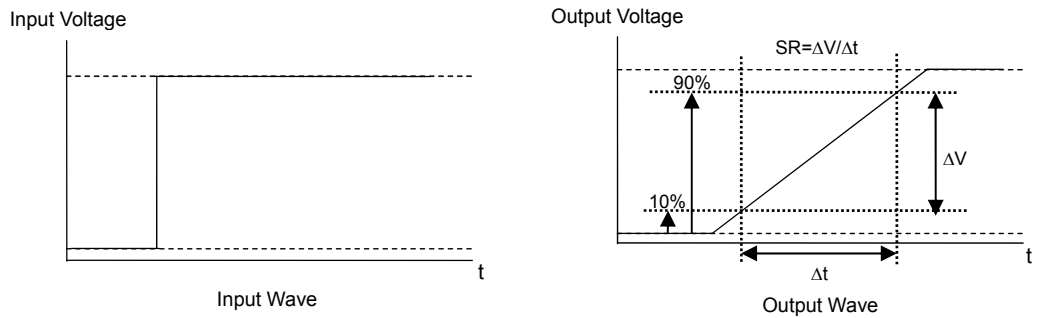


Figure 47. Slew Rate Input and Output Wave

**Application example**

○Voltage follower

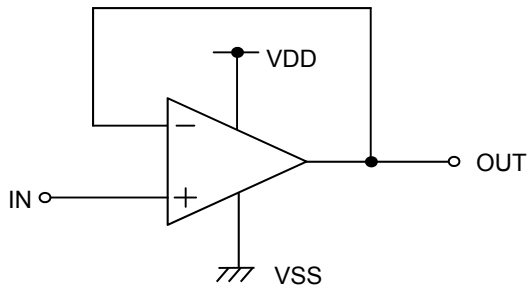


Figure 48. Voltage follower Circuit

Voltage gain is 0dB.

Using this circuit, the output voltage (OUT) is configured to be equal to the input voltage (IN). This circuit also stabilizes the output voltage (OUT) due to high input impedance and low output impedance. Computation for output voltage (OUT) is shown below.

$$OUT=IN$$

○Inverting amplifier

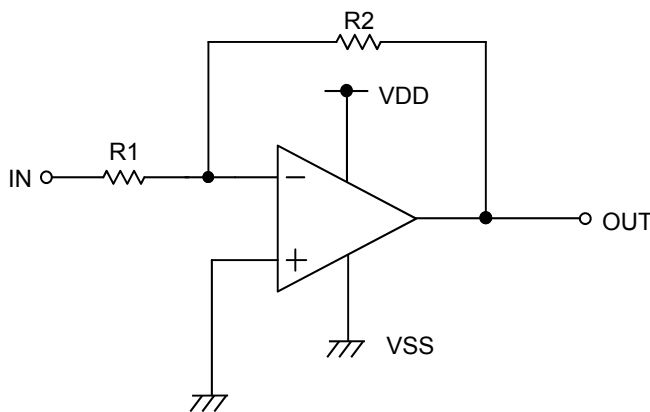


Figure 49. Inverting amplifier circuit

For inverting amplifier, input voltage (IN) is amplified by a voltage gain and depends on the ratio of R1 and R2. The out-of-phase output voltage is shown in the next expression

$$OUT=-\left(\frac{R2}{R1}\right) \cdot IN$$

This circuit has input impedance equal to R1.

○Non-inverting amplifier

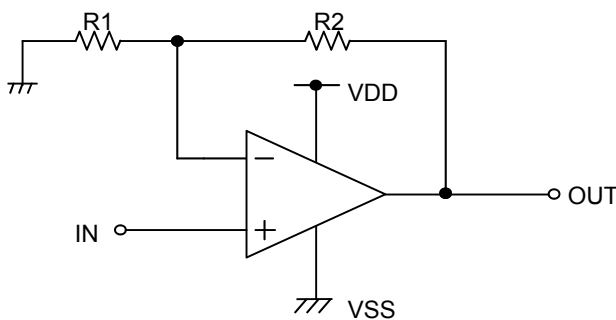


Figure 50. Non-inverting amplifier circuit

For non-inverting amplifier, input voltage (IN) is amplified by a voltage gain, which depends on the ratio of R1 and R2. The output voltage (OUT) is in-phase with the input voltage (IN) and is shown in the next expression.

$$OUT=\left(1 + \frac{R2}{R1}\right) \cdot IN$$

Effectively, this circuit has high input impedance since its input side is the same as that of the operational amplifier.

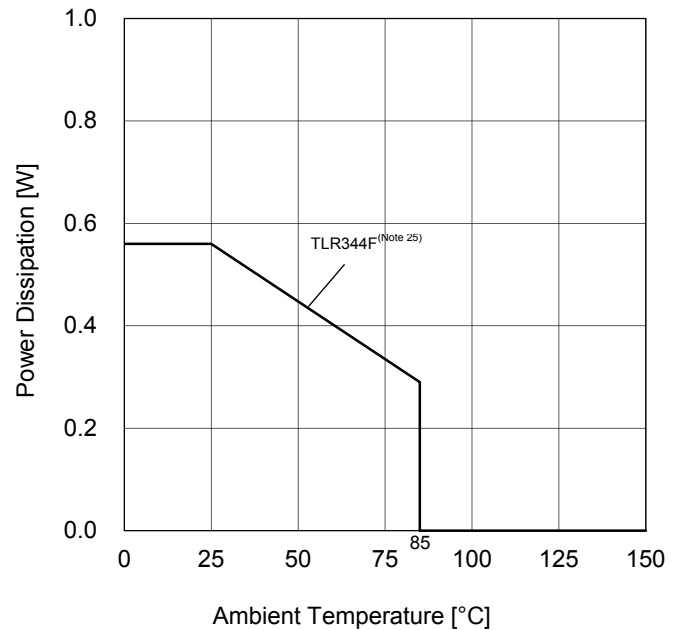
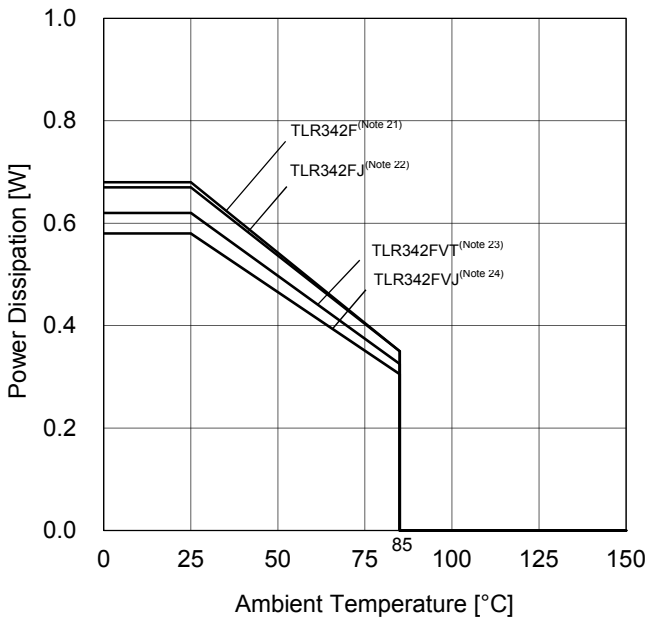
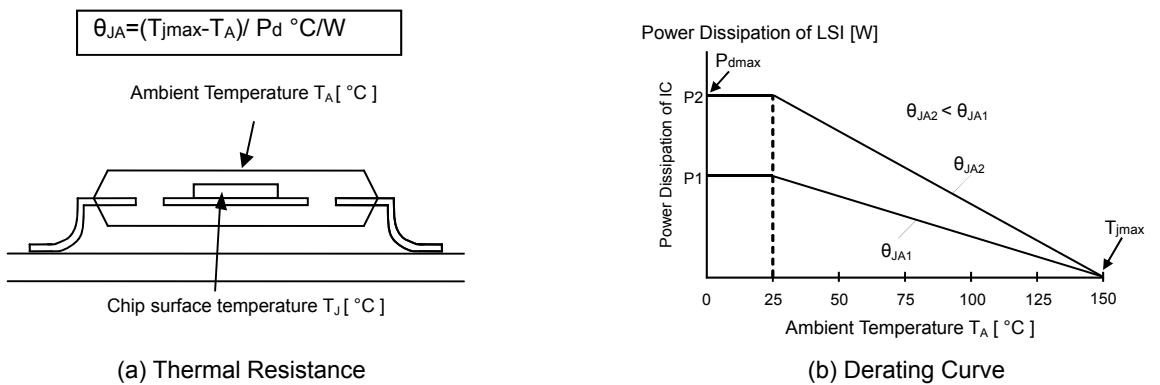


**Power Dissipation**

Power dissipation (total loss) indicates the power that the IC can consume at  $T_A=25^{\circ}\text{C}$  (normal temperature). As the IC consumes power, it heats up, causing its temperature to be higher than the ambient temperature. The allowable temperature that the IC can accept is limited. This depends on the circuit configuration, manufacturing process, and consumable power. Power dissipation is determined by the allowable temperature within the IC (maximum junction temperature) and the thermal resistance of the package used (heat dissipation capability). Maximum junction temperature is typically equal to the maximum storage temperature. The heat generated through the consumption of power by the IC radiates from the mold resin or lead frame of the package. Thermal resistance, represented by the symbol  $\theta_{JA}^{\circ}\text{C/W}$ , indicates this heat dissipation capability. Similarly, the temperature of an IC inside its package can be estimated by thermal resistance. Figure 51(a) shows the model of the thermal resistance of a package. The equation below shows how to compute for the Thermal resistance ( $\theta_{JA}$ ), given the ambient temperature ( $T_A$ ), maximum junction temperature ( $T_{Jmax}$ ), and power dissipation ( $P_d$ ).

$$\theta_{JA} = (T_{Jmax} - T_A) / P_d \quad ^{\circ}\text{C/W}$$

The derating curve in Figure 51(b) indicates the power that the IC can consume with reference to ambient temperature. Power consumption of the IC begins to attenuate at certain temperatures. This gradient is determined by Thermal resistance ( $\theta_{JA}$ ), which depends on the chip size, power consumption, package, ambient temperature, package condition, wind velocity, etc. This may also vary even when the same of package is used. Thermal reduction curve indicates a reference value measured at a specified condition. Figure 51 (c) and (d), shows an example of the derating curve for TLR342xxx and TLR344F, respectively.



(Note 21)	(Note 22)	(Note 23)	(Note 24)	(Note 25)	Unit
5.4	5.0	4.7	4.5	5.5	mW/°C

When using the unit above  $T_A = 25^{\circ}\text{C}$ , subtract the value above per Celsius degree.  
 Power dissipation is the value when FR4 glass epoxy board 70mm x 70mm x 1.6mm (copper foil area less than 3%) is mounted.

Figure 51. Thermal Resistance and Derating Curve

## Operational Notes

### 1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

### 2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

### 3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

### 4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

### 5. Thermal Consideration

Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. The absolute maximum rating of the  $P_d$  stated in this specification is when the IC is mounted on a 70mm x 70mm x 1.6mm glass epoxy board. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the  $P_d$  rating.

### 6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

### 7. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

### 8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

### 9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

### 10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

### 11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

## Operational Notes – continued

**12. Regarding the Input Pin of the IC**

In the construction of this IC, P-N junctions are inevitably formed creating parasitic diodes or transistors. The operation of these parasitic elements can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions which cause these parasitic elements to operate, such as applying a voltage to an input pin lower than the ground voltage should be avoided. Furthermore, do not apply a voltage to the input pins when no power supply voltage is applied to the IC. Even if the power supply voltage is applied, make sure that the input pins have voltages within the values specified in the electrical characteristics of this IC.

**13. Unused Circuits**

When there are unused op-amps, it is recommended that they are connected as in Figure 52, setting the non-inverting input pin to a potential within the in-phase input voltage range ( $V_{ICM}$ ).

**14. Input Voltage**

Applying  $V_{DD}+0.3V$  to the input pin is possible without causing deterioration of the electrical characteristics or destruction. However, this does not ensure normal circuit operation. Please note that the circuit operates normally only when the input voltage is within the common mode input voltage range of the electric characteristics.

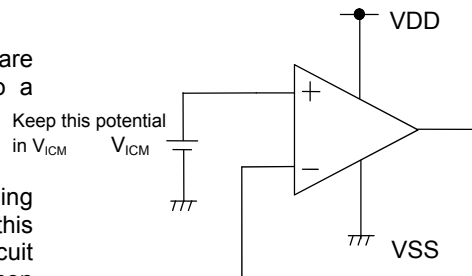


Figure 52. Example of Application Circuit for Unused Op-amp

**15. Power Supply(single/dual)**

The operational amplifiers operate when the voltage supplied is between  $V_{DD}$  and  $V_{SS}$ . Therefore, the single supply operational amplifiers can be used as dual supply operational amplifiers as well.

**16. Output Capacitor**

If a large capacitor is connected between the output pin and  $V_{SS}$  pin, current from the charged capacitor will flow into the output pin and may destroy the IC when the  $V_{DD}$  pin is shorted to ground or pulled down to 0V. Use a capacitor smaller than 0.1  $\mu F$  between output pin and  $V_{SS}$  pin.

**17. Oscillation by Output Capacitor**

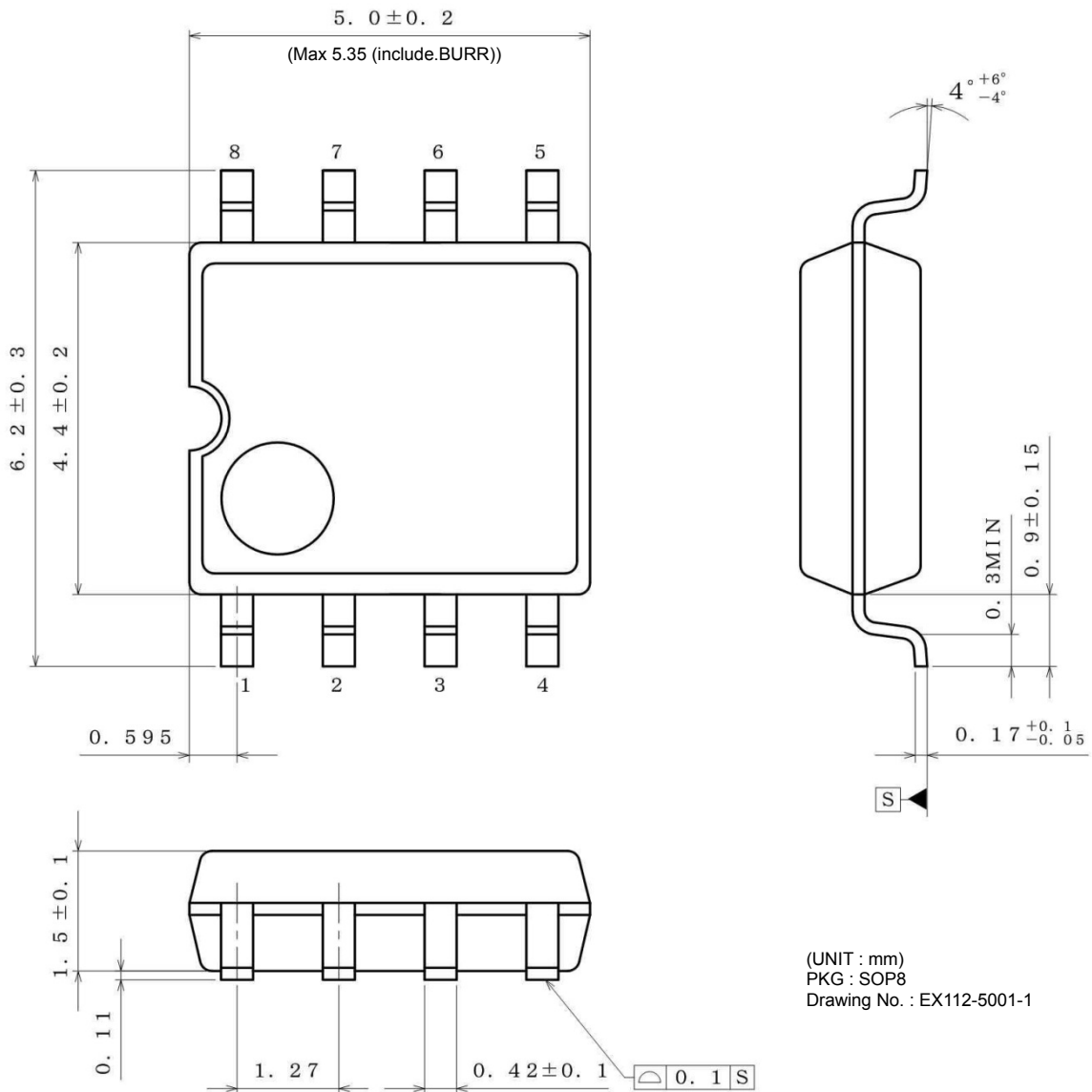
Please pay attention to the oscillation by output capacitor and in designing an application of negative feedback loop circuit with these ICs.

**18. Latch Up**

Be careful of input voltage that exceed the  $V_{DD}$  and  $V_{SS}$ . When CMOS device have sometimes occur latch up and protect the IC from abnormally noise.

Physical Dimension Tape and Reel Information

Package Name	SOP8
--------------	------



(UNIT : mm)  
 PKG : SOP8  
 Drawing No. : EX112-5001-1

<Tape and Reel information>

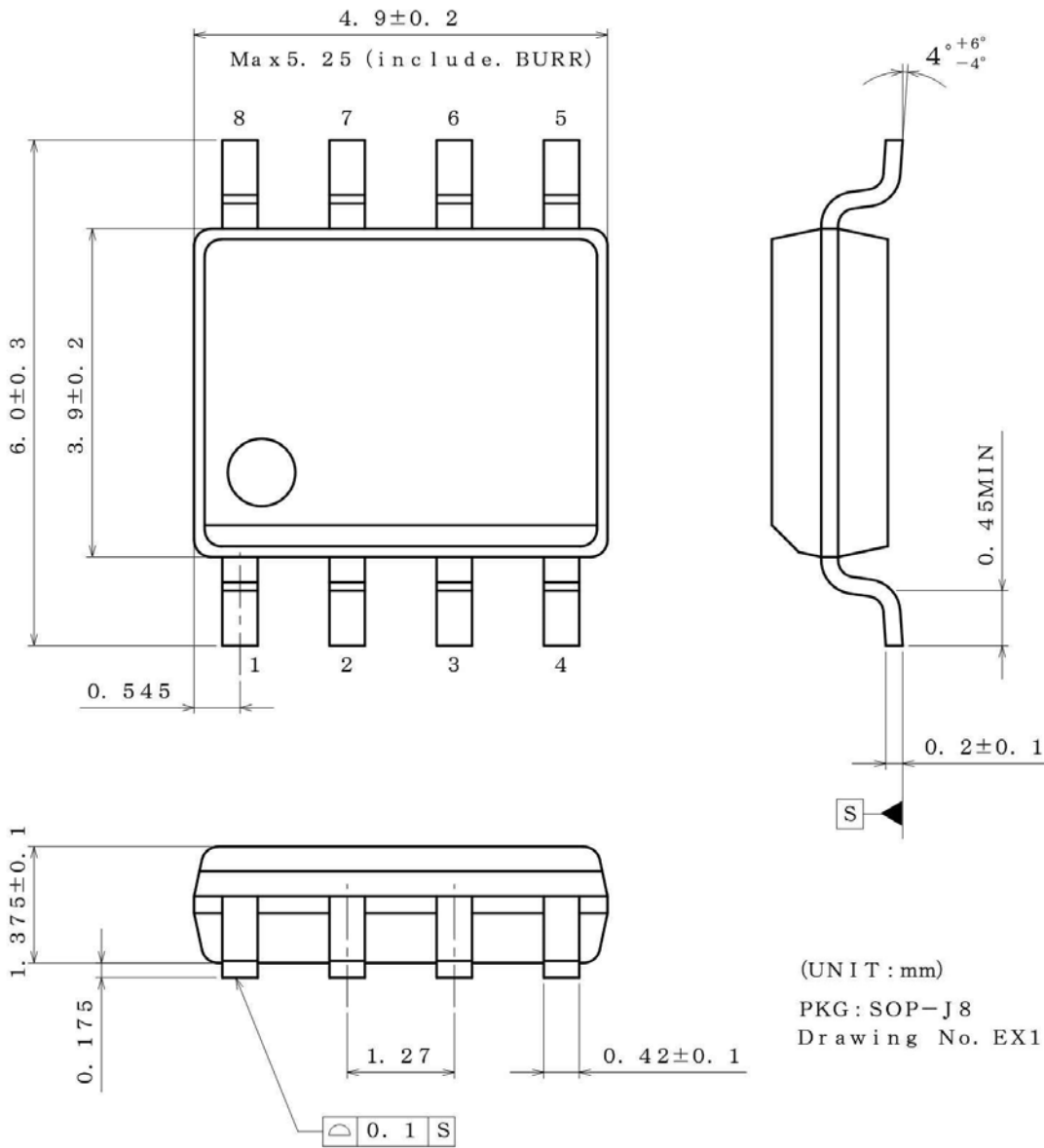
Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	E2 ( The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand )

Reel      1pin      Direction of feed

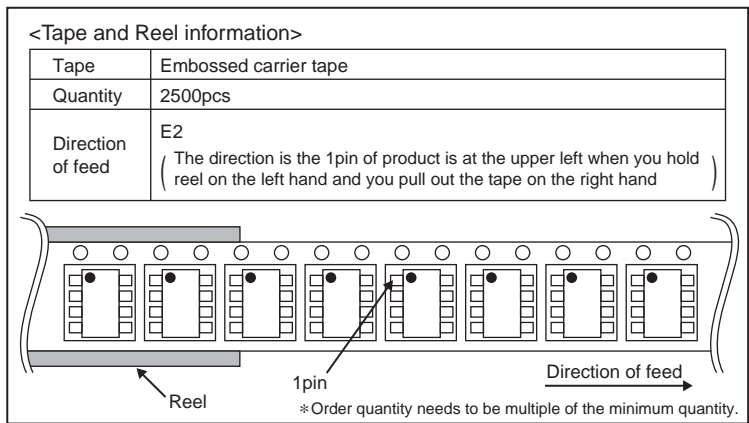
\* Order quantity needs to be multiple of the minimum quantity.

Physical Dimension Tape and Reel Information – continued

Package Name	SOP-J8
--------------	--------

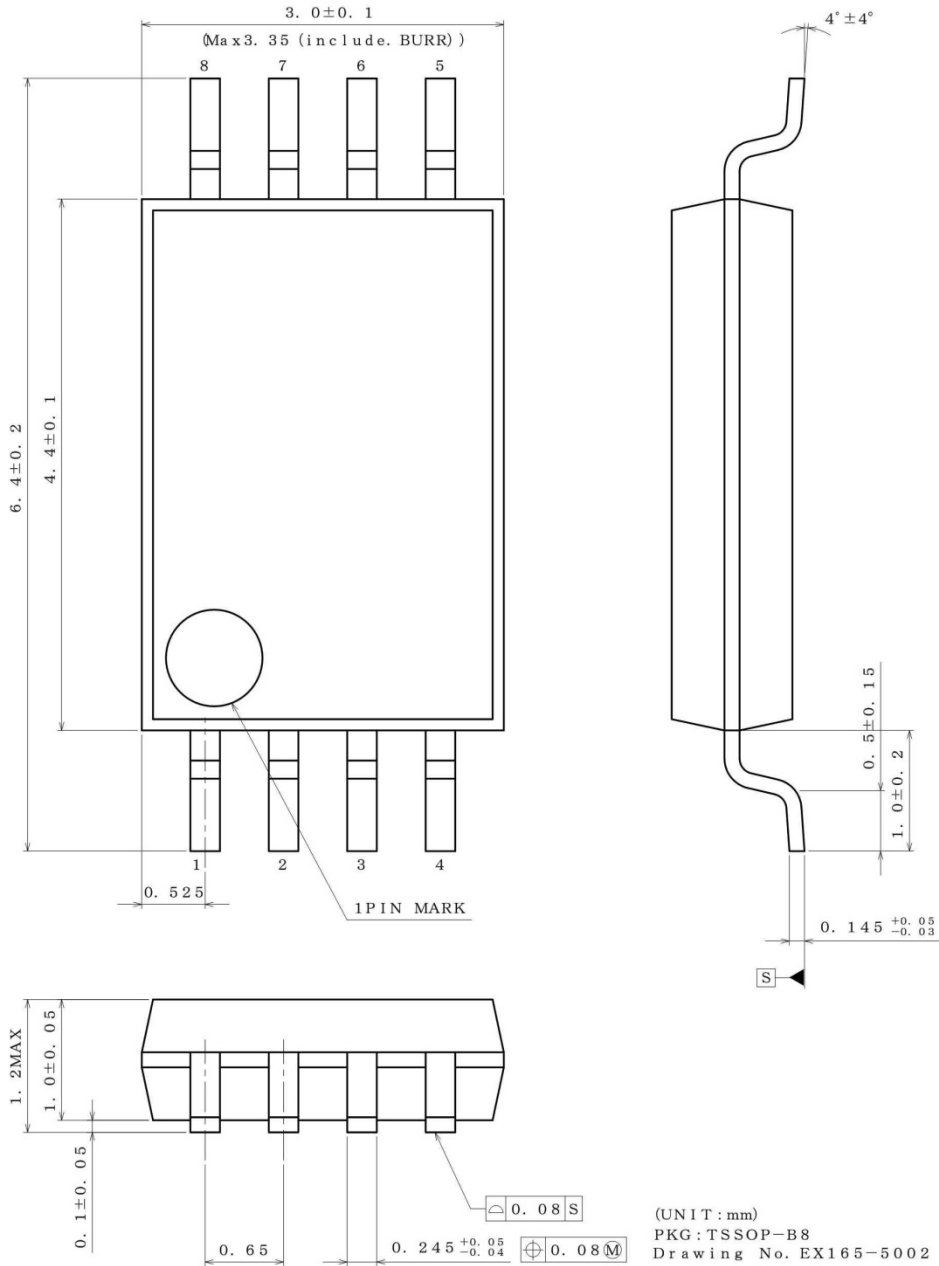


(UNIT : mm)  
 PKG : SOP-J8  
 Drawing No. EX111-5002



Physical Dimension Tape and Reel Information – continued

Package Name	TSSOP-B8
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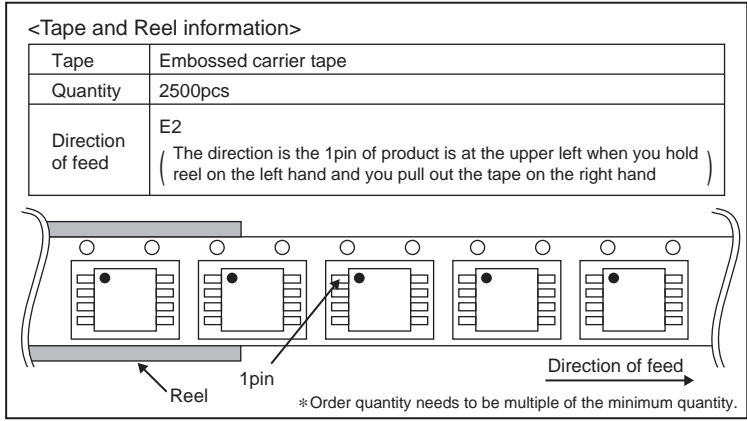
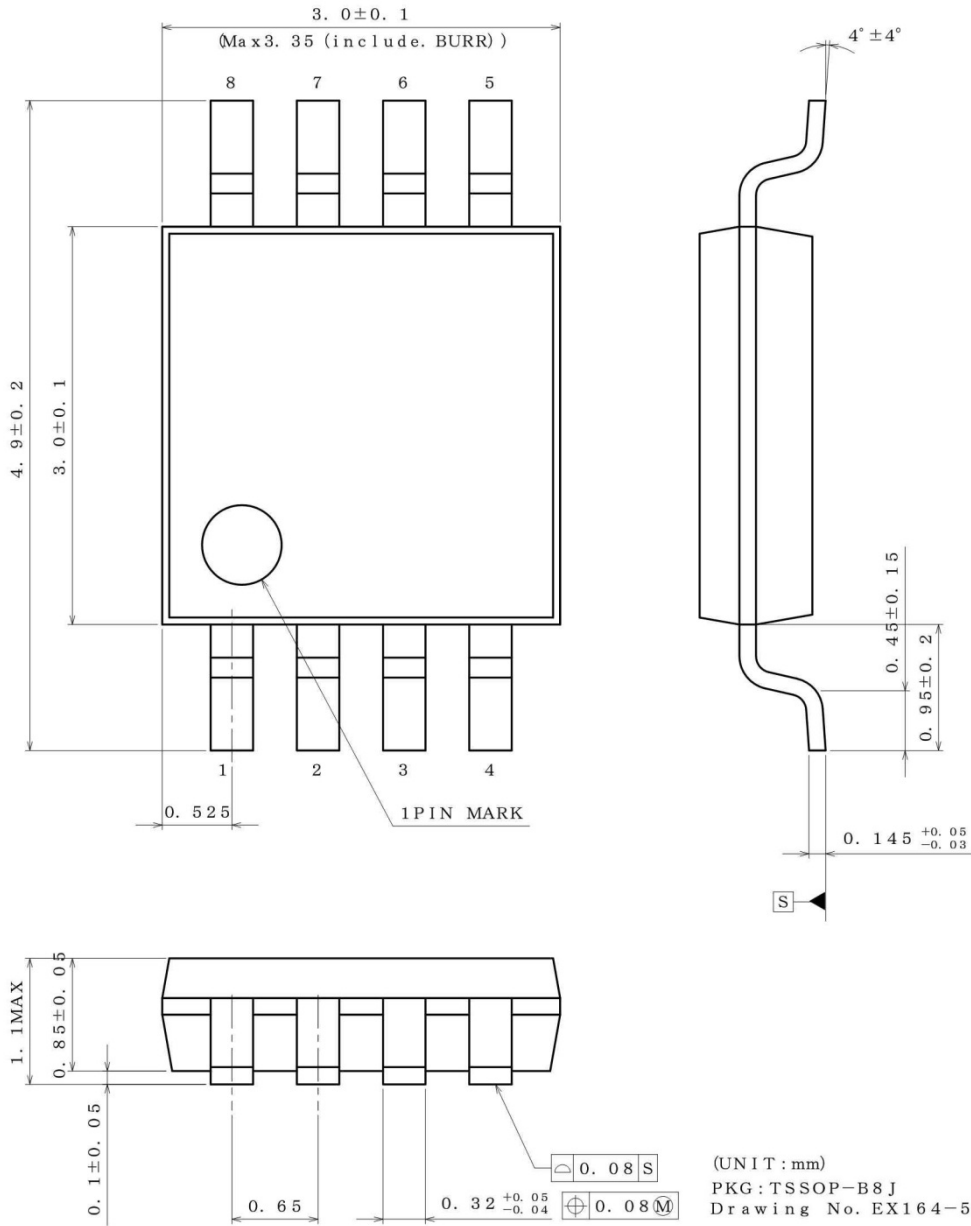
<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	3000pcs
Direction of feed	E2 ( The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand )

\*Order quantity needs to be multiple of the minimum quantity.

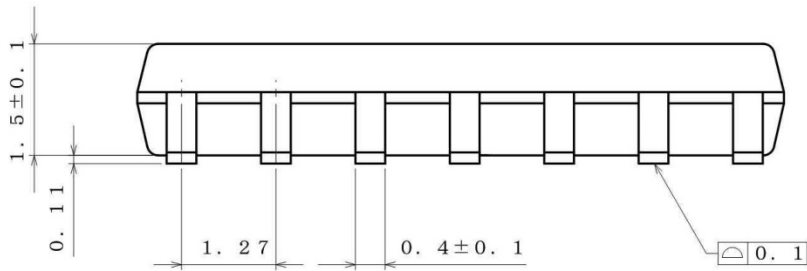
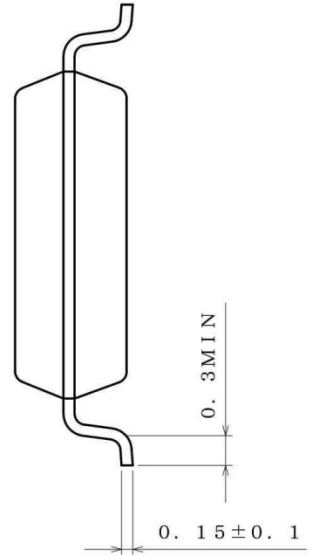
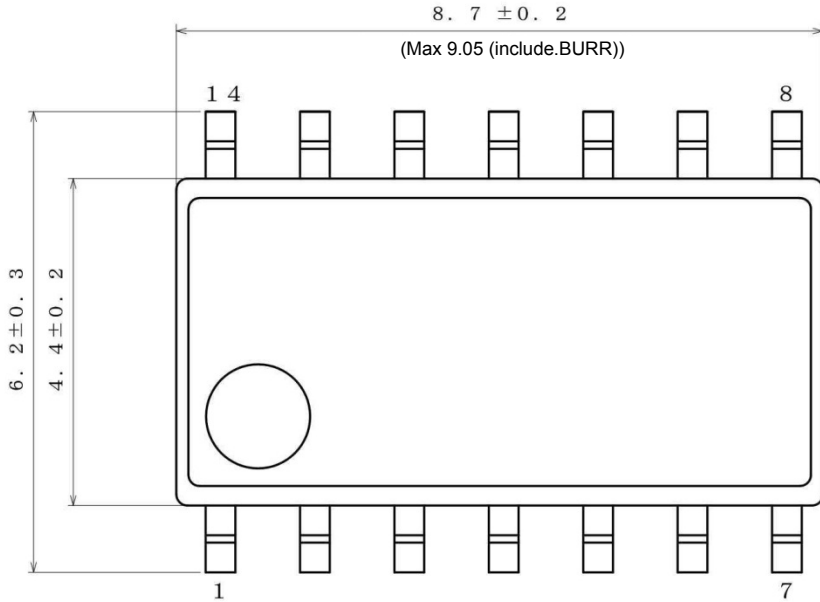
Physical Dimension, Tape and Reel Information – continued

Package Name	TSSOP-B8J
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Physical Dimension Tape and Reel Information – continued

Package Name	SOP14
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(UNIT : mm)  
PKG : SOP14  
Drawing No. : EX113-5001

<Tape and Reel information>	
Tape	Embossed carrier tape
Quantity	2500pcs
Direction of feed	E2 ( The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand )

Reel

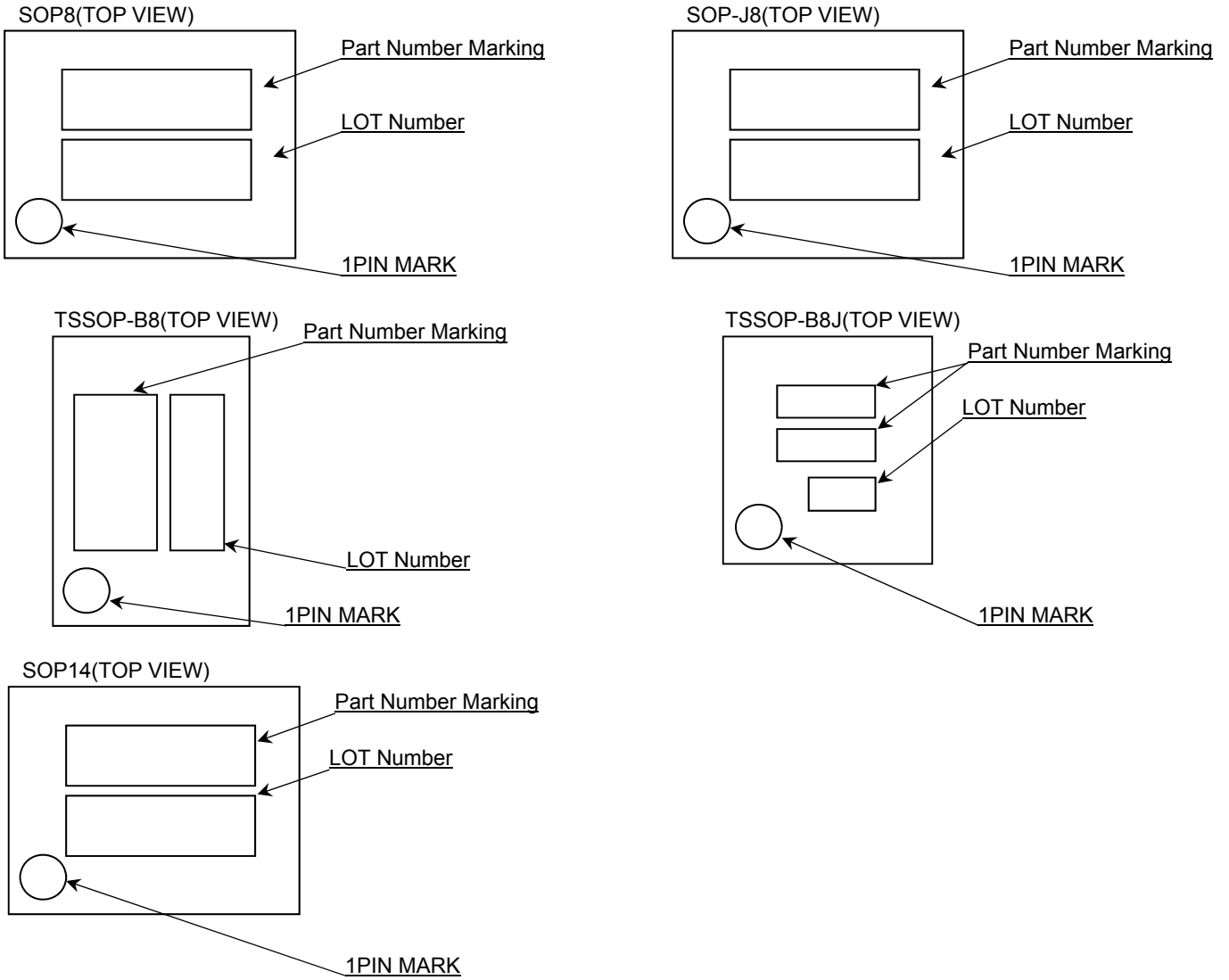
1pin

Direction of feed

\*Order quantity needs to be multiple of the minimum quantity.



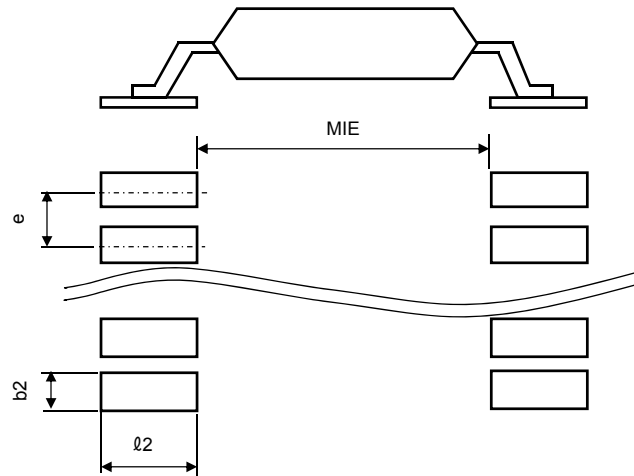
**Marking Diagram**



Product Name		Package Type	Marking
TLR342	F	SOP8	T342
	FJ	SOP-J8	T342
	FVT	TSSOP-B8	T342
	FVJ	TSSOP-B8J	T342
TLR344	F	SOP14	TLR344F

Land Pattern Data

SOP8, SOP-J8, TSSOP-B8, TSSOP-B8J, SOP14



All dimensions in mm

Package	Land pitch e	Land space MIE	Land length $\geq l2$	Land width b2
SOP8 SOP14	1.27	4.60	1.10	0.76
SOP-J8	1.27	3.9	1.35	0.76
TSSOP-B8	0.65	4.60	1.20	0.35
TSSOP-B8J	0.65	3.20	1.15	0.35

**Revision History**

Date	Revision	Changes
29.Aug.2014	001	New Release
19.Mar.2015	002	Add TLR342FJ-G, TLR342FVT-G, TLR342FVJ-G, TLR344F-G

# Notice

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- Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment <sup>(Note 1)</sup>, transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

- ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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  - Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - Sealing or coating our Products with resin or other coating materials
  - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

## Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

**Precautions Regarding Application Examples and External Circuits**

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

**Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

**Precaution for Storage / Transportation**

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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**Precaution for Disposition**

When disposing Products please dispose them properly using an authorized industry waste company.

**Precaution for Foreign Exchange and Foreign Trade act**

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