

SMCM090805 Series Common Mode Line Filter





◆特征:

- 适用大电流的片式共模滤波器
- 高阻抗特性, 能高效抑制共模噪音效果
- 体积小、性能高、成本低
- 符合 RoHS,无卤和REACH

◆用途:

- 数据线和信号线的电流补偿扼流圈
- 各种电子产品、多媒体设备,电源线的共 模噪音抑制

◆环境:

工作温度: -40℃ 至+125℃
 (包括线圈自身温升)

◆试验设备:

- 电感值:HP4274A 或同等仪器
- 电流:HP4284+42841A

SMCM090805

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- 阻抗:E4991+ HP16092 测试夹具
- 直流电阻: Chroma 16502 或同等仪器

◆产品型号:

000005

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类型 Type 贴片共模滤波器 SMCM SMT Common Mode Line Filter

包装 Packing			
В	散装Bulk Package		
Т	编带Tape & Reel		

Features:

- Chip common mode filter for large current applications
- Excellent impedance characteristics, making it great for suppressing common mode noise
- Compact size high performance and low cost
- RoHS, Halogen Free and REACH Compliance

Applications:

- Current compensated choke for data and signal lines
- Common mode noise suppression of various electronic products, multimedia equipment, power cords

Environmental Data:

Operating Temperature: -40°C to +125°C
 (Including coils self-temperature rise)

Test Equipment:

- L:HP4284A or HP4285A LCR meter or equivalent
- Isat & Irms: HP4284+42841A
- Impedance: E4991 analyzer with HP16092 test fixture
- DCR:Chroma 16502 or equivalent

Product Identification:

11/1/20.	
外形尺寸(L;	·W×H) (mm)
External Dimer	nsions (L×W×H)
(m	nm)
090805	9.0×7.8×5.3

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Impedance
700 Ω

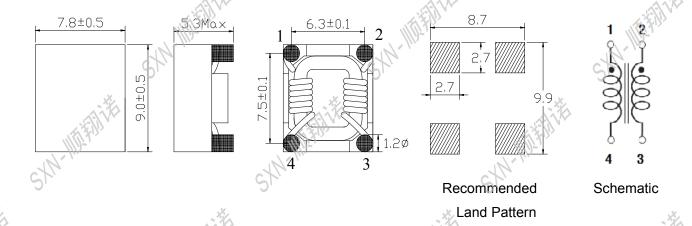
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◆外观尺寸:

Shape and Dimensions (dimensions are in mm):

SMCM090605 Series



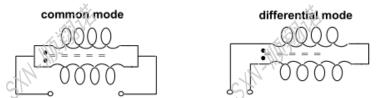
◆规格特性:

Specifications:

• SMCM090605 Series Electrical Characteristics (Electrical specifications at 25℃)

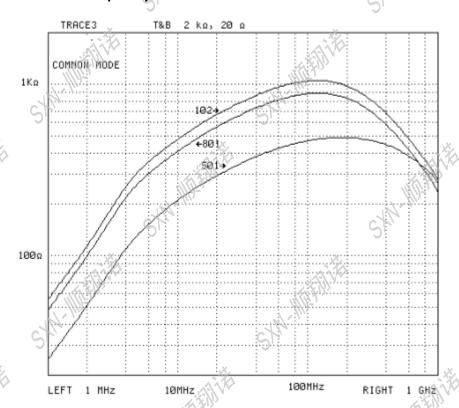
Impedance			DCR	Rated	V _{DC}
Part No	Commo	n Mode		Current	Rated
Fattino	(Ω)	Test	(m Ω)	(A)	(V)
J. M.	Max	Freq	Max	Max	TYP
SMCM090805-501	500±25%	100MHz	10	5.0	80
SMCM090805-801	800±25%	100MHz	20	3.5	80
SMCM090805-102	1000±25%	100MHz	50	2.5	80

- Rated Current: the actual value of DC current when the temperature rise is ΔT 40°C (Ta=25°C)
- Hi-Pot Test: 500VAC ,60Hz,3mA, 3Sec
- Circuit: Test Mode:





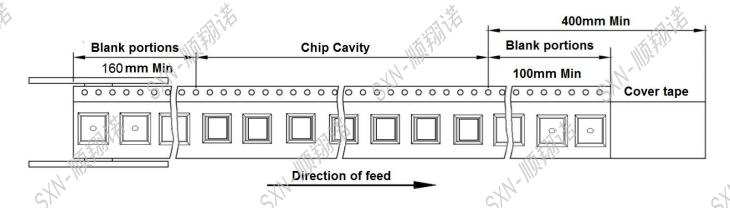
• Typical Impedance versus Frequency



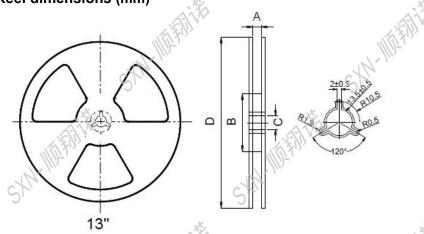
◆产品包装:

Packaging:

• Tape and Reel Specifications: (Dimensions are in mm)

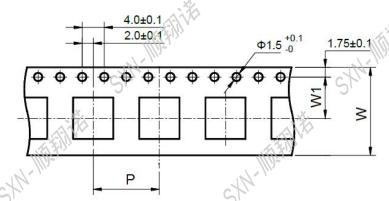


Reel dimensions (mm)

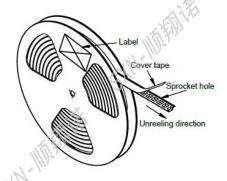




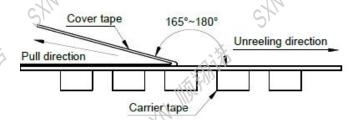
●Tape Dimension (mm)



• Cover tape peel off condition

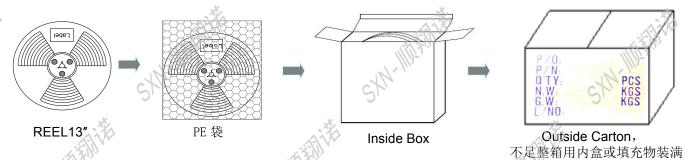


- a) Cover tape peel force shall be 10 to 120g
- b) Noodle strip peeling angle165° to 180°



Packing quantity

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Part No.	Тар	e Dimen	sion	CKM	Reel Din	nensions	3	REEL	Inside	Outside
Part No.	W	Р	W1	A	В	С	D	(PCS)	Box(PCS)	Carton(PCS)
SMCM090805	16.0	12.0	7.5	16.4	60	13	330	1000	3000	12,000

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◆可靠性测试:

Reliability Testing:

→ り 靠 性 测 试:	<u>GP</u>	Reliability lesting:			
Items	Requirements	Test Methods and Remarks			
	1. Pulling test:	Solder the inductor to the testing jig using leadfree			
	Define: A: sectional area of terminal	solder. Then apply a force in the			
Terminal Strength	A ≤ 8mm2 force ≥ 5N time:30sec	Keep time: 10±1s Speed: 1.0mm/s.			
Reference docu	8mm2 <a 10n="" 10sec<="" 20mm2="" force="" td="" time.="" ≥="" ≦=""><td>Force</td>	Force			
ments: GB/T	20mm2 <a 10sec<="" force="" td="" time:="" ≥20n=""><td>1103</td>	1103			
2423.60-2008	2.Solder paste thickness:0.12mm				
端子強度(SMT)	3.Meet the above requirements without any				
19-1113	loose terminal	al-fibe			
-SX		SAL			
	1.Terminal diameter(d) mm 0.35 <d≤< td=""><td>Pull Force:the force shall be applied gradually to</td></d≤<>	Pull Force:the force shall be applied gradually to			
	0.50Applied force:5N Duration:	the terminal and thenmaintained for 10 seconds.			
erminal Strength	10sec2.Terminal diameter(d) mm0.50 < d ≤				
Reference docu	0.80Applied force:10N Duration:				
ments: GB/T	10sec3.Terminal diameter(d) mm0.80 < d <	1			
2423.60-2008	1.25Applied force:20N Duration:	Pulling test			
端子強度(DIP)	10sec4.Terminal diameter(d) mmD>	×2.			
1 (L) (D) ()	1.25Applied force:40N Duration.	:311 ^{1/2}			
	10sec5.Meet the above requirements				
M. III	without any loose terminal.	M. M.			
5	1.No visible mechanical damage.	1.Solder the inductor to the test jig (glass epoxy			
	-%	board			
	4:01/1/2	2.shown in Using a leadfree solder. Then apply a			
		force in the direction shown			
Resistance to Flexure	14.11.	3.Flexure: 2mm.			
JIS C 5321:1997	Br. Sr.	4.Pressurizing Speed: 0.5mm/sec.			
 抗弯曲性试验 ※	X.	5.Keep time: 30 sec.			
		20			
		R230			
19-111/2	W-Mag				
SKI	SK	Flexure			
		45[1.772] 45[1.772] 43			
-	AND				
Dropping	1.No case deformation or change	1.Drop the packaged products from 1m high in 1			
Reference documents:	inappearance.	angle, 3 ridges and 6surfaces, twice in each			
GB/T 2423.7-2018	2.No short and no open.	direction.			
 落下試驗		J.			
Solderability	1.No visible mechanical damage.	1.Solder temperture:240±2℃			
, ,	2.Wetting shall exceed 75% coverage for	2.Duration: 3 sec.			
Reference documents:	3. Terminals must have 95% minimum solder	3. Solder: Sn/3.0Ag/0.5Cu.			
COVERAGE		4.Flux: 25% Resin and 75% ethanol in weight			
可焊性试验					
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Items	Requirements	Test Methods and Remarks		
	1.No visible mechanical damage.	1.Solder the inductor to the testing jig (glass epoxy		
	2. Inductance change: Within ±10%.	boardshown in) using leadfree solder.		
	3 Q factor change: Within ±20%.	2.The inductor shall be subjected to a simple		
	Cu pad Solder mask	harmonic motion having total amplitude of 1.5mm,		
		the frequency being varieduniformly between the		
		approximate limits of 10 and 55 Hz.		
Vibration		3.The frequency range from 10 to 55 Hz and		
Reference documents:		return to 10 Hz shallbe traversed in approximately		
GB/T 2423.10-2019	Glass Epoxy Board	1 minute. This motion shall be applied for a period		
振動試验	: SIVIE	of 2 hours in each 3mutually perpendicular		
		directions(total of 6 hours).		
	14-111	Freq		
	5r'	55Hz \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
-%	-%			
		10Hz V V V Time		
Sth	1.No visible mechanical damage.	1.Start at (85~125℃) for T time, rush to		
	.v	(-55~40℃) for T time as one cycle, go through100		
	Within $\leq 30\%$)	cycles.		
Thermal Shock	3.Q factor change: Within ±20%.	2.Transforming interval: Max. 20 sec.		
Reference documents:	14-111	3.Tested cycle: 100 cycles.		
GB/T 2423.22-2012	5r	4. The chip shall be stabilized at normal condition		
Method Na	-%	for 1~2 hours 30 min 30 min.		
冷热冲击试验		125 C/85 C		
		Ambient 30 min		
ct/l	STA	-55°C/-40°C		
,	J.	20sec. (max.)		
	1.No visible mechanical damage.	1.Temperature:M(-55~-40±2℃)		
	Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours		
	Within ≦ 30%)	3.The chip shall be stabilized at normal condition for		
Low temperature Storage	3.Q factor change: Within ±20%.	1~2 hoursbefore measuring.		
Reference documents:	- X	-%		
GB/T 2423.1-2008		Room Temp		
Method Ab		0 96H / Test 97H 98H Time		
低温储存试验	ct.	M.C CAN		
, and the second	<i>J</i>	Temp Low temperature		
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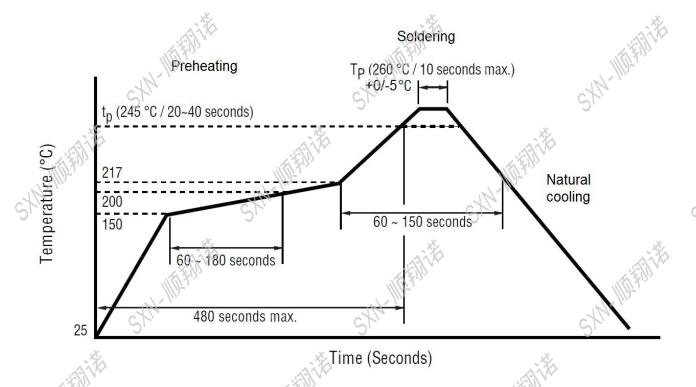


_	GT'	C N
Items	Requirements	Test Methods and Remarks
	1.No visible mechanical damage.	1.Temperature:N(125~85±2℃).
High temperature	2. Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours
Storage	Within ≦30%)	3.The chip shall be stabilized at normal condition
	3.Q factor change: Within ±20%.	for 1~2 hoursbefore measuring.
GB/T 2423.2-2008	**	Temp High temperature
Method Bb		N'C
高温储存试验	11/2/2	Room
SH	Str	0 Test 96H 97H 98H Time
	1.No visible mechanical damage.	1.Temperature: 60±2℃
Ì	2. Inductance change: Within ±10%.(Mn-Zn:	2.Humidity: 90% to 95% RH.
Damp Heat	Within ≦30%)	3.Duration: 96±2 hours.
(Steady States)	3.Q factor change: Within ±20%.	4.The chip shall be stabilized at normal condition
Reference documents:	Dr. St.	for 1~2 hoursbefore measuring.
GB/T 2423.3-2016	, N.	Temp & Humidity 60°C Temp & Humidity 60°C
恒定湿热试验	14 JVC	93%RH High temperature High humidity
	11/1/2/2	Conditions
cth	c the	0 96H 97H 98H Time
Heat endurance of	1.No significant defects in appearance.	1.Refer to the above reflow curve and go through
Reflow soldering	2. △ L/L ≦ 10% (Mn-Zn: △ L/L ≦ 30%)	the reflow for twice.
Reference documents:	3. ∆ Q/Q≦30% (SMD series only)	2.The peak temperature : 260+0/-5℃
GJB 360B-2009	4. △ DCR/DCR ≦ 10%	
回流焊耐热性试验	14 24	£20
	No case deformation or change in	To dip parts into IPA solvent for 5±0.5Min,then
Resistance to solvent	appearance or obliteration of marking	drying them at room temp for 5Min,at last ,to
test		brushing making 10 times.
Reference documents:	W - Mar	10 m
IEC 68-2-45:1993 耐溶剂性试验	SK	SK
	JX.	
Overload test	1.During the test no smoke, no peculiar,	
	smell, no fire	
JIS C5311-6.13	2.The characteristic is normal after test	Apply twice as rated current for 5 minutes.
过负荷试验	br Sr	SN
voltage resistance test	1.During the test no breakdown	
\ \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	The characteristic is normal after test	
MIL-STD-202G Method	17-1113-	1. For parts with two coils
301	SK	2. DC1000V, Current: 1mA, Time: 1Min.
绝缘耐压测试	JC.	Refer to catalogue of specific products
A		



◆推荐回流焊温度曲线

Recommended reflow soldering curve:



The recommended reflow conditions as above graph, is set according to our soldering equipment. DUE to various manufactures may have different reflow soldering equipment, products, process conditions, set methods. And so on, when setting the reflow conditions, Please adjust and confirm according to users' environment/equipment.

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使用注意事项

REMINDERS FOR USING THESE PRODUCTS



● 保存时间为12 个月以内,保存条件(温度5~40°C以下、湿度35 ~ 66%RH 以下),需充分注意。若超过保存时间,端子电极的可焊性将可能老化。

The storage period is within 12 months. Be sure to follow the storage conditions (temperature: $5\sim40^{\circ}$ C, humidity: 35 to 65% RH or less). If the storage period elapses, the soldering of the terminal electrodes may deteriorate.

• 请勿在气体腐蚀环境(盐、酸、碱等)下使用和保存。

Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).

• 手上的油脂会导致可焊性降低,应避免用手直接接触端子。

Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering Always ensure optimum conditions for soldering.

请小心轻拿轻放,避免由于产品的跌落或取出不当而导致的损坏。

Please always handle products carefully to prevent any damage caused bydropping down or inappropriate removing.

• 端子过度弯曲会导致断线,请不要过度弯曲端子。

Don't bend the terminals with excessive stress in case of any wire fracture.

• 不要清洗产品, 如需要清洗时请联系我司。

Don't rinse coils by yourself and please contact SXN if necessary.

• 请勿将本产品靠近磁铁或带有磁力的物体

Don't expose the products to magnets or magnetic fields

- 在实施焊接前,请务必进行预热。预热温度与焊接温度及芯片温度的温度差要在150°C 以内。
 Before soldering, be sure to preheat components. The preheating temperature should be set so that the temperature difference between the solder temperature and chip temperature does not exceed 150°C.
- 安装后的焊接修正应在规格书规定的条件范围内。若加热过度可能导致短路、性能降低、寿命减少。
 Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
- 装置会因通电而自我发热(温度上升),因此在热设计方面需留有充分余地。
 Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
- 非磁屏蔽型在基板设计时需注意配置线圈,受到电磁干扰可能会导致误动作。
 Carefully lay out the coil for the circuit board design of the non-magnetic shield type. A malfunction may occur due to magnetic interference.
- 当本公司产品使用在一般电子设备以外的场合,如:车载,医疗设备,军用,航空航天等,请务必联繫本公司营业部门, 如超出本公司产品使用条件而引起的机器故障时,本公司概不负责。
 - If SXN product will be applied in area like automotive product, medical equipment, military and aerospace except generalelectronic device, please keep SXN sales informed in advance. SXN shall not be held liable for any malfunction or breakdowncaused by using product in the condition which is inconsistent with that recommended by SXN.