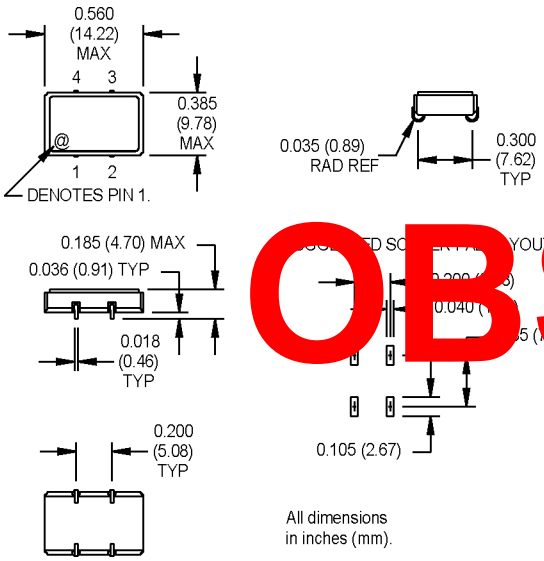
M5S Series 3.3 Volt PECL Oscillators

M5S Series PECL Clock Oscillators with Optional Complementary Outputs. PLL Version



Ordering Information

M5S 1 3 X A J 00.0000 MHz

Product Series

Temperature Range
- **1:** 0°C to +70°C
- **2:** -40°C to +85°C
- **6:** -20°C to +70°C
- **7:** 0°C to +85°C

Stability
- **1:** ±1000 ppm
- **2:** ±500 ppm
- **3:** ±100 ppm
- **4:** ±50 ppm
- **5:** ±35 ppm
- **6:** ±25 ppm
- **8:** ±20 ppm

Output Type
See Table Below

Symmetry/Logic Compatibility
- **A:** 40/60
- **B:** 45/55 (Up to 80.000 MHz)

Package/Lead Configurations
- **J:** J Lead

Frequency (customer specified)

OBSOLETE

Available Stabilities vs. Temperature

T	1	2	3	4	5	6	8
1	A	A	S	A	A	C	C
2	A	A	A	A	A	C	N
6	A	A	A	A	A	C	C
7	A	A	A	A	A	C	N

A = Available
N = Not Available
S = Standard
C = Consult Factory

Pin Connections

PIN	FUNCTION(S) (Model Dependent)
1	N/C or Output #2
2	Case Ground
3	Output #1
4	+Vcc

PECL Output Type

PIN FUNCTIONS:	X	Z
Single Output Pin 3	•	
Dual Output Pin 1 & 3		•
Pin 2 Case Ground	•	•
Pin 4 +Vcc	•	•

Electrical Specifications

Standard Operating Conditions • 0°C to +70°C; Vcc = 3.3 ±5% VDC

PARAMETERS	MIN.	TYP.	MAX.	UNITS
Frequency Range	19.440		100.000	MHz
Output Load [1]				
Symmetry [2]	40/60		60/40	%
Logic "0" Level			Vcc-1.63	V
Logic "1" Level	Vcc-0.98			V
Rise/Fall Time [3]			2.5	nS
Supply Current		50	90	mA

[1] Internally terminated outputs. See load circuit diagram #4 on page 137.
[2] Symmetry is measured at Vcc-1.3 V level.
[3] Rise/Fall times are measured between Vcc-0.98 and Vcc-1.63.
Utilizes PLL clock generation. Not recommended for low jitter applications.
See page 136, Figure "2" for suggested solder profile.

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